



Synchronization Principles

- Background
 - Concurrent access to shared data may result in data inconsistency.
 - Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating tasks.
- The Critical-Section Problem
 - Pure software solution
 - With help from the hardware

5



The Critical-Section Problem

- Problem context:
 - n tasks all competing to use some shared data
 - Each task has a code segment, called critical section, in which the shared data is accessed.
- Find a solution that satisfies the following:
 - 1. Mutual Exclusion. No two tasks simultaneously in the critical section.
 - Progress. No task running outside its critical section may block other tasks.
 - 3. Bounded Waiting/Fairness. Given the set of concurrent tasks, a bound must exist on the number of times that other tasks are allowed to enter their critical sections after a task has made a request to enter its critical section and before that request is granted.

0



Critical Section for Two Tasks

- Only 2 tasks, P₀ and P₁
- General structure of task P_i (other task P_i)

do {
 entry section
 critical section
 exit section
 remainder section

} while (1);

- Tasks may share some common variables to synchronize their actions.
- Assumption: instructions are atomic and no re-ordering of instructions.

7



Algorithm 1

- Shared variables:
 - int turn; initially turn = 0;
 - turn==i ⇒ P_i can enter its critical section
- Task P;
 do {
 while (turn != i) ;
 critical section
 turn = j;
 remainder section
 } while (1);
- Satisfies mutual exclusion, but not progress

8



Algorithm 2

- Shared variables:
 - boolean flag[2]; initially flag[0] = flag[1] = false;
 - flag[i] == true ⇒ P_i ready to enter its critical section
- Task P_i
 do {
 flag[i] = true;
 while (flag[j]);
 critical section
 flag[i] = false;
 remainder section
 } while (1);
- Satisfies mutual exclusion, but may lead to deadlock.

9



Algorithm 3

- Combine shared variables of algorithms 1 and 2.
- Task P;
 do {
 flag[i] = true;
 turn = j;
 while (flag[j] && turn==j);
 critical section
 flag[i] = false;
 remainder section
 } while (1);
- Meets all three requirements; solves the critical-section problem for two tasks. ⇒ called <u>Peterson's algorithm</u>.

10



Synchronization Using Special Instruction: TSL (test-and-set)

```
entry_section:

TSL R1, LOCK | copy lock to R1 and set lock to 1

CMP R1, #0 | was lock zero?

JNE entry_section | if it wasn't zero, lock was set, so loop

RET | return; critical section entered

exit_section:

MOV LOCK, #0 | store 0 into lock

RET | return; out of critical section
```

- Solve the synchronization problem
- Work for multiple (>2) tasks
- Instruction atomicity and ordering only necessary on TSL
- What if you have special instruction SWP (swap the value of a register and a memory word)?

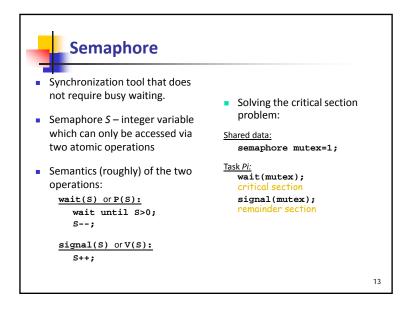
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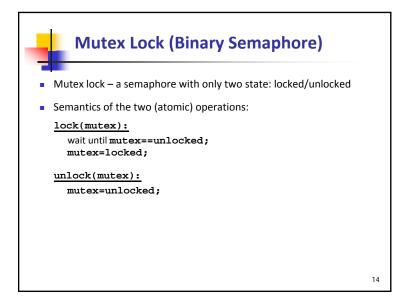


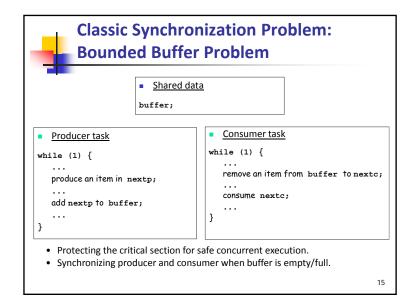
Solving Critical Section Problem with Busy Waiting

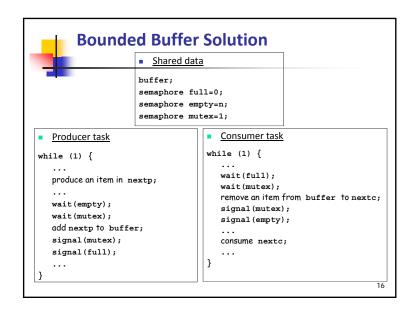
- In all our solutions, a task enters a loop until the entry is granted
 ⇒ busy waiting (or spin waiting).
- Problems with busy waiting:
 - Waste of CPU time
 - If a task is switched out of CPU during critical section
 - other tasks may have to waste a whole CPU quantum
 - may even deadlock with strictly prioritized scheduling
- Solution
 - Avoid busy wait as much as possible (yield the processor instead).

12

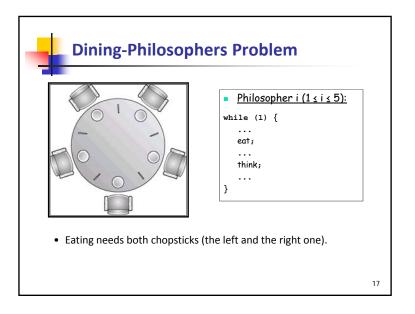


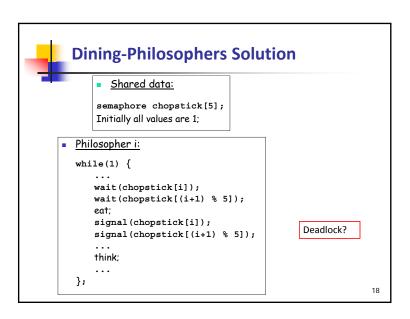


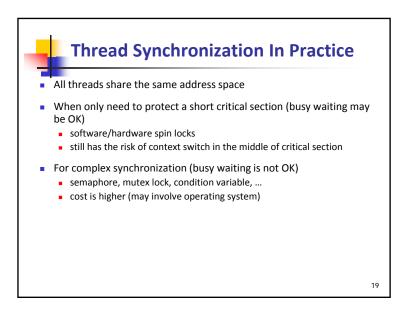


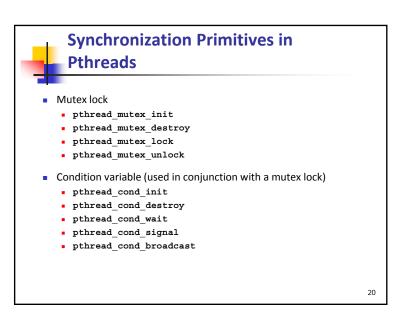


CSC252 - Spring 2015 4









21



Condition Variables

To allow a task to wait, a condition variable must be declared, as condition x, y;

- Condition variable can only be used with the operations wait and signal.
 - The operation

x.wait();

means that the task invoking this operation is suspended until another task invokes

- The x.signal operation resumes exactly one suspended task. If no task is suspended, then the signal operation has no effect.
- Unlike semaphore, there is no counting in condition variables



Process Synchronization

- Processes naturally do not share the same address space
- Process synchronization:
 - semaphore

22