

CSC 252: Computer Organization

Spring 2019: Lecture 4

Instructor: Yuhao Zhu

Department of Computer Science
University of Rochester

Action Items:

- Assignment 1 due Feb. 1, midnight**

Announcement

- Programming Assignment 1 is out
 - Details: <http://cs.rochester.edu/courses/252/spring2019/labs/assignment1.html>
 - Due on **Feb 1, 11:59 PM**
 - You have 3 slip days
- Piazza: <http://piazza.com/rochester/spring2019/csc2522019spring52350>.
- TA review sessions.

20	21	22	23	24	25	26
27	28	29	30	31	Feb 1	2
		Today			Due	

Previously in 252...

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- Least significant bit (byte)
 - Bit (byte) that is least significant to the numerical value of the bit stream — always the rightmost!

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Least significant bit

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Most significant bit

10100011

The binary number 10100011 is shown. The first bit, '1', is circled in red and labeled 'Most significant bit' in red text to its left. The last bit, '1', is circled in green and labeled 'Least significant bit' in green text to its right.

Least significant bit

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Most significant bit

10100011

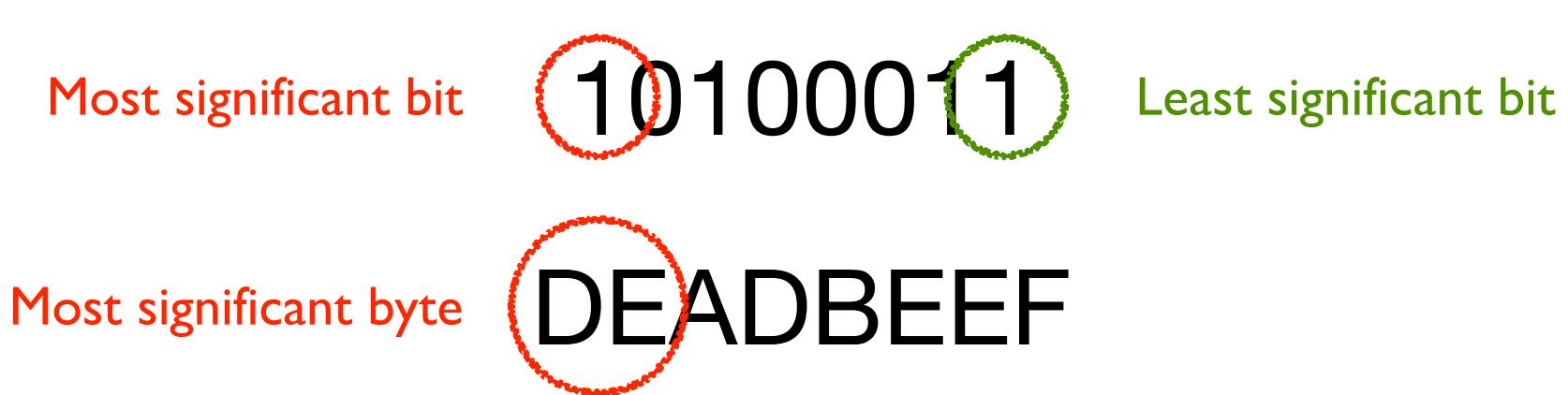
The binary sequence 10100011 is shown. The first bit, '1', is circled in red and labeled 'Most significant bit'. The last bit, '1', is circled in green and labeled 'Least significant bit'.

Least significant bit

DEADBEEF

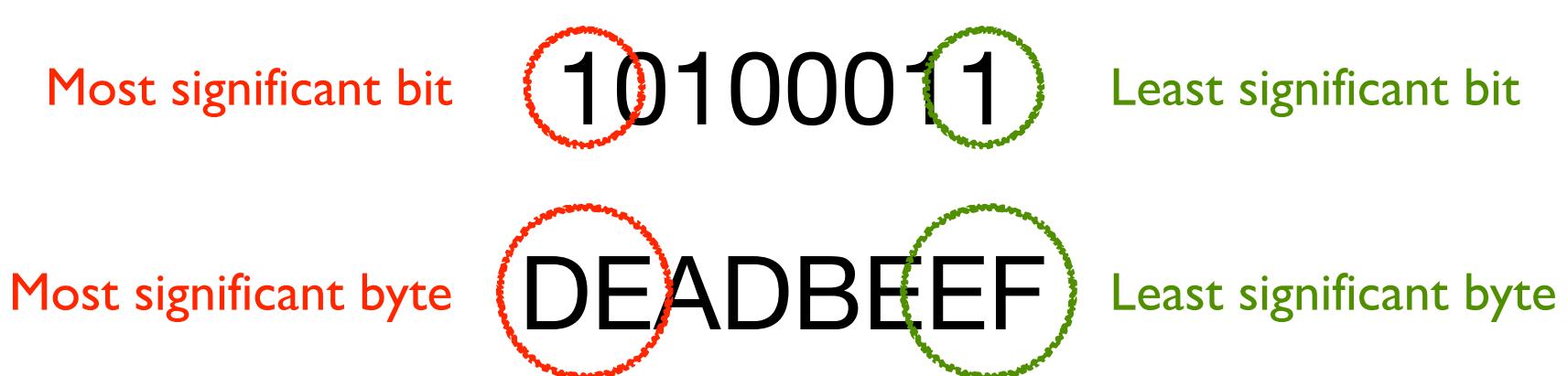
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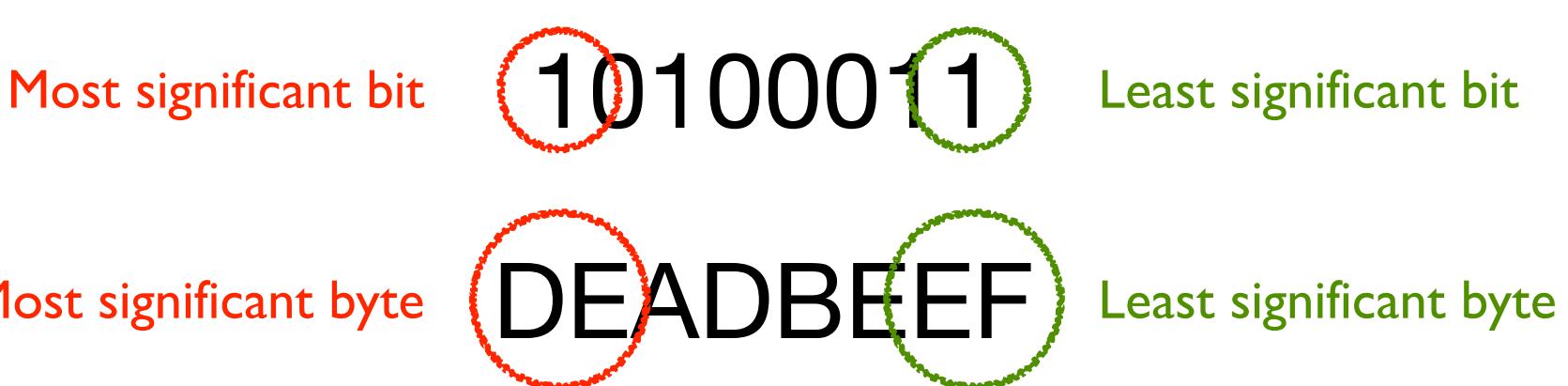
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Previously in 252...

- Least significant bit (byte)
 - Bit (byte) that is least significant to the numerical value of the bit stream — always the rightmost!
- How to represent integers (positive, zero, and negative)
 - Signed vs. Unsigned Integer in C
 - Integer is a special case of fixed-point
 - Fractions can also be represented in fixed-point



Today: Floating Point

- Background: Fractional binary numbers and fixed-point
- Floating point representation
- IEEE 754 standard
- Rounding, addition, multiplication
- Floating point in C
- Summary

Can We Represent Fractions in Binary?

- What does 10.01_2 mean?
 - C.f., Decimal

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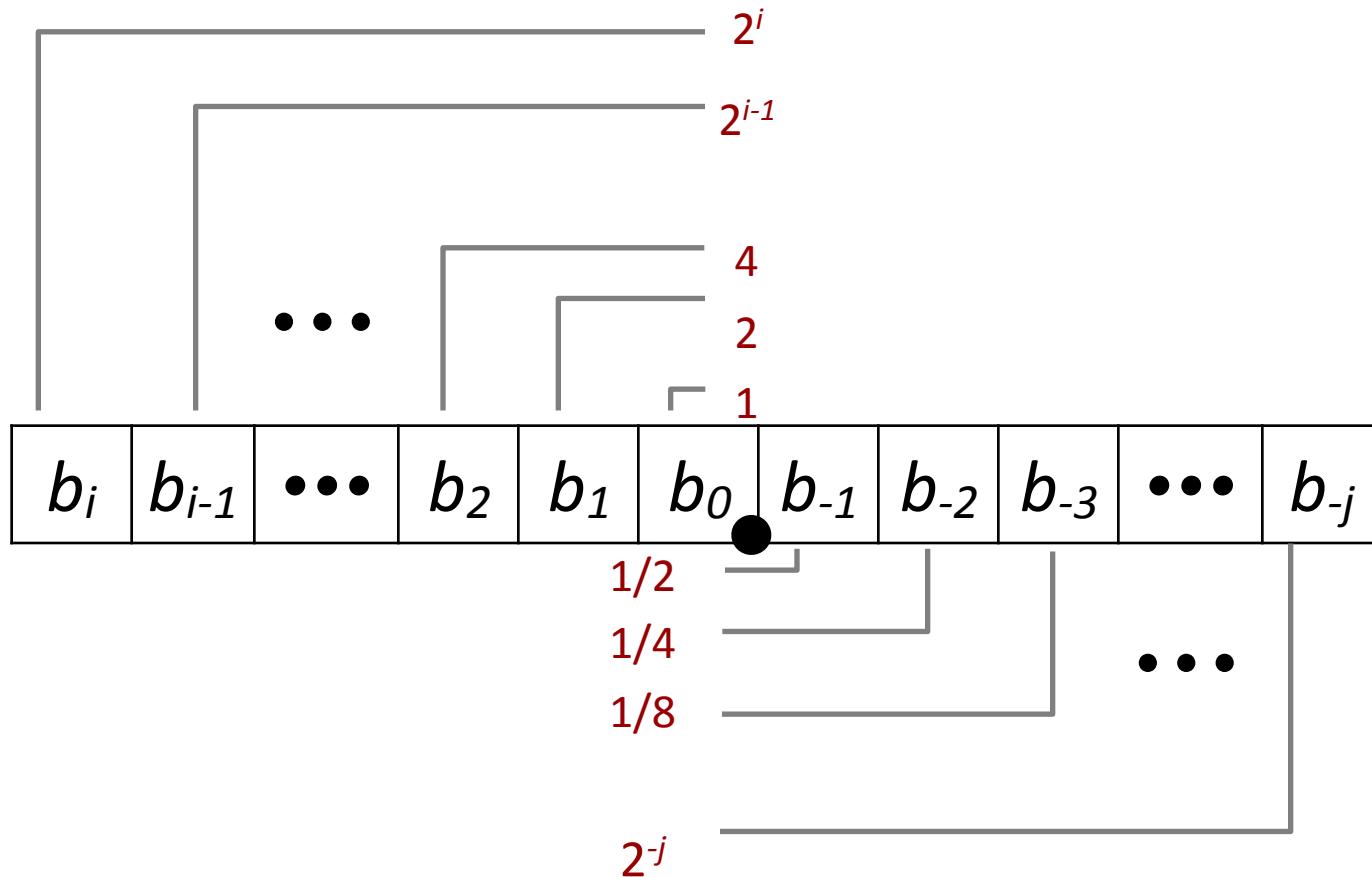

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$$12.45 = 1 * 10^1 + 2 * 10^0 + 4 * 10^{-1} + 5 * 10^{-2}$$

$$\begin{aligned}10.01_2 &= 1 * 2^1 + 0 * 2^0 + 0 * 2^{-1} + 1 * 2^{-2} \\&= 2.25_{10}\end{aligned}$$

Fractional Binary Numbers



Fractional Binary Numbers: Examples

Decimal Value	Binary Representation
5 3/4	101.11
2 7/8	10.111
1 7/16	1.0111

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- We would need to remember:
 - The raw bit stream (5 bits)
 - Where the binary point is (potentially another 3 bits for 5 positions)

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- If we have a weighted positional notation system that has 5 bits, can the three numbers above all be represented in this notation system? How to do calculations?
- We would need to remember:
 - The raw bit stream (5 bits)
 - Where the binary point is (potentially another 3 bits for 5 positions)
- Makes calculations (e.g. addition) hard
 - Need to first align numbers according to the binary point

Fixed-Point Representation

Fixed-Point Representation

- Binary point stays fixed

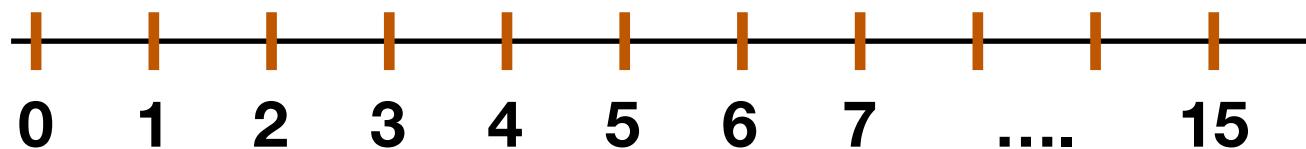
Fixed-Point Representation

- Binary point stays fixed

Decimal	Binary
0	0000.
1	0001.
2	0010.
3	0011.
4	0100.
5	0101.
6	0110.
7	0111.
8	1000.
9	1001.
10	1010.
11	1011.
12	1100.
13	1101.
14	1110.
15	1111.

Fixed-Point Representation

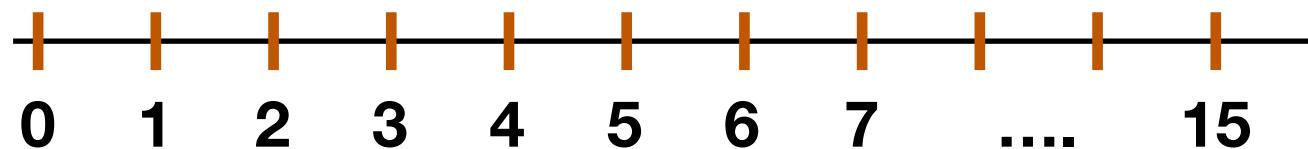
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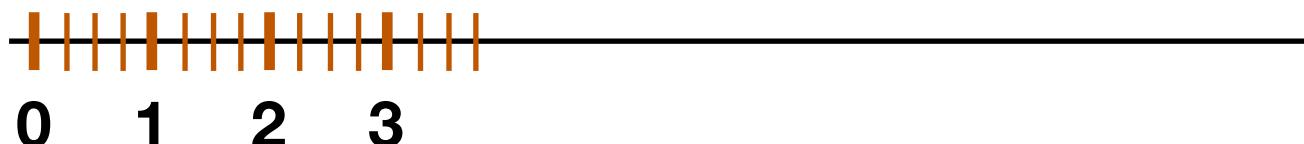
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Decimal	Binary
0	00.00
0.25	00.01
0.5	00.10
0.75	00.11
1	01.00
1.25	01.01
1.5	01.10
1.75	01.11
2	10.00
2.25	10.01
2.5	10.10
2.75	10.11
3	11.00
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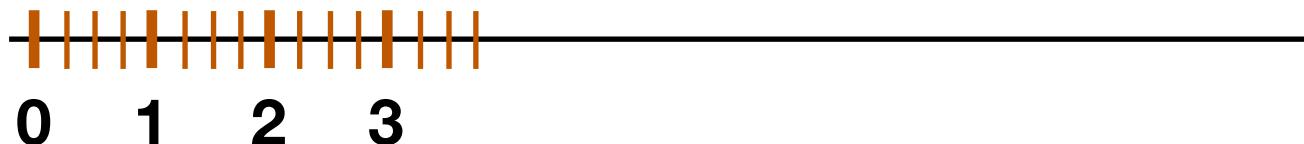
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Fixed-Point Representation

- Binary point stays fixed
- Fixed interval between representable numbers
 - Each bit represents 0.25_{10}



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- Still need to remember the binary point, but just once for all numbers

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Fixed-Point Representation

- Binary point stays fixed
- Fixed interval between representable numbers
 - Each bit represents 0.25_{10}



- Still need to remember the binary point, but just once for all numbers
- No need to align (already aligned)

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1.5	01.10
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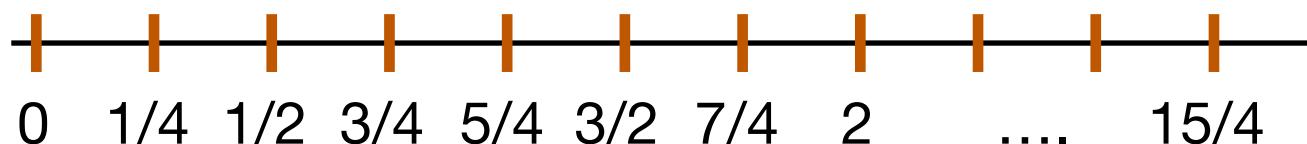
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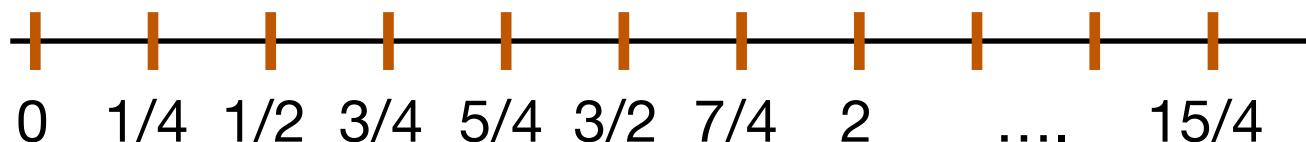
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$b_3b_2.b_1b_0$

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 - Other rational numbers have repeating bit representations

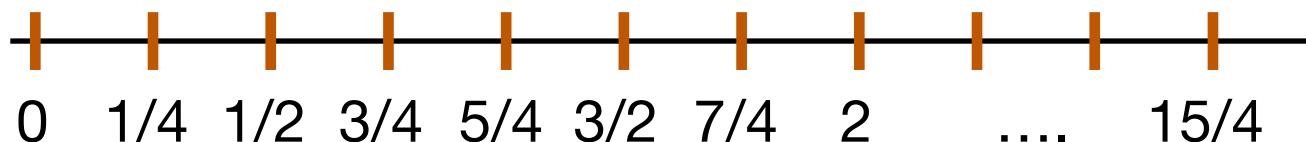


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Limitations of Fixed-Point (#2)

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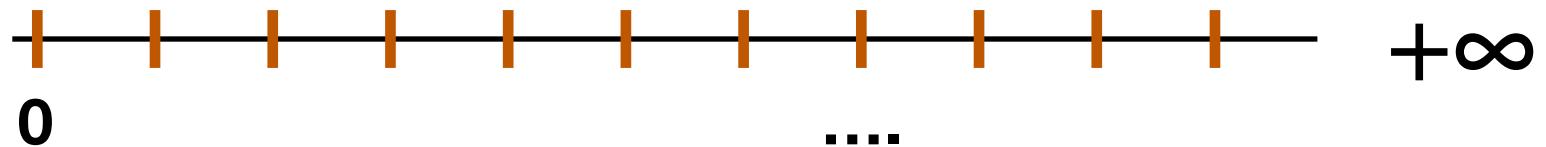
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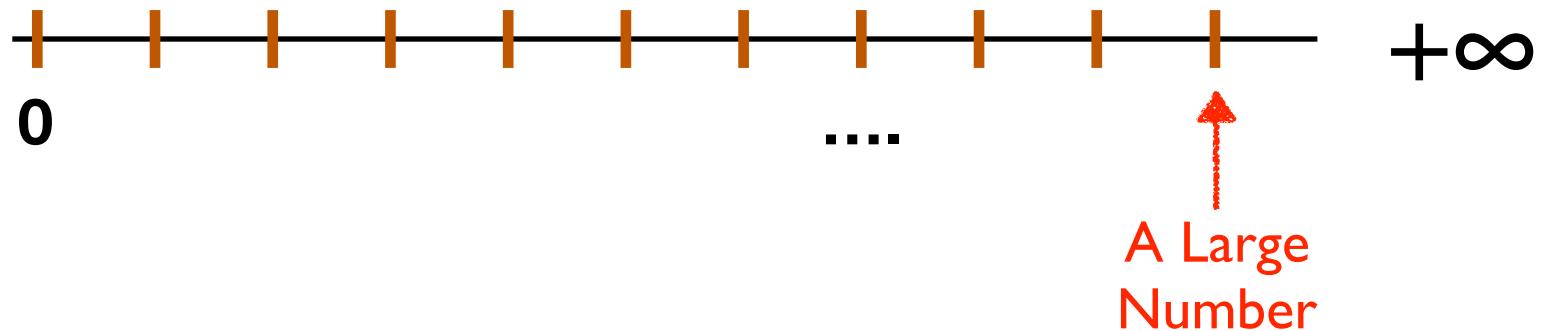
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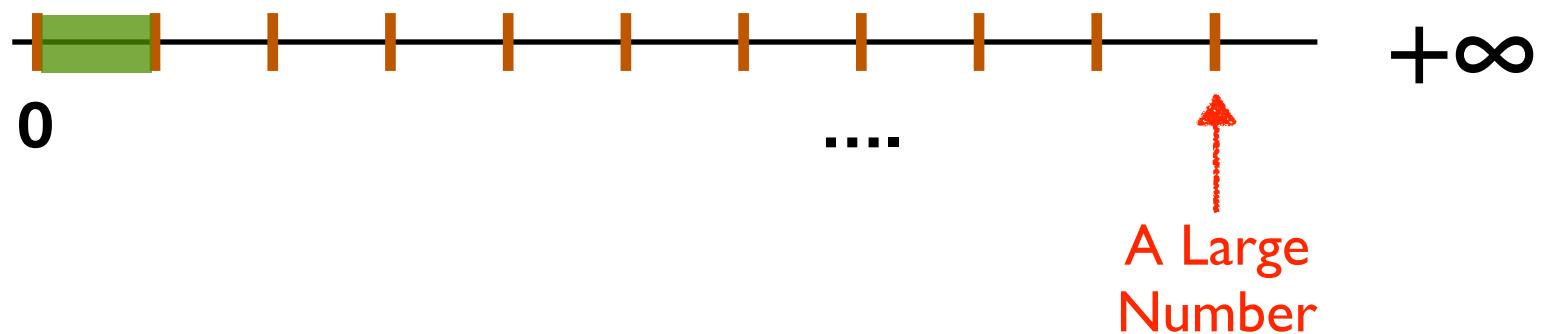
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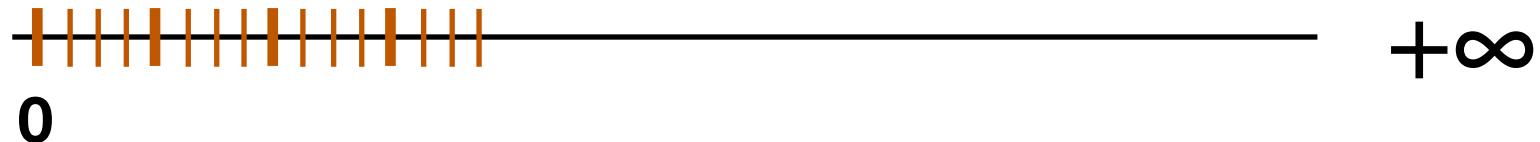
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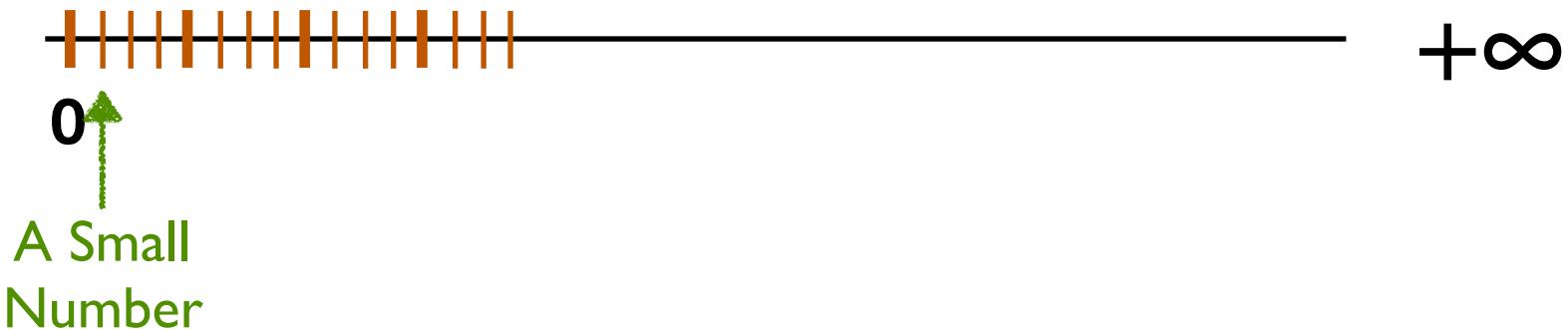
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Today: Floating Point

- Background: Fractional binary numbers and fixed-point
- **Floating point representation**
- IEEE 754 standard
- Rounding, addition, multiplication
- Floating point in C
- Summary

Primer: (Normalized) Scientific Notation

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Decimal Value	Scientific Notation
2	2×10^0
-4,321.768	-4.321768×10^3
0.000 000 007 51	7.51×10^{-9}

Primer: (Normalized) Scientific Notation

- In decimal: $M \times 10^E$
 - E is an integer
 - Normalized form: $1 \leq |M| < 10$

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Significand

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↑ ↑
Significand Base

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Significand Base Exponent

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Binary Value	Scientific Notation
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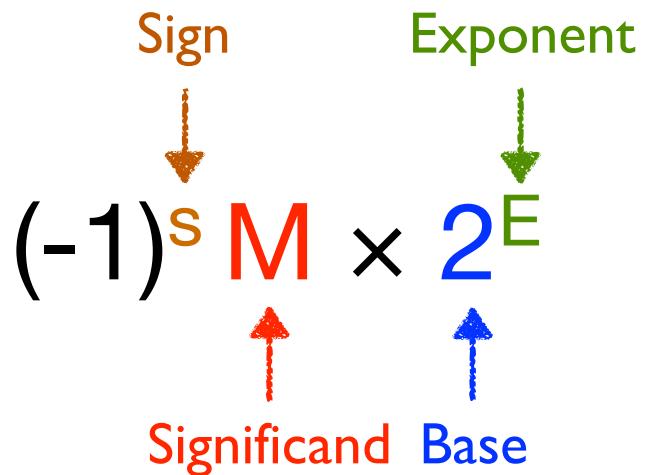
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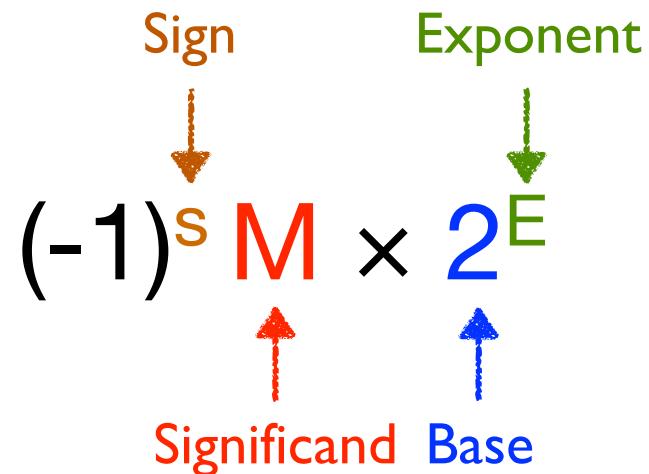
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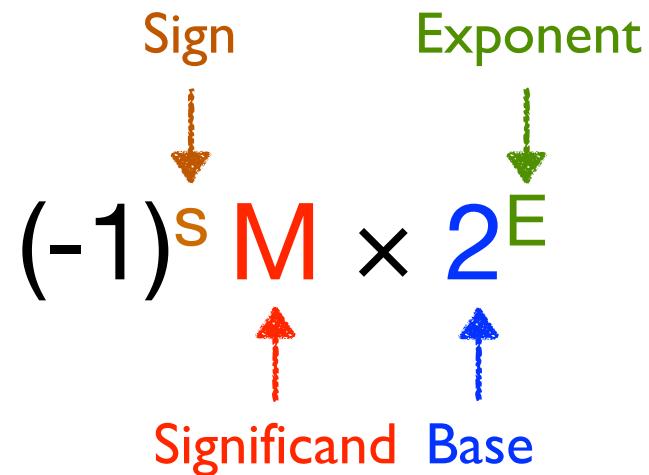


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Fraction

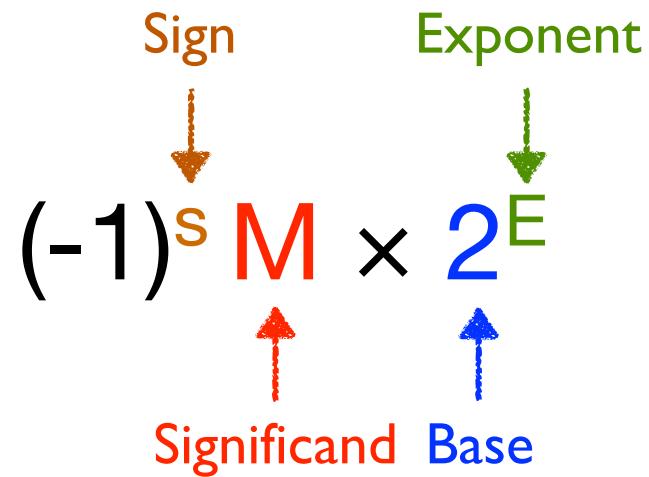


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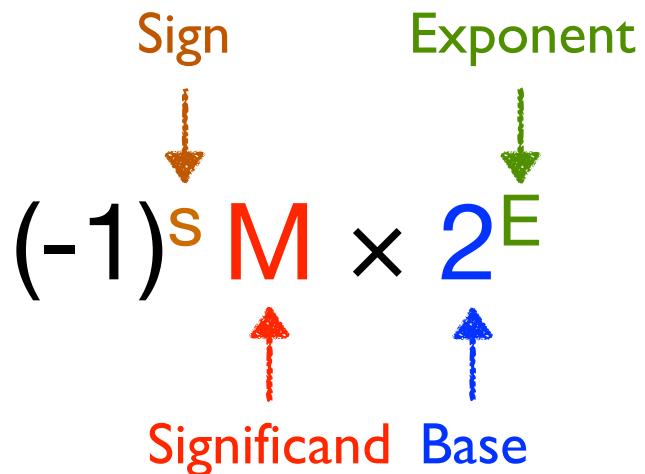
Fraction



- If I tell you that there is a number where:
 - Fraction = 0101
 - $s = 1$
 - $E = 10$
 - You could reconstruct the number as $(-1)^1 1.0101 \times 2^{10}$

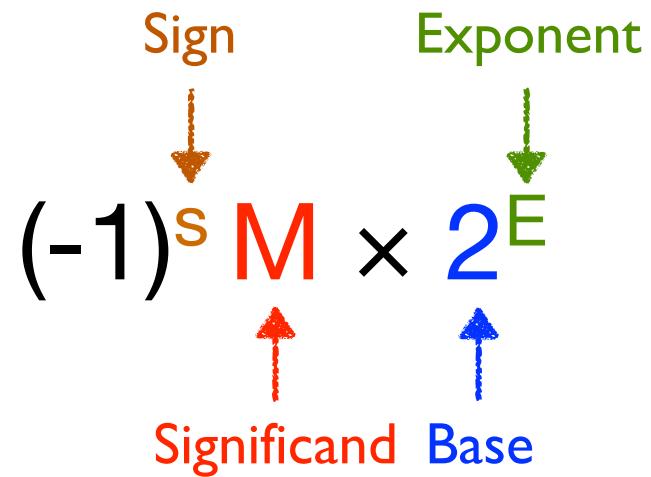
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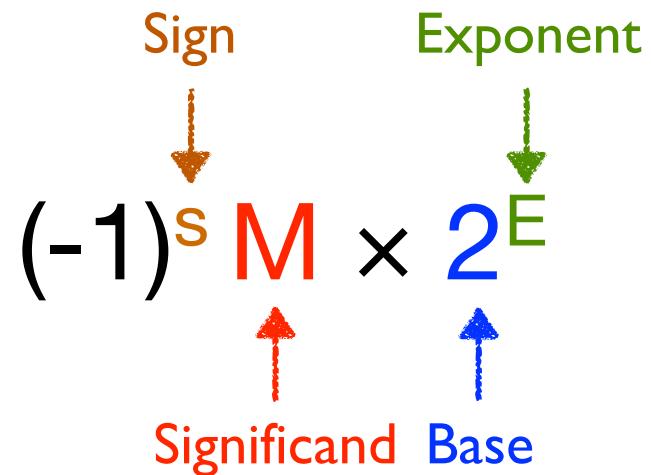
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- Encoding



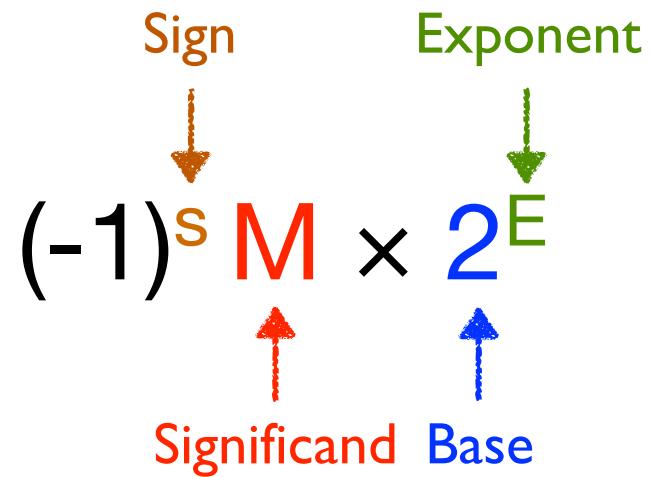
Primer: Floating Point Representation

- In binary: $(-1)^s M 2^E$
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 - $1 \leq M < 2$
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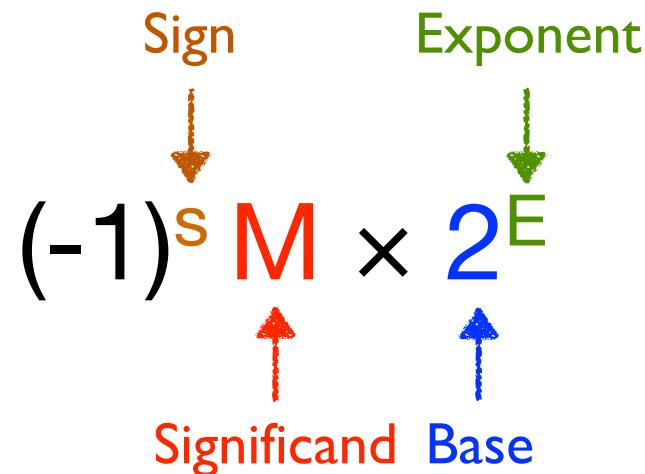
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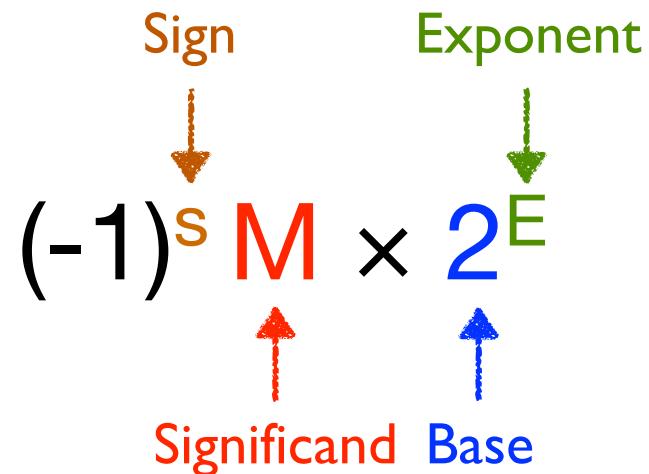
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6-bit Floating Point Example

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E	exp
-3	000
-2	001
-1	010
0	011
1	100
2	101
3	110
4	111

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 - Reserve 000 and 111 for other purposes (more on this later)

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 - bias is always $2^{k-1} - 1$, where k is number of exponent bits
- Example when $k = 3$:
 - bias = 3
 - If $E = -2$, exp is 1 (001_2)
 - Reserve 000 and 111 for other purposes (more on this later)
 - We can now represent exponents from **-2 (exp 001) to 3 (exp 110)**

E	exp
-3	000
-2	001
-1	010
0	011
1	100
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$$-10.1_2 = (-1)^1 1.01 \times 2^1$$

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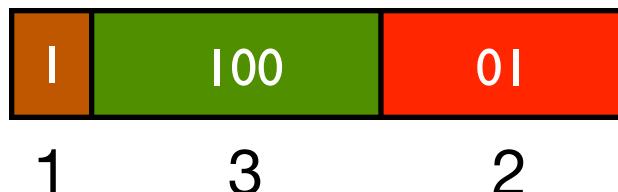


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Representable Numbers (Positive Only)

$$v = (-1)^s M \cdot 2^E$$



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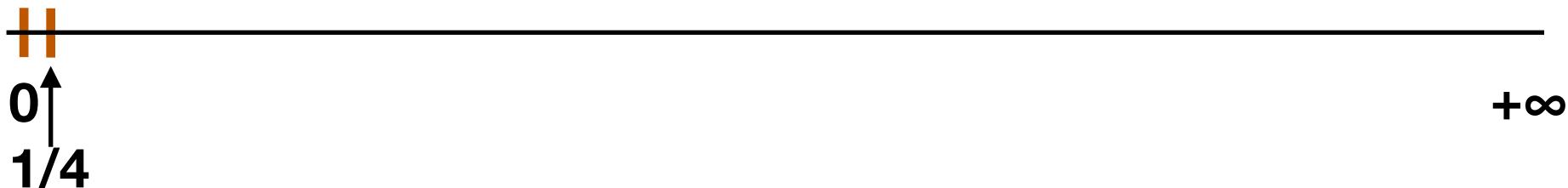


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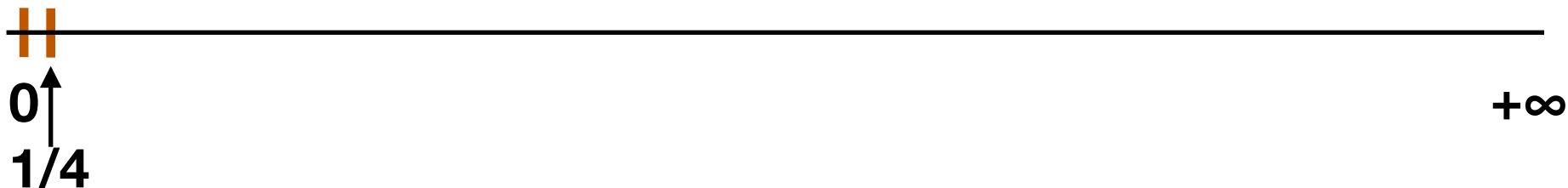


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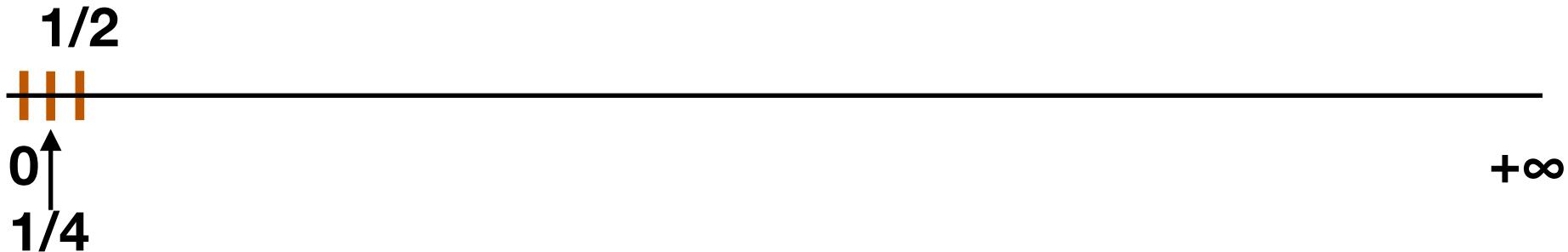


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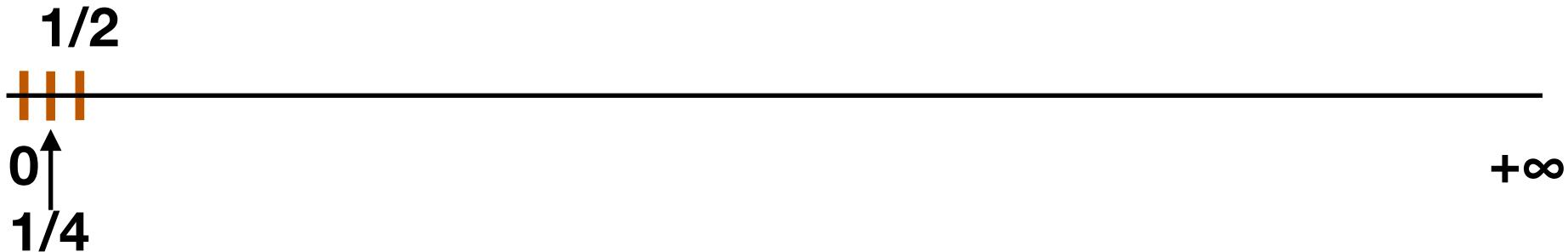


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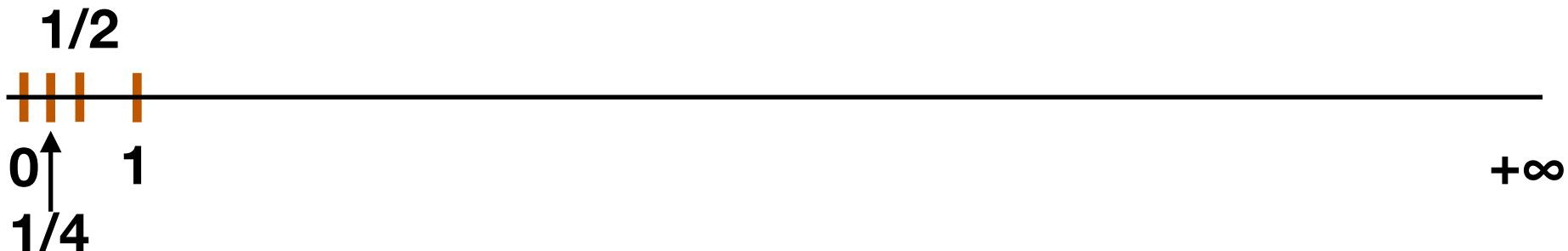


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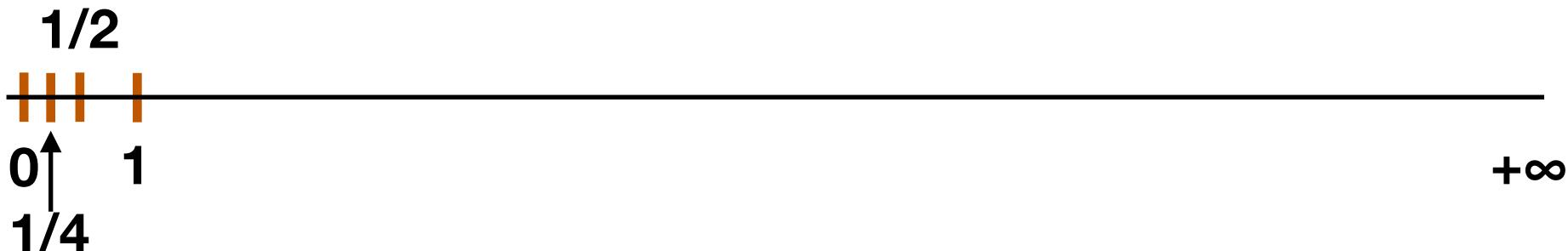


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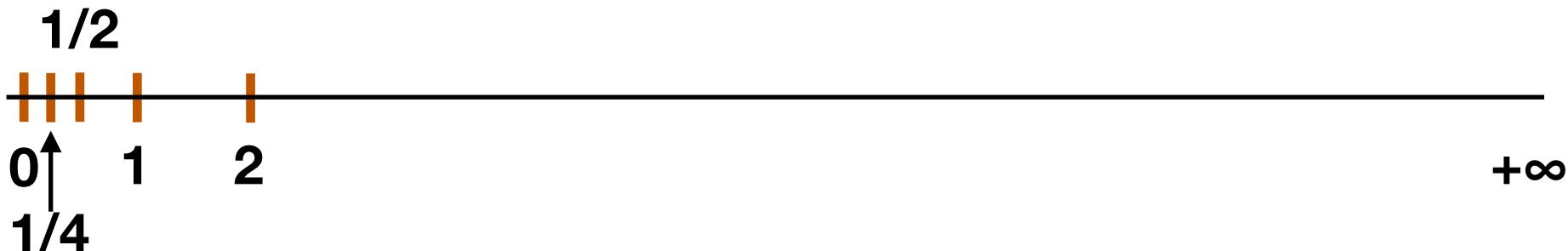


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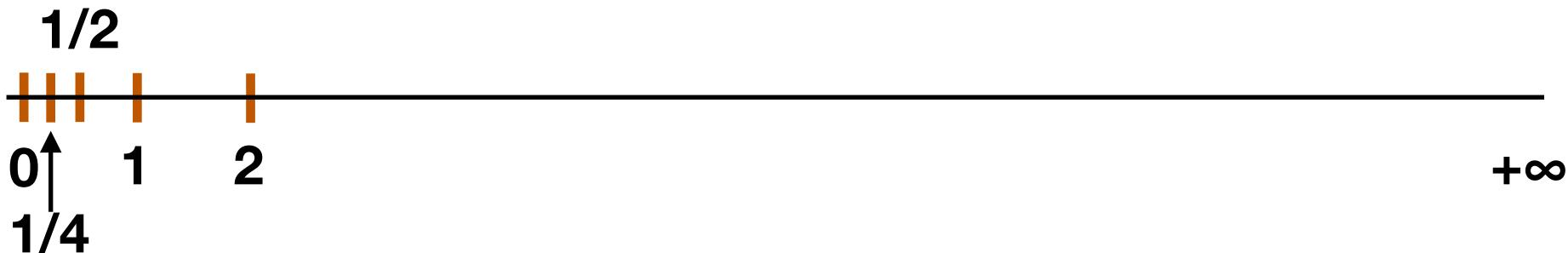


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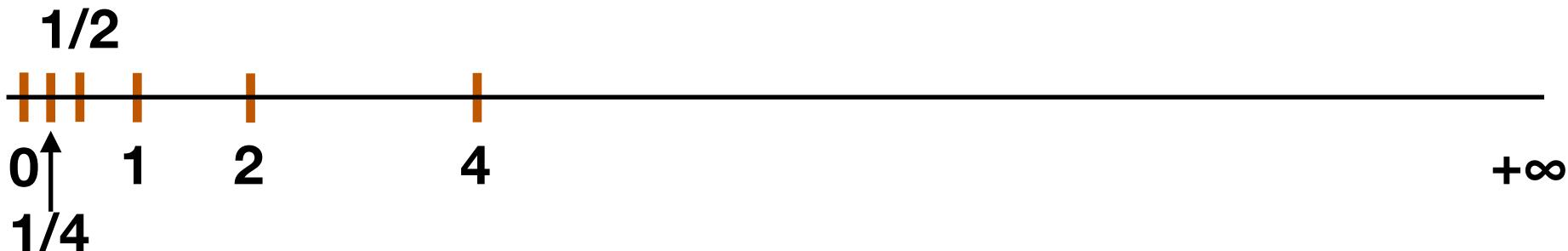


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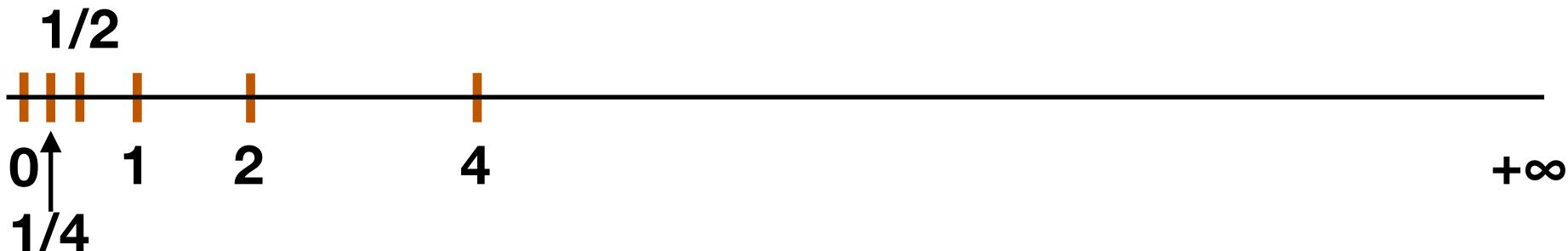


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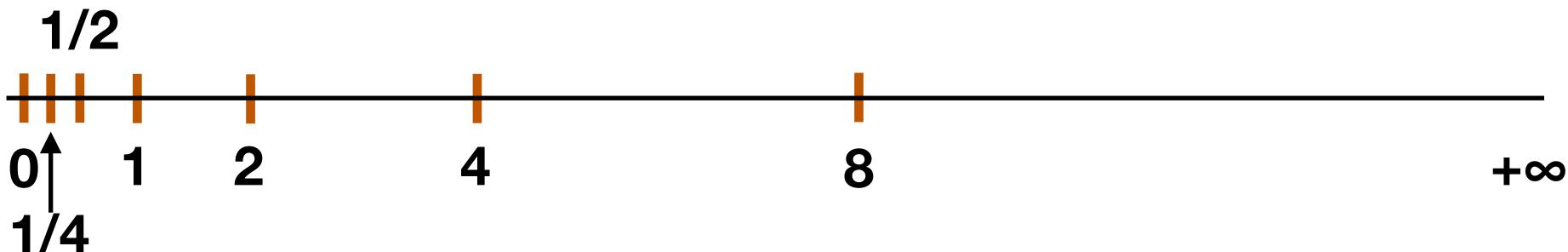


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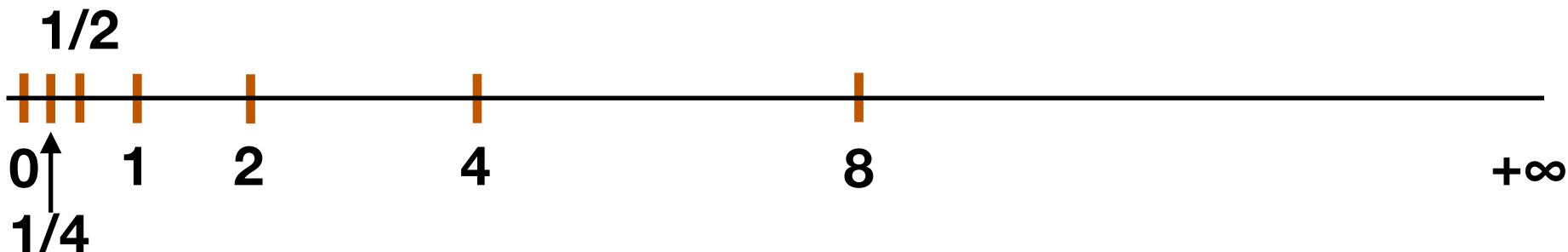


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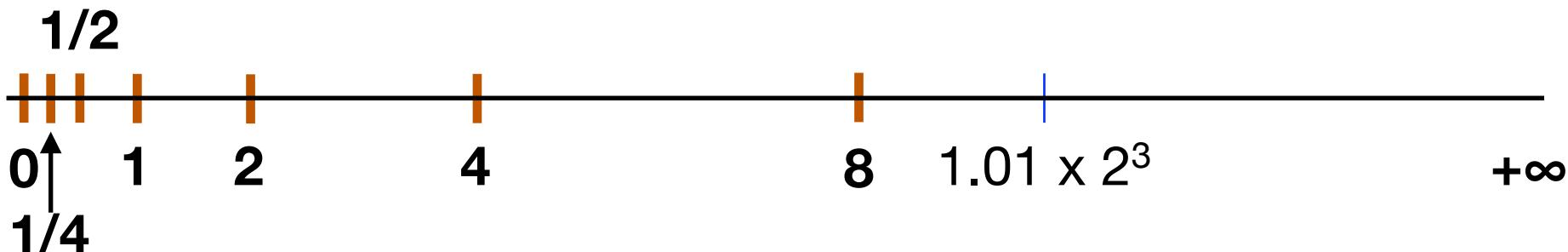


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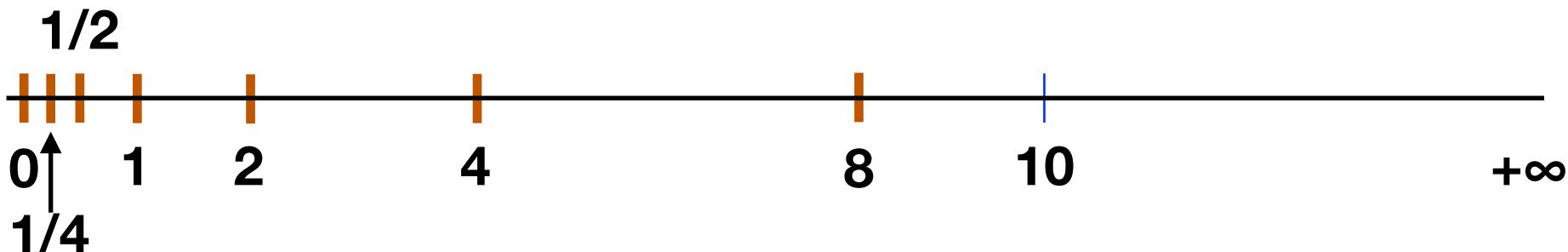


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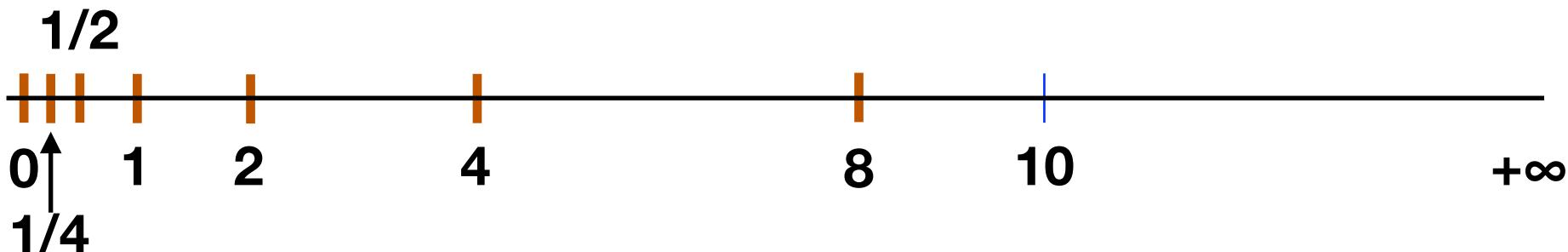


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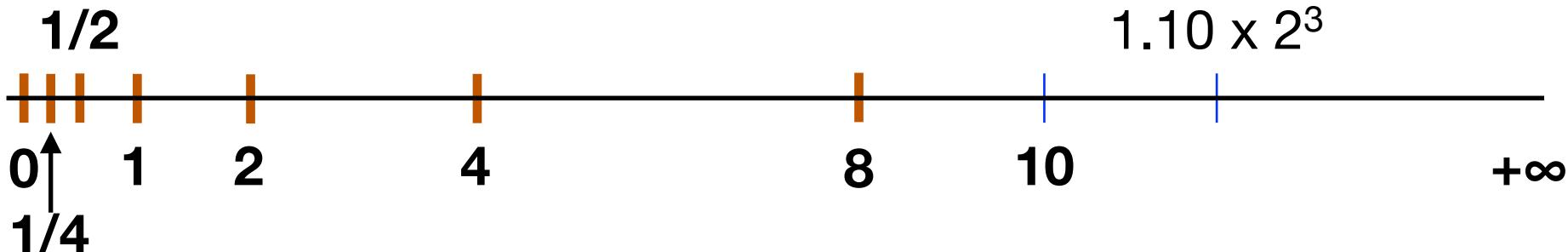


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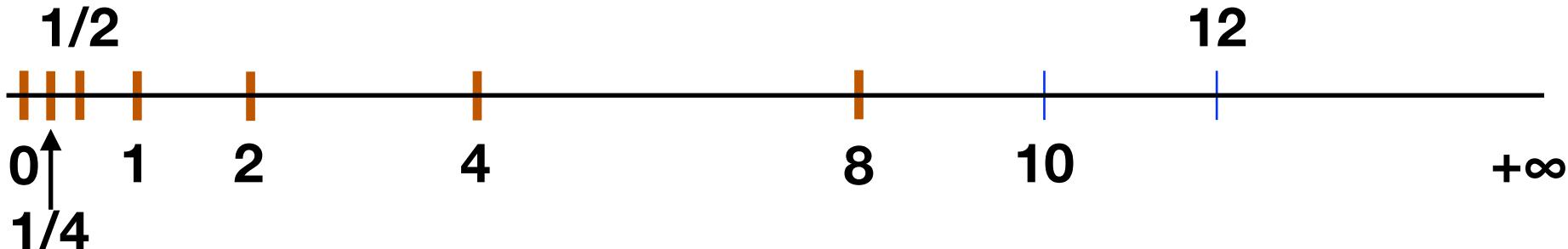


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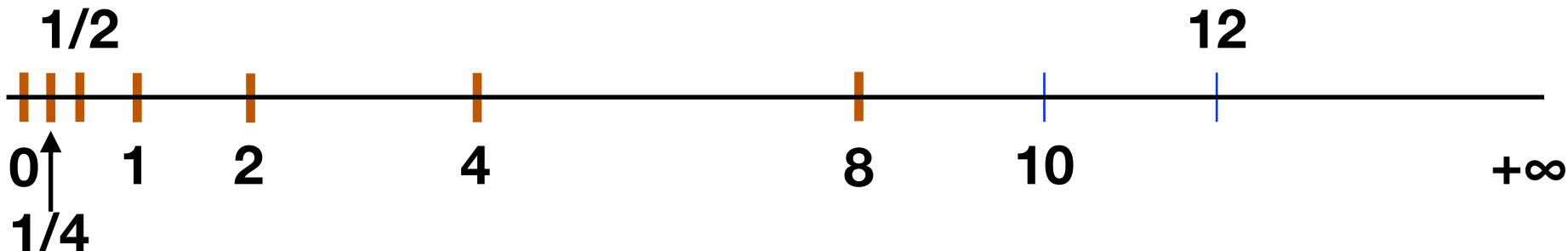


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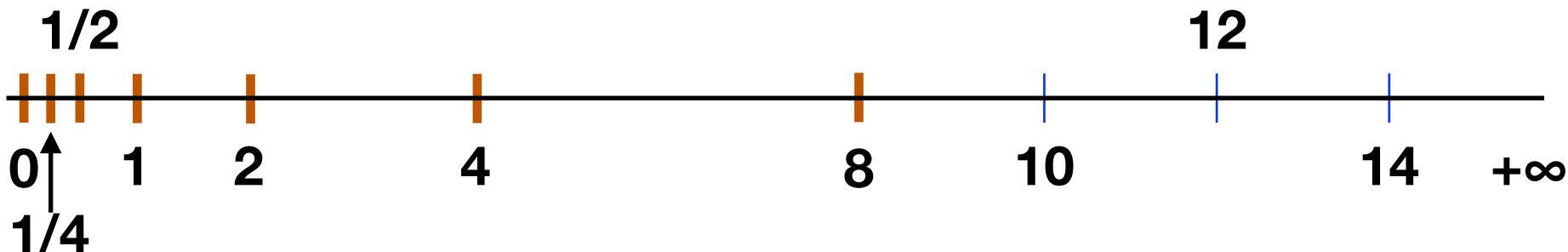


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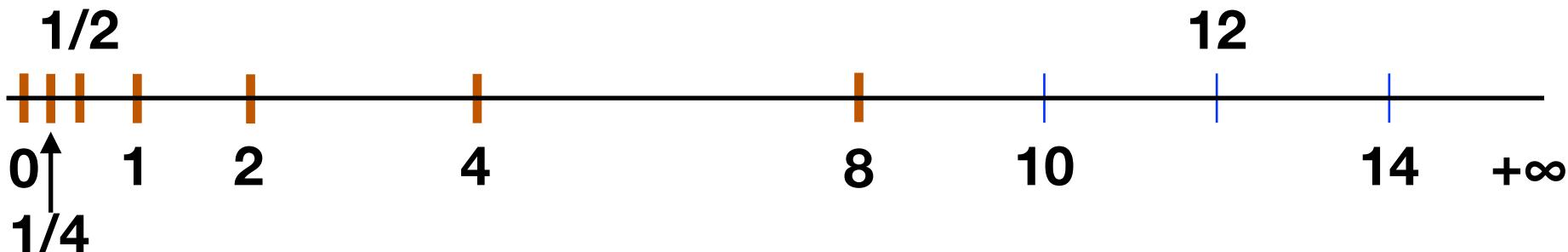


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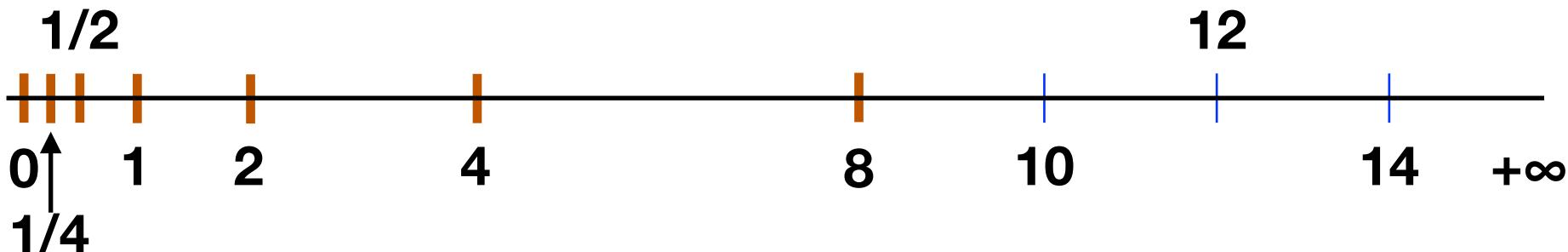


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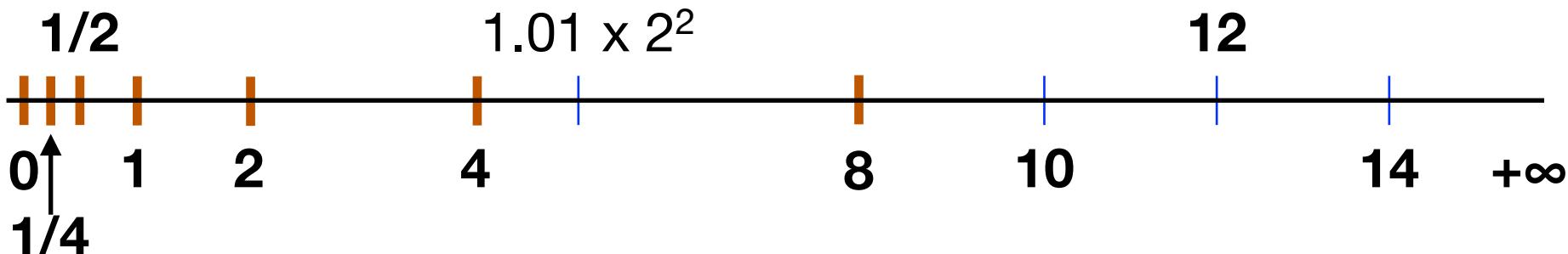


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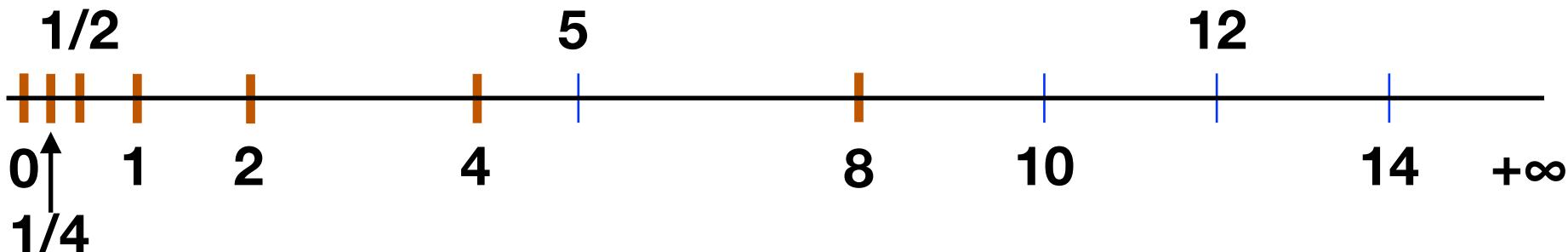


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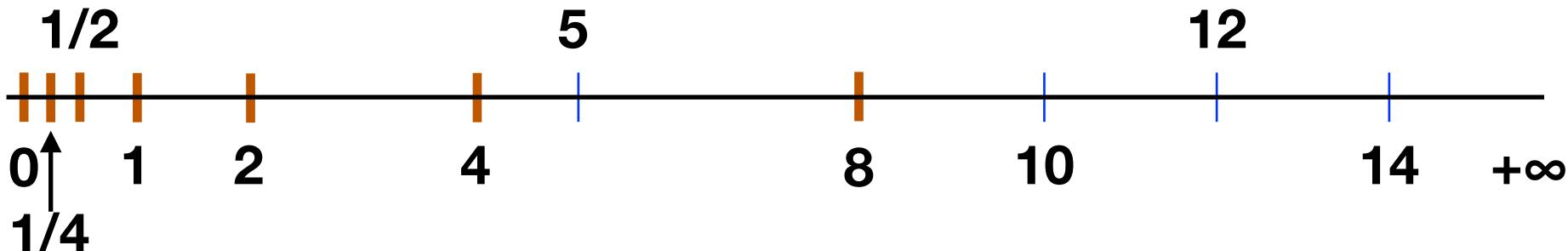


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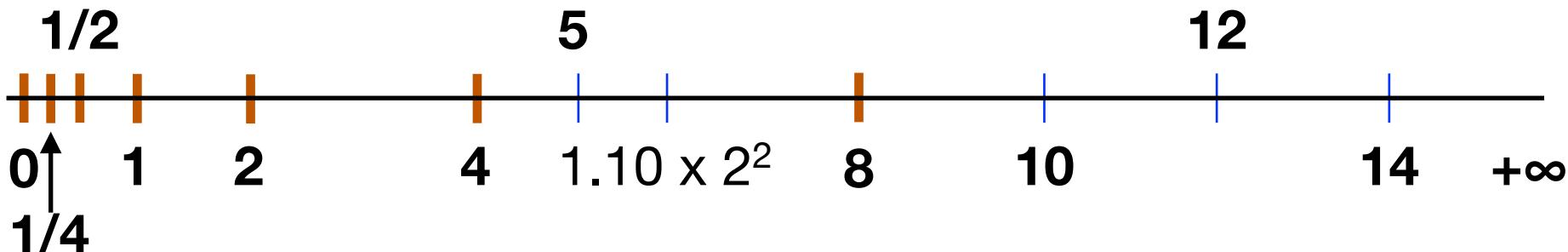


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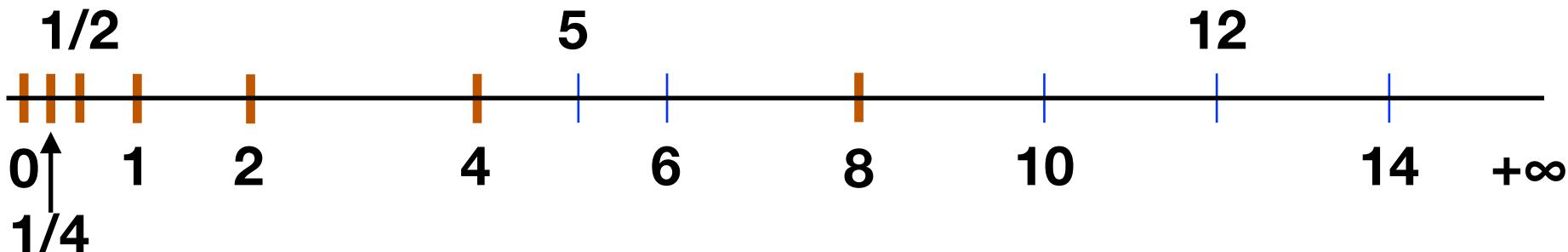


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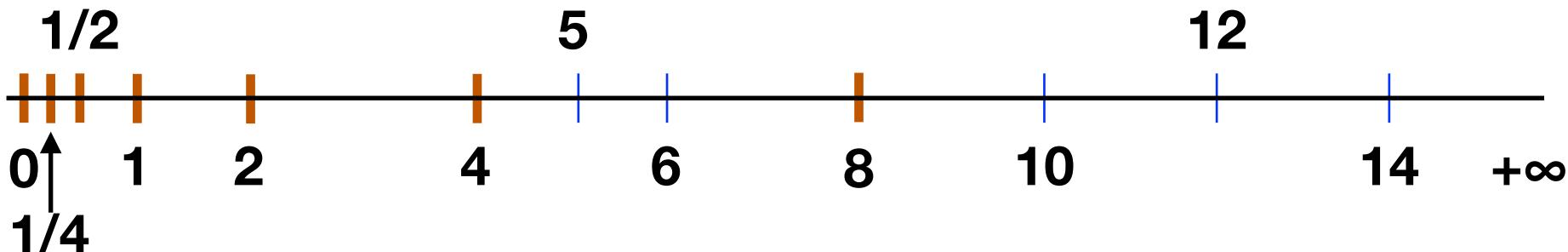


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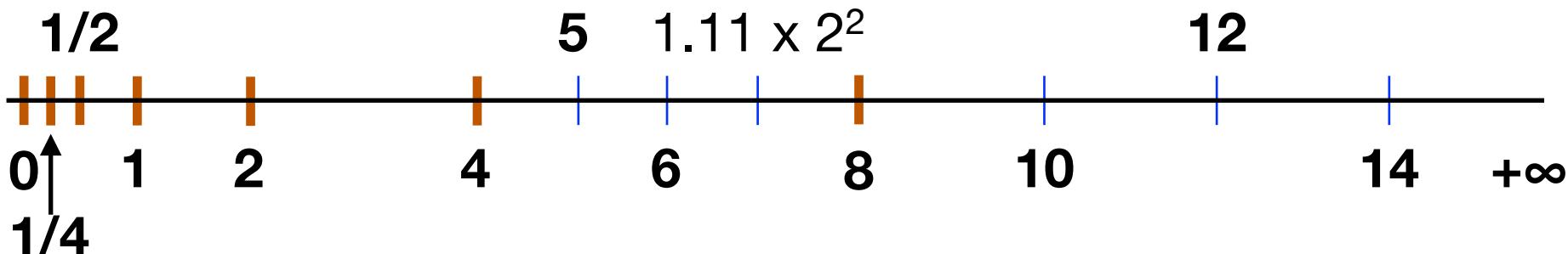


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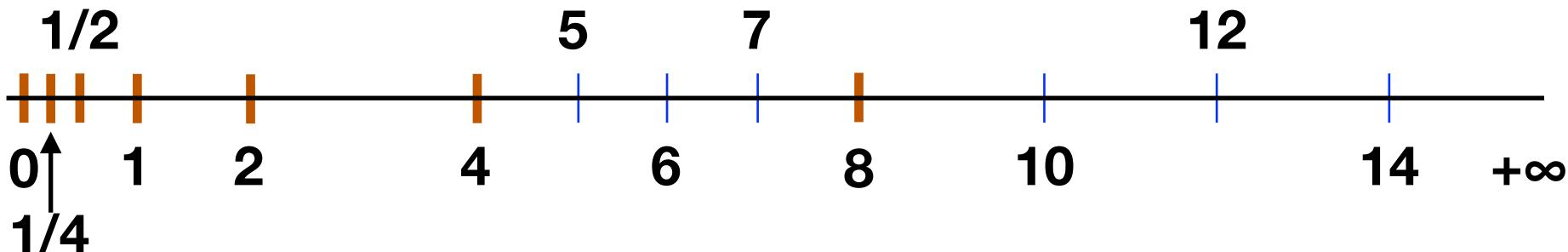


Representable Numbers (Positive Only)

$$v = (-1)^s M \cdot 2^E$$



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-2	001	2	101
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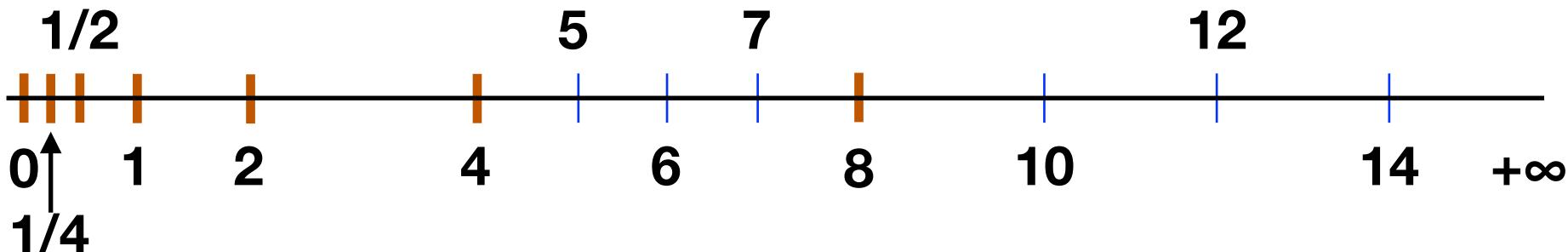


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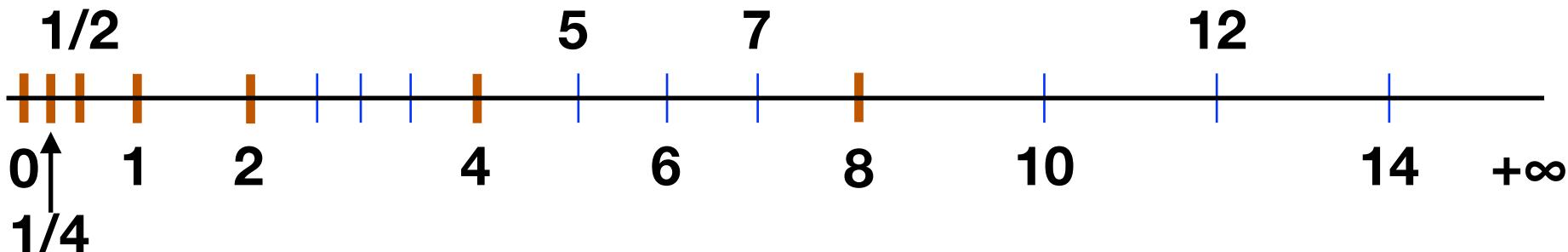


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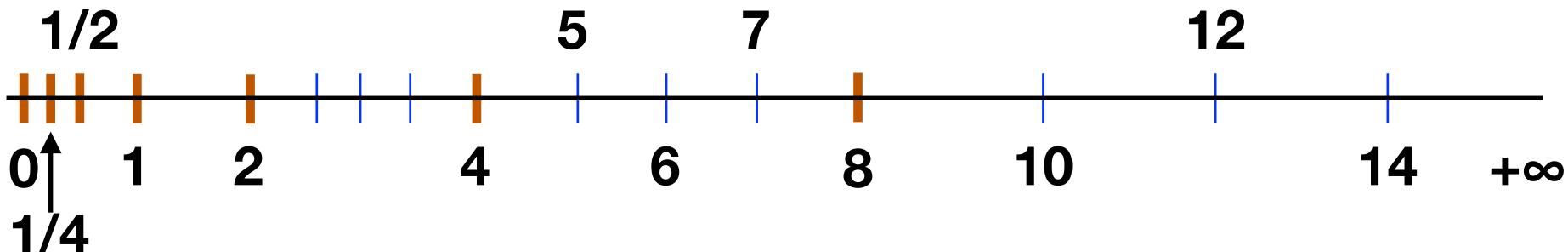


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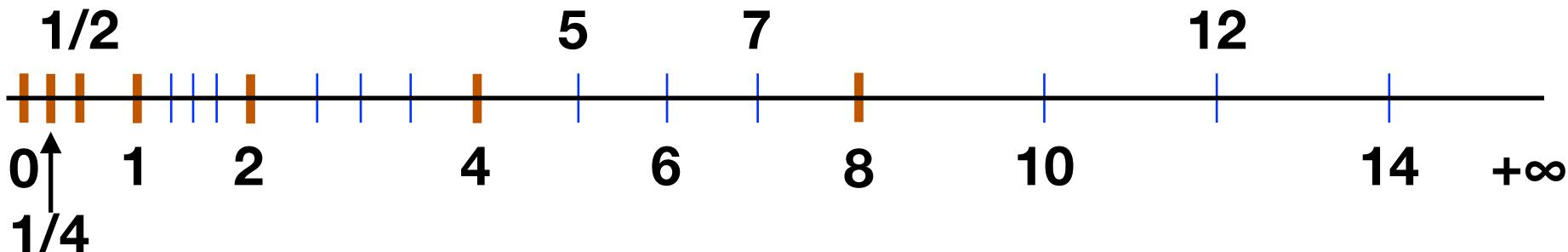


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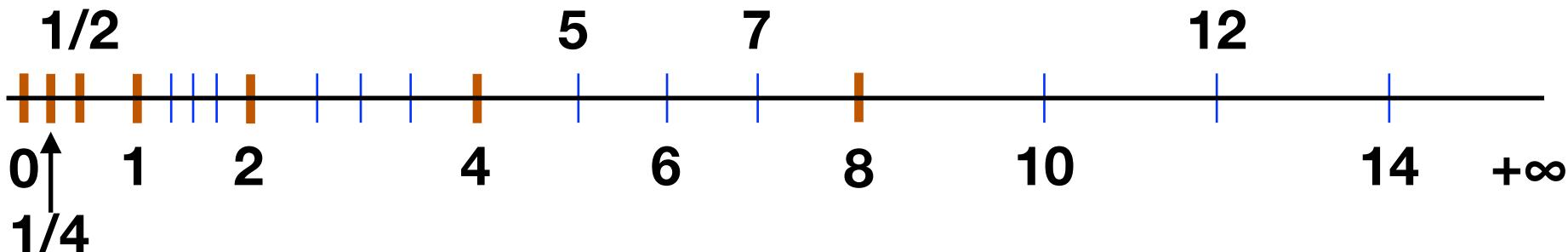


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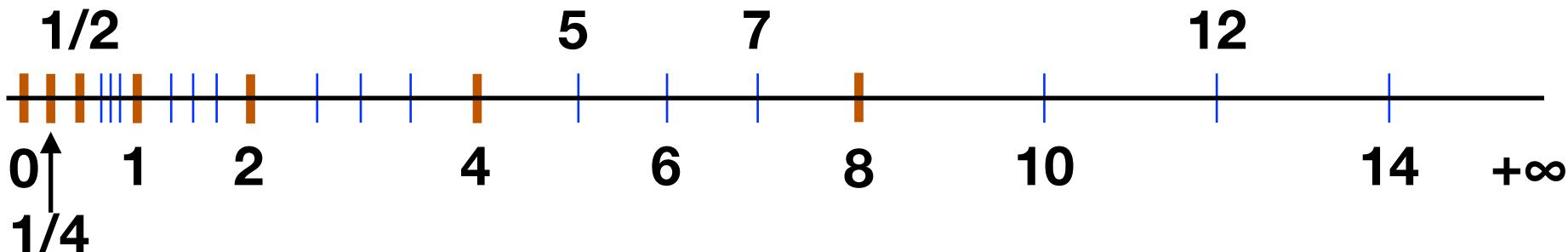


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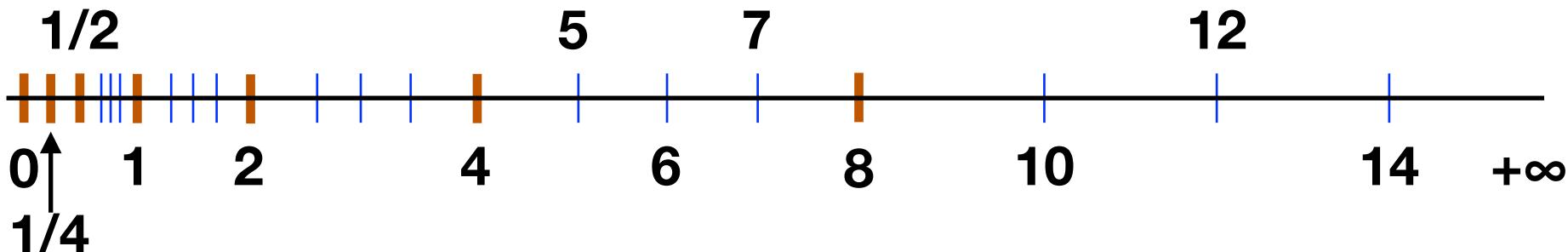


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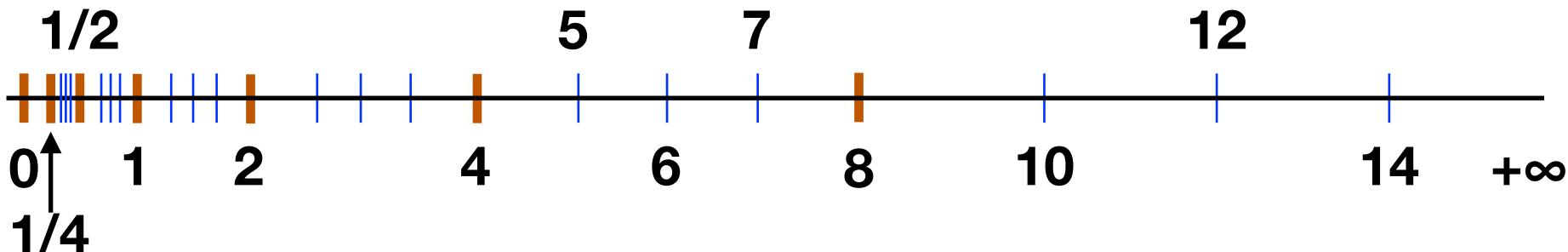


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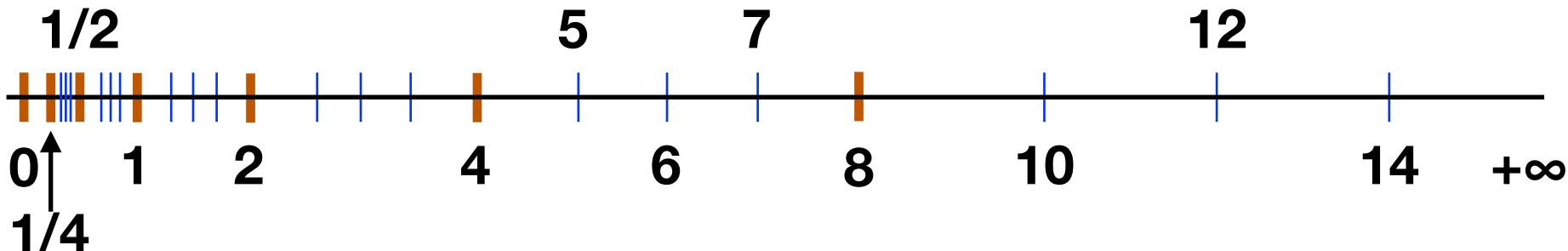
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 - More dense toward 0, sparser toward infinite
 - Allow encoding small and large numbers at the same time



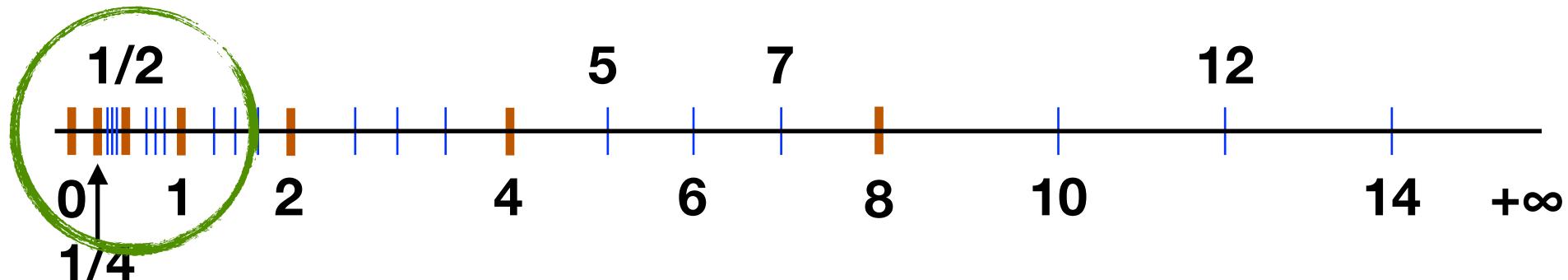
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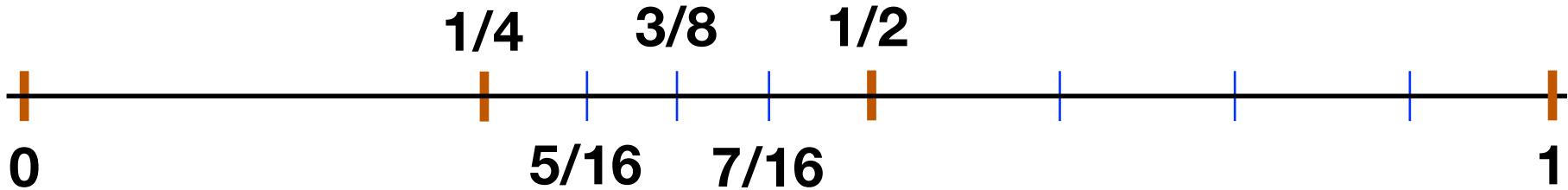


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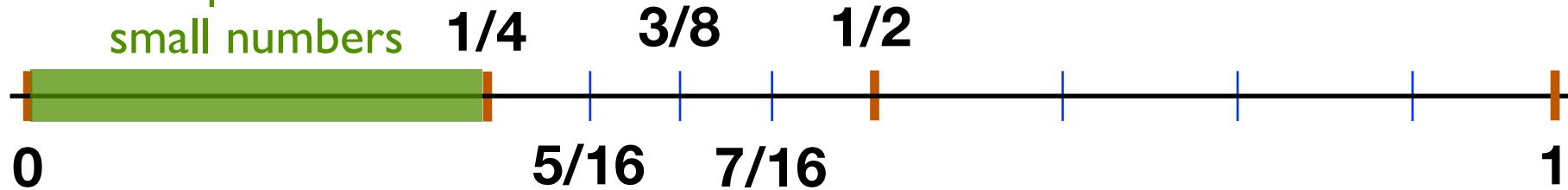
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Unrepresented
small numbers



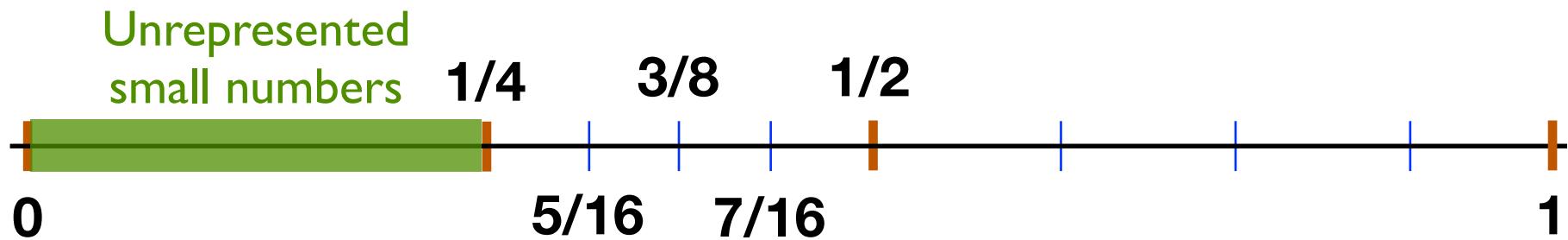
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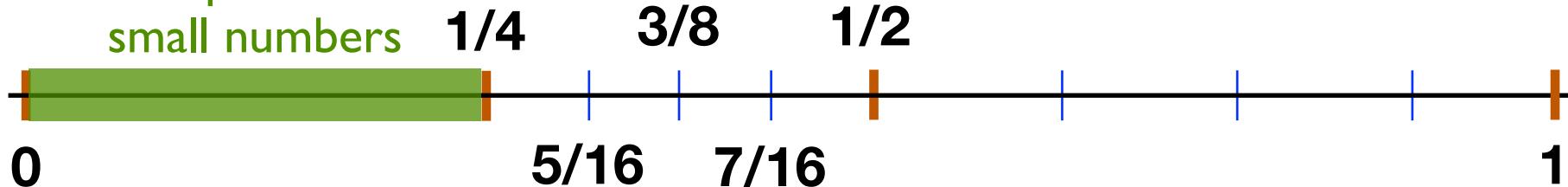
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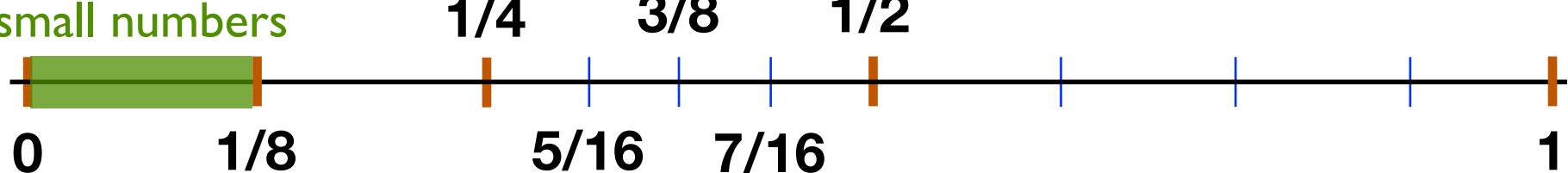
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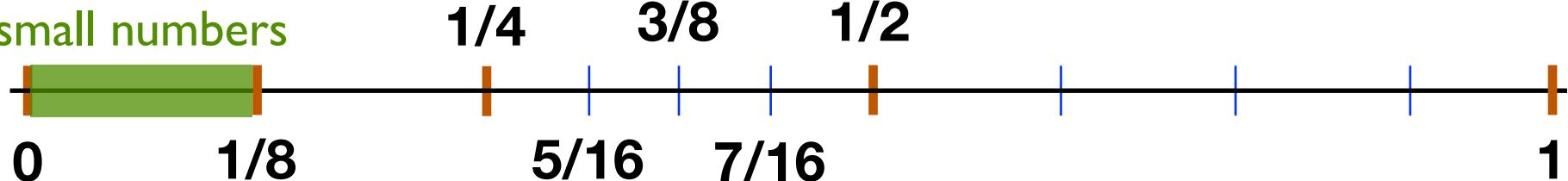
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- Using 000 for exp would only postpone the problem rather than solving it

Unrepresented
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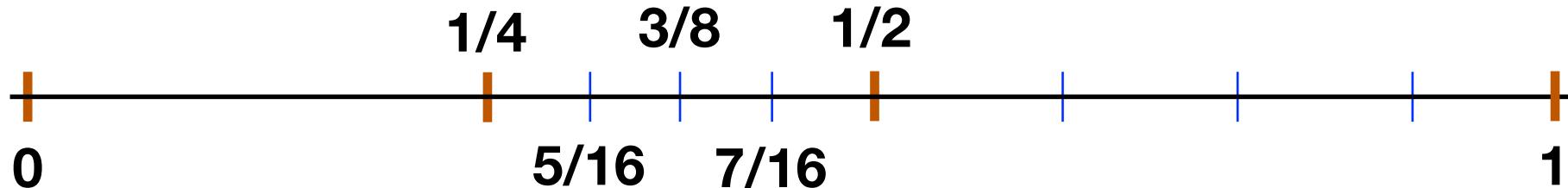
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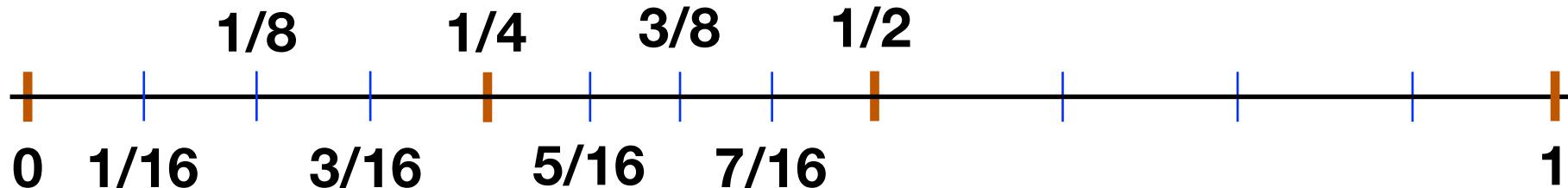
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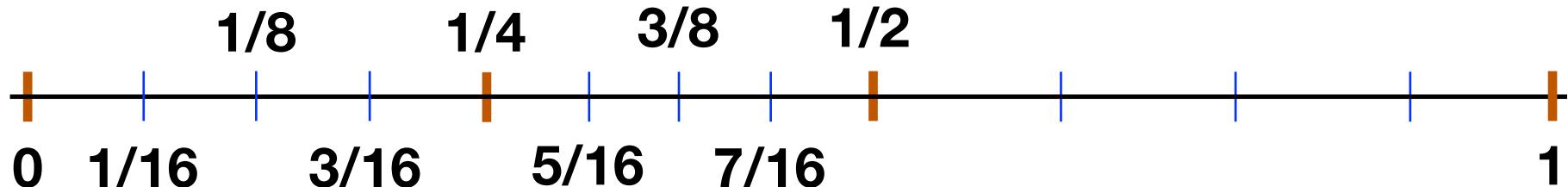
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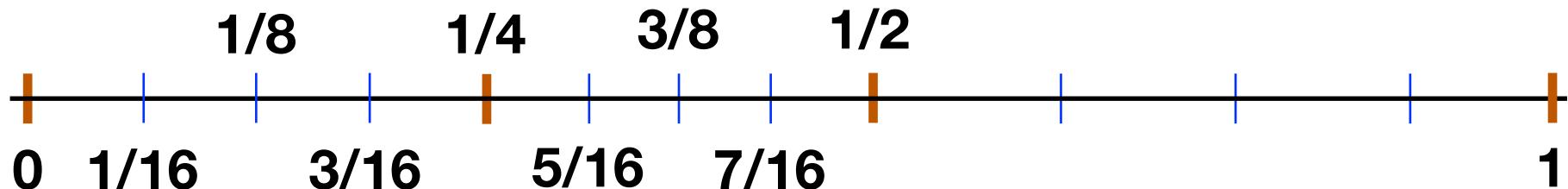
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A diagram showing the bit fields for a subnormal number. It consists of three colored boxes: orange for the sign (0), green for the exponent (000), and red for the fraction (01).

$$= (-1)^0 0.01 \times 2^{(0+1-3)} = 1/16$$

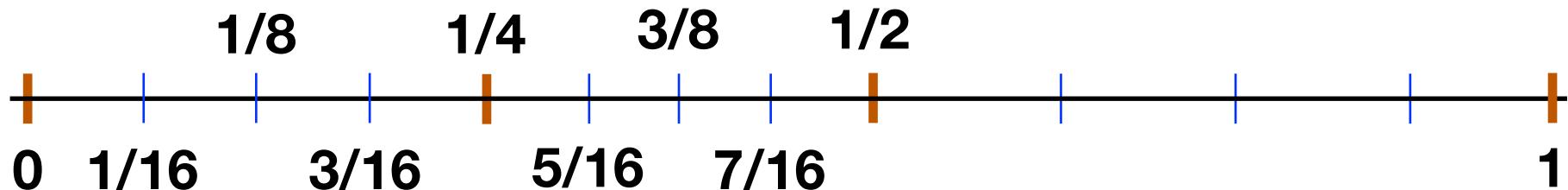
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- Subnormal numbers allow graceful underflow



A binary representation of a subnormal number: sign (0), exponent (000), and fraction (01). This corresponds to the value $1/16$.

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Special Values

$$v = (-1)^s M \cdot 2^E$$



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 - +/- ∞ , Not-a-Numbers (NaNs) e.g., $0 / 0$, $0 / \infty$, ∞ / ∞ , $\sqrt{-1}$, $\infty - \infty$, $\infty \times 0$, etc.

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- $\text{exp} = 111$, $\text{frac} = 000$
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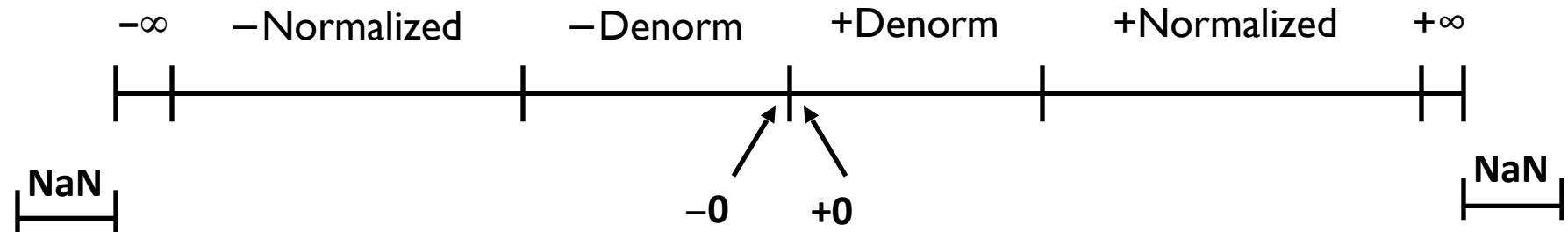
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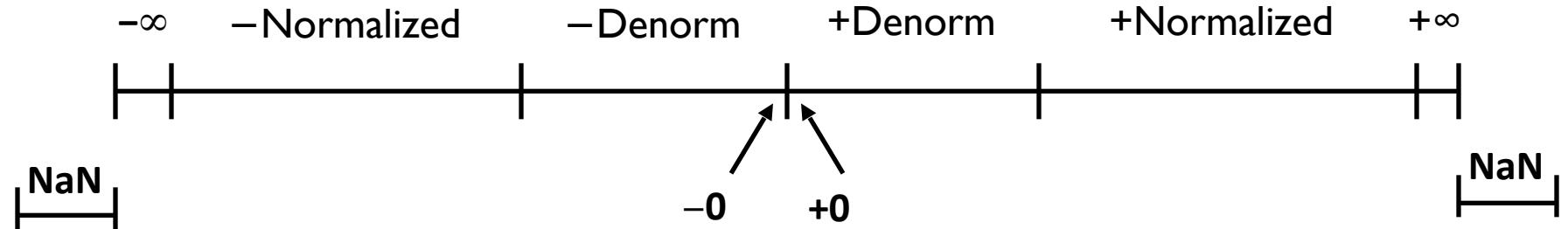
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- $\text{exp} = 111$, $\text{frac} \neq 000$
 - Represent NaNs

Visualization: Floating Point Encodings



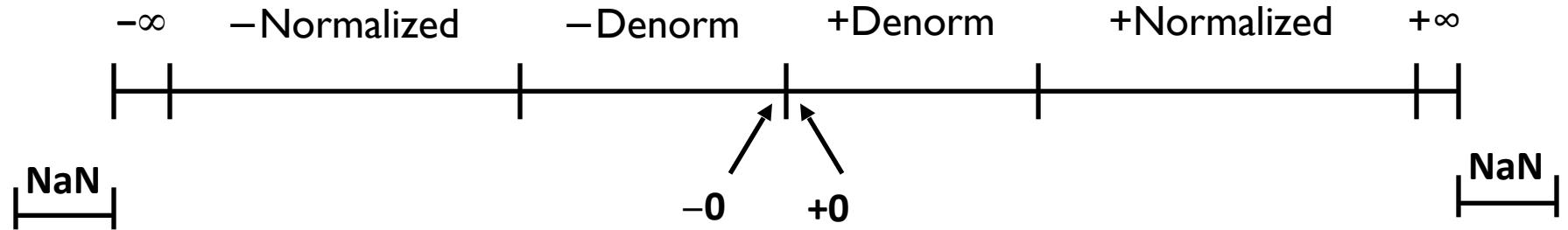
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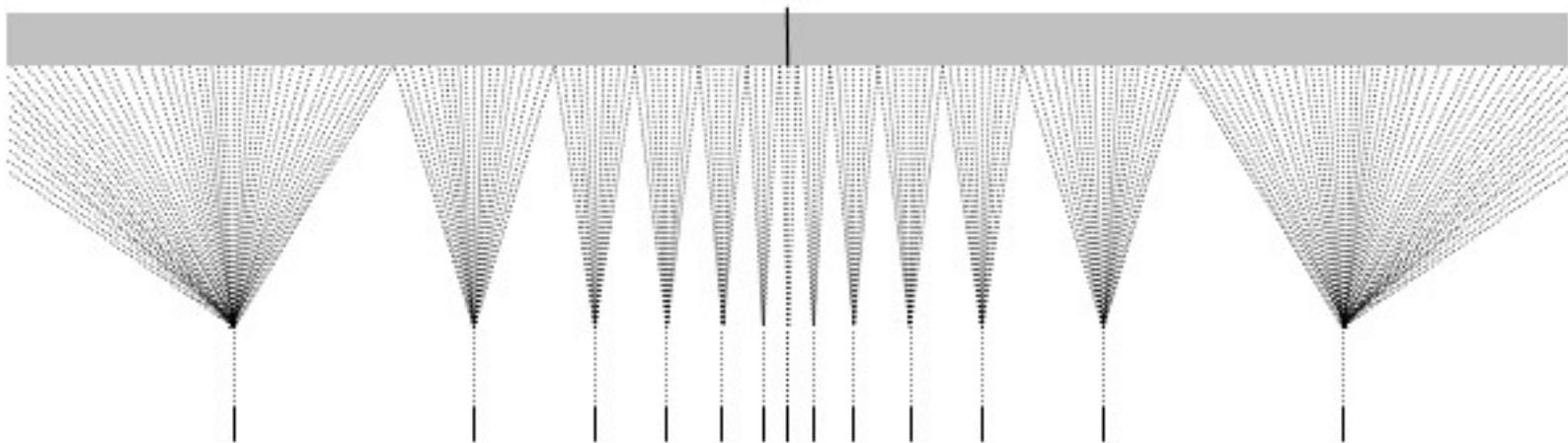
Infinite Amount of Real Numbers



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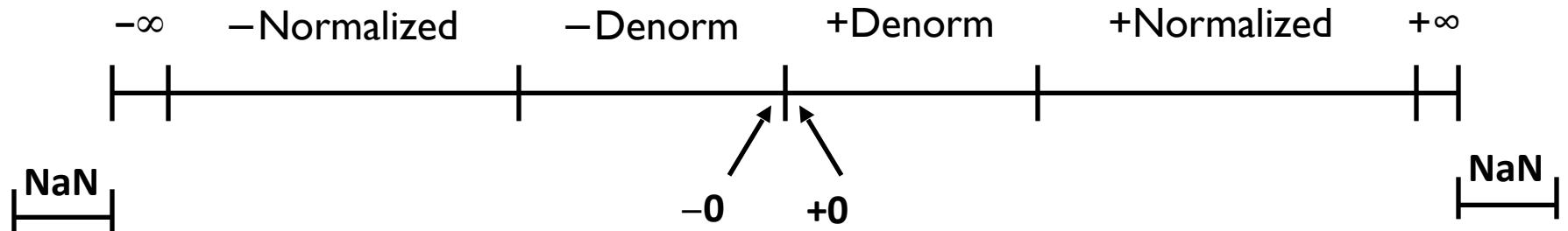


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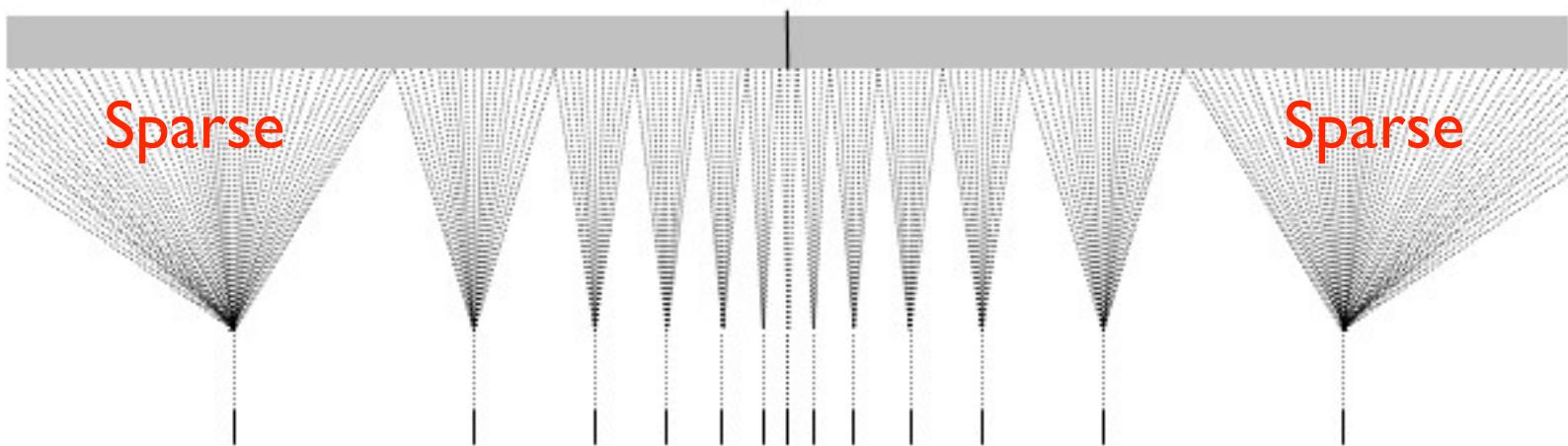


Finite Amount of Floating Point Numbers

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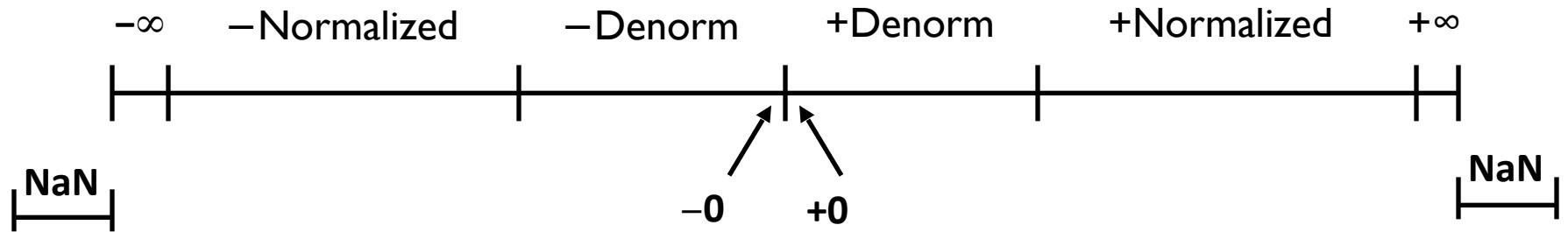


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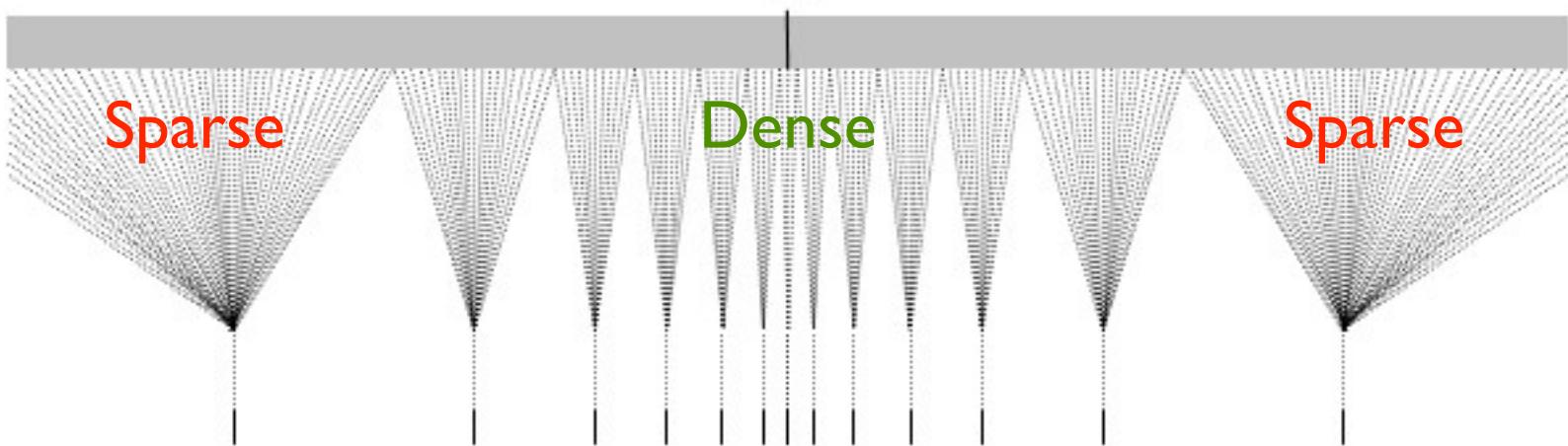


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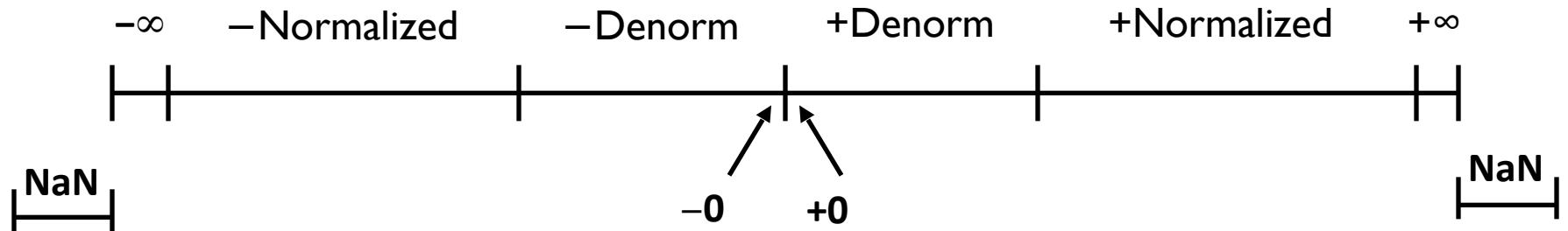


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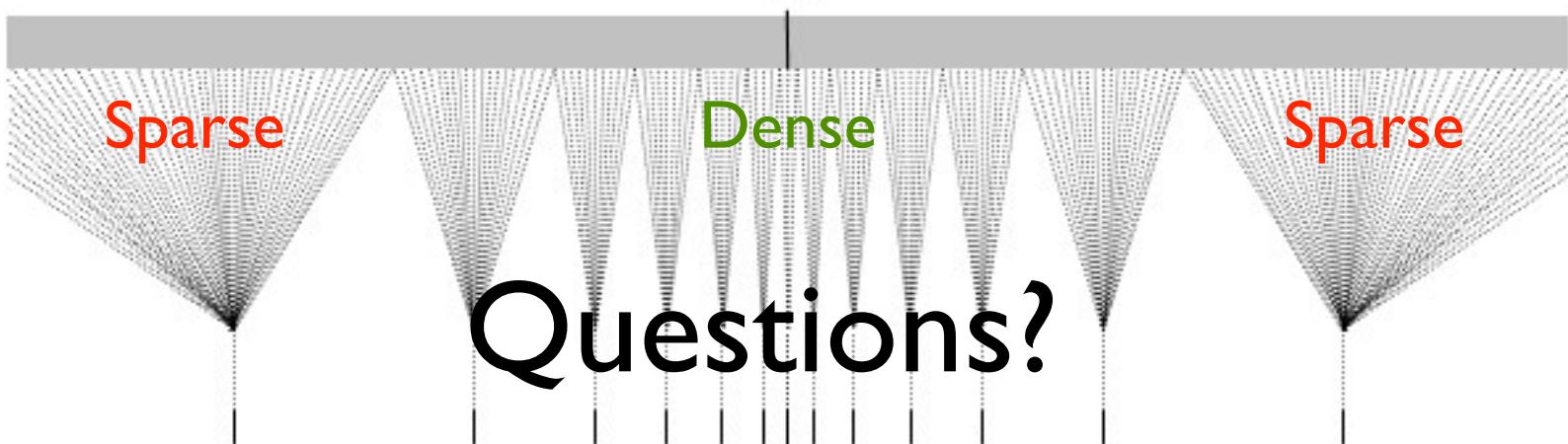


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Questions?

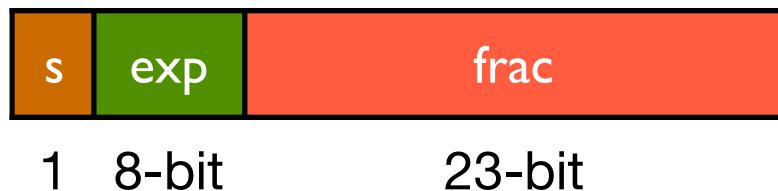
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Today: Floating Point

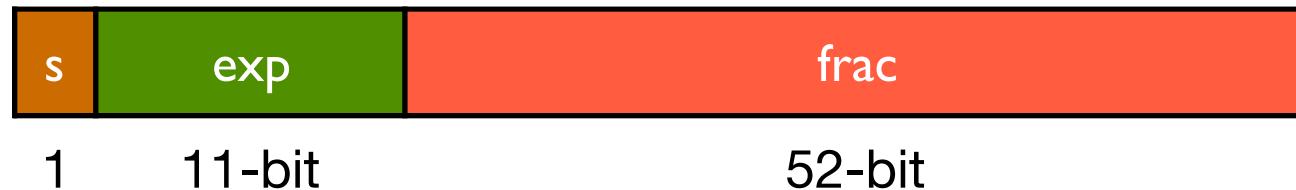
- Background: Fractional binary numbers and fixed-point
- Floating point representation
- IEEE 754 standard
- Rounding, addition, multiplication
- Floating point in C
- Summary

IEEE 754 Floating Point Standard

- Single precision: 32 bits



- Double precision: 64 bits



IEEE Floating Point

- IEEE Standard 754
 - Established in 1985 as uniform standard for floating point arithmetic
 - Before that, many idiosyncratic formats
 - Supported by all major CPUs (and even GPUs and other processors)
- Driven by numerical concerns
 - Nice standards for rounding, overflow, underflow
 - Hard to make fast in hardware
 - Numerical analysts predominated over hardware designers in defining standard

Single Precision (32-bit) Example

$$v = (-1)^s M 2^E$$

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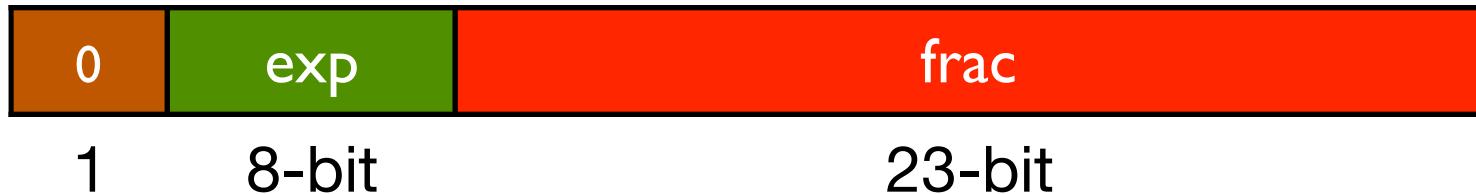


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Today: Floating Point

- Background: Fractional binary numbers and fixed-point
- Floating point representation
- IEEE 754 standard
- Rounding, addition, multiplication
- Floating point in C
- Summary

Floating Point Computations

- The problem: Computing on floating point numbers might produce a result that can't be precisely represented
- Basic idea
 - We perform the operation & produce the infinitely **precise** result
 - Make it fit into desired precision
 - Possibly **overflow** if exponent too large
 - Possibly **round** to fit into frac

Rounding Modes (Decimal)

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Rounding Mode	1.40	1.60	1.50	2.50	-1.50
Towards zero	1	1	1	2	-1
Round down ($-\infty$)	1	1	1	2	-2
Round up ($+\infty$)	2	2	2	3	-1
Nearest even (default)	1	2	2	2	-2

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Precise Value	Rounded Value	Notes
1.000 ⁰¹¹	1.000	1.000 is the nearest (down)
1.000 ¹¹⁰	1.001	1.001 is the nearest (up)
1.000 ¹⁰⁰	1.000	1.000 is the nearest even (down)
1.001 ¹⁰⁰	1.010	1.010 is the nearest even (up)

Rounding Modes (Binary Example)

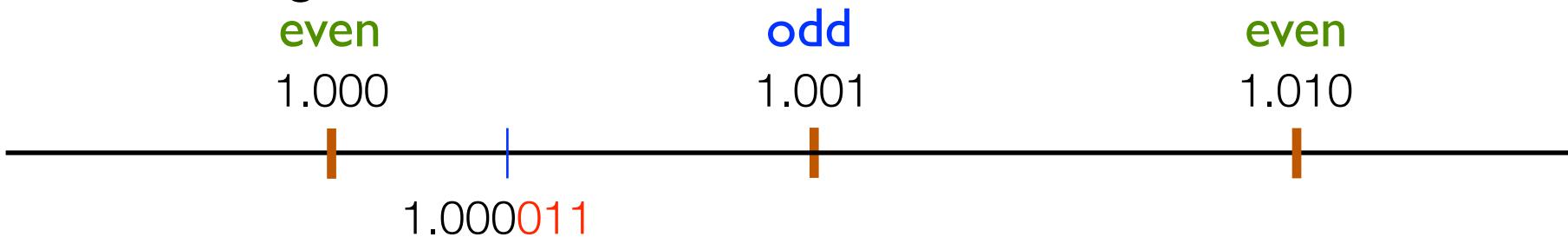
- Default: To nearest; if equally near, then to the one having an even least significant digit (bit)
- Assuming 3 bits for *frac*



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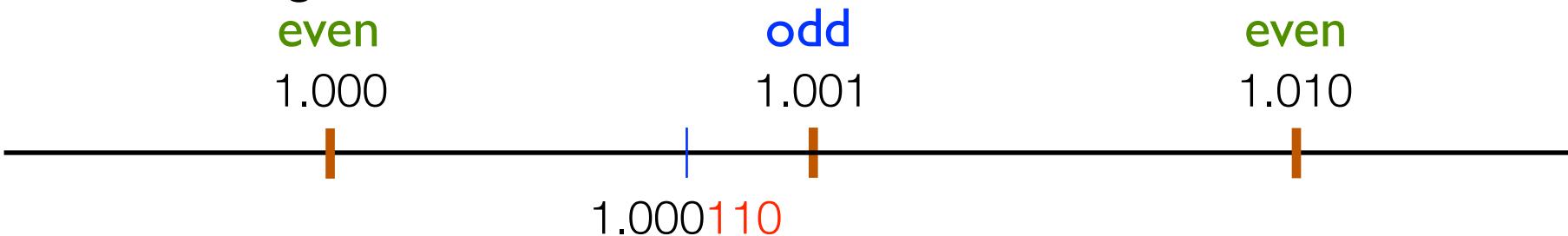
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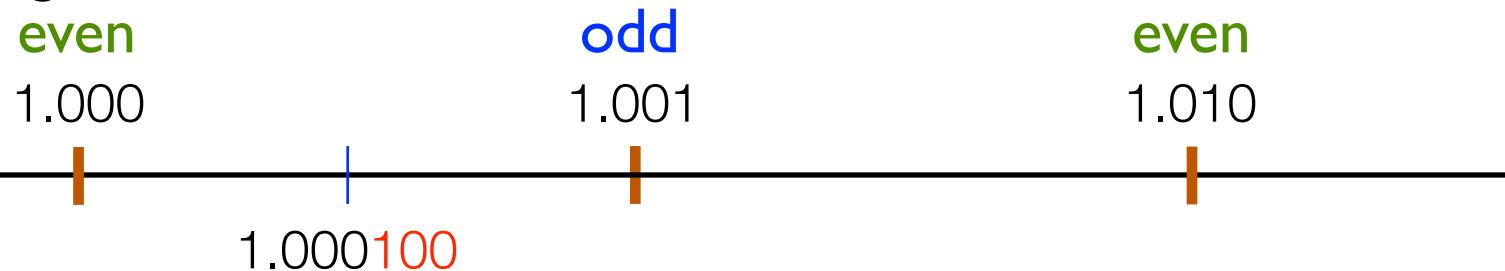


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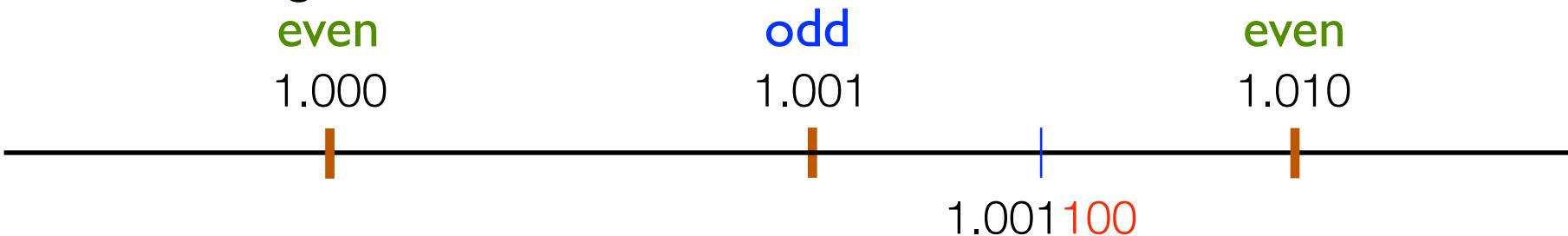
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Floating Point Addition

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$$\bullet (-1)^{s_1} M_1 2^{E_1} + (-1)^{s_2} M_2 2^{E_2}$$

$$1.000 \times 2^{-1} + 11.10 \times 2^{-3}$$

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add

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- If $M < 1$, shift M left k positions, decrement E by k
- Overflow if E out of range
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- Implementation
 - Biggest chore is multiplying significands

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Today: Floating Point

- Background: Fractional binary numbers and fixed-point
- Floating point representation
- IEEE 754 standard
- Rounding, addition, multiplication
- **Floating point in C**
- Summary

Floating Point in C

64-bit Machine

Fixed point
(implicit binary point) {

SP floating point

DP floating point

C Data Type	Bits	Max Value	Max Value (Decimal)
char	8	$2^7 - 1$	127
short	16	$2^{15} - 1$	32767
int	32	$2^{31} - 1$	2147483647
long	64	$2^{31} - 1$	$\sim 9.2 \times 10^{18}$
float	32	$(2 - 2^{-23}) \times 2^{127}$	$\sim 3.4 \times 10^{38}$
double	64	$(2 - 2^{-52}) \times 2^{1023}$	$\sim 1.8 \times 10^{308}$

Floating Point in C

- C Guarantees Two Levels

- `float` single precision
 - `double` double precision

- Conversions/Casting

- Casting between `int`, `float`, and `double` changes bit representation
 - **`double/float → int`**
 - Truncates fractional part
 - Like rounding toward zero
 - Not defined when out of range or NaN: Generally sets to TMin
 - **`int → double`**
 - Exact conversion (as long as int has \leq 53 bit word size, which is the case in both 32-bit and 64-bit machines where `int` is 32 bits)
 - **`int → float`**
 - Not always exact. Will round according to rounding mode