CSC 252: Computer Organization Spring 2020: Lecture 1

Instructor: Yuhao Zhu

Department of Computer Science University of Rochester

Action Items:

- Get CSUG account
- Sign up for Blackboard







Snake circa 2000





Snake circa 2000

Snake circa 2020

Computers are More Capable





Snake circa 2000 Snake circa 2020

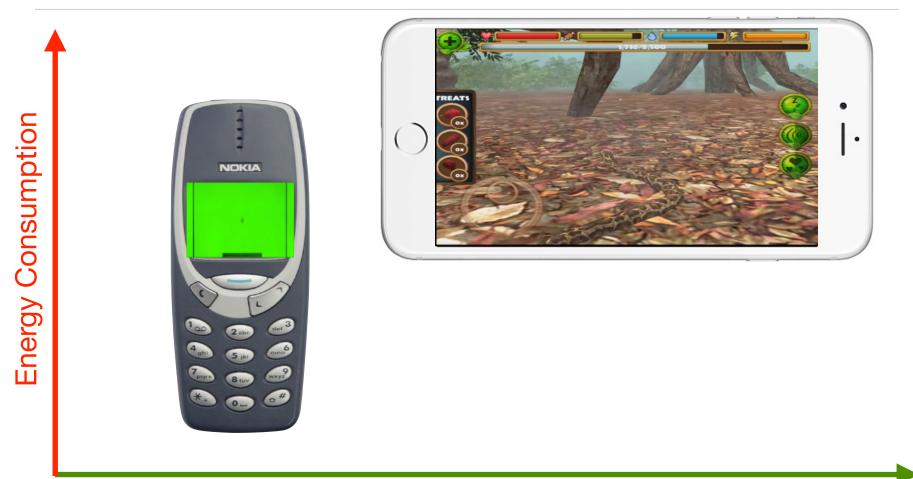
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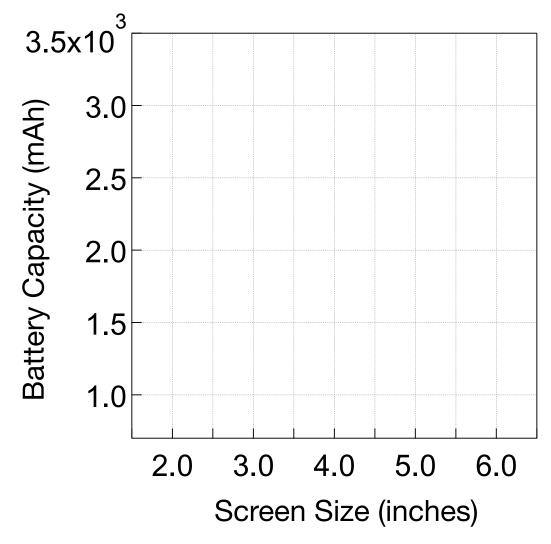


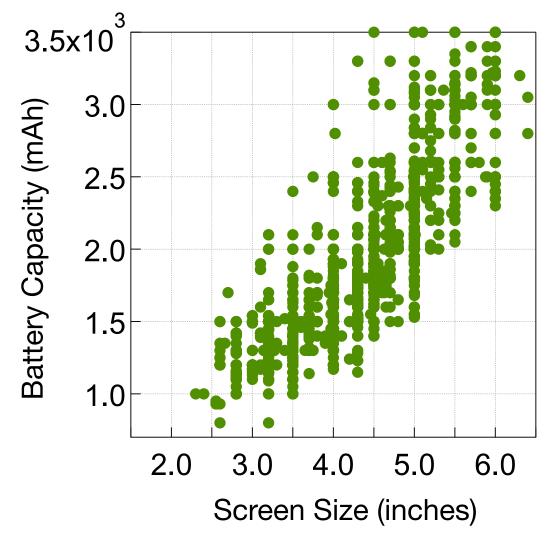
Performance

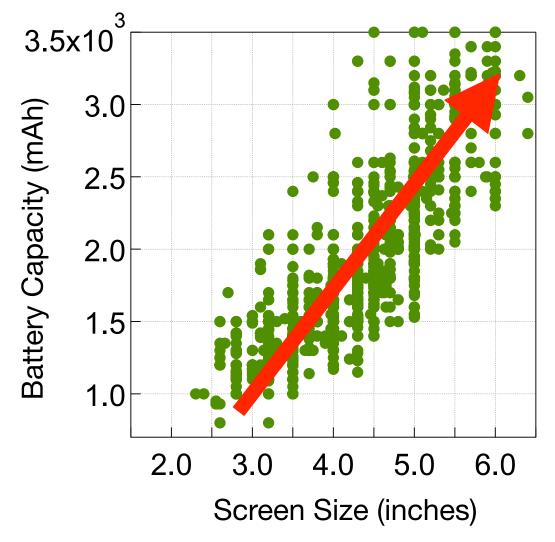
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Performance







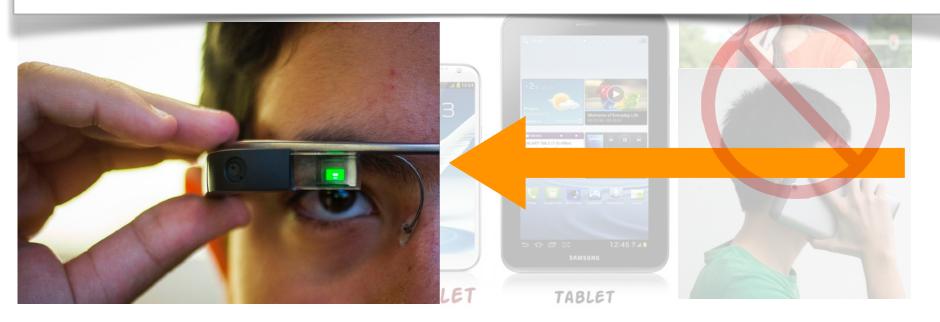








How to build ever-more capable computers but with lower energy consumption and smaller form factor?



Outline: Class Introduction

- Introduction
 - What Are You Supposed to Learn in this Class?
 - What Is Computer Organization Anyways?
 - Instructor & TAs
 - What Do I Expect From You?
 - How am I Going to Teach?
 - Grading, Policies
- Action items:
 - Get a CSUG account.
 - cycle1.csug.rochester.edu (or cycle2, cycle3)
 - If you don't already have one, go to this link: <u>https://</u> accounts.csug.rochester.edu/
 - Sign up for Blackboard (https://learn.rochester.edu/)

Where to Find Stuff

- http://cs.rochester.edu/courses/252/spring2020/
 - General info
 - Programming assignments details
 - Slides
 - Practice problems, past exams
- CSUG machines for programming assignments submissions



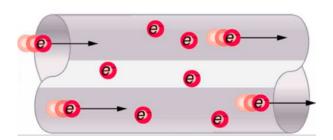
Problem

Who scores the highest on the exam?



Problem

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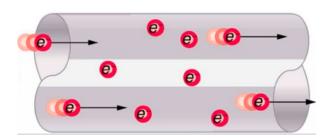
Circuit

Electrons, Resistors, Capacitors, etc.



Problem

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Circuit

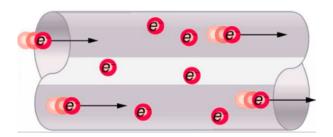
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Quicksort



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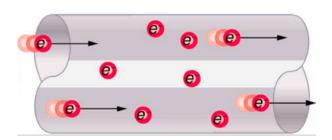


Problem

Who scores the highest on the exam?

Algorithm

Quicksort



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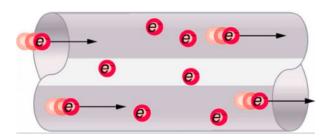
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Algorithm

Who scores the highest on the exam?

Quicksort

Human-readable language (Java, C)



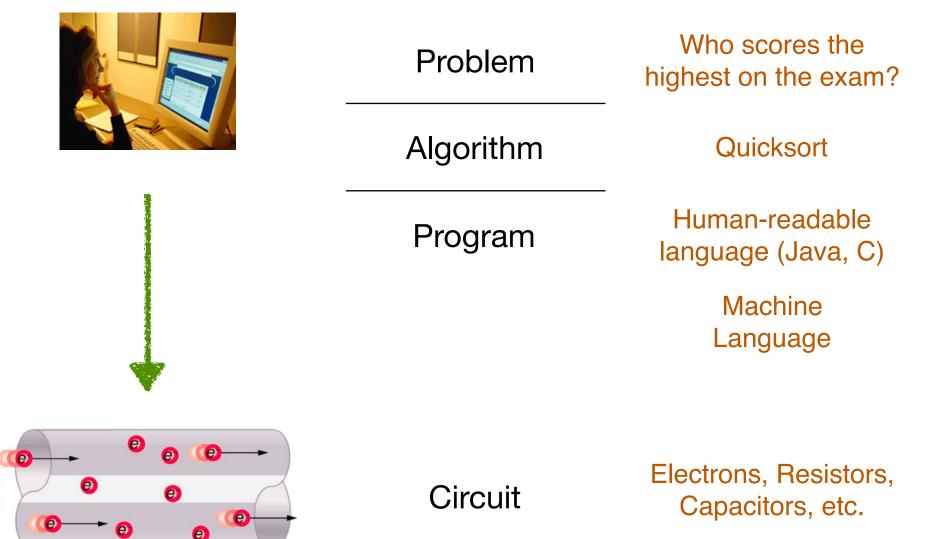
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Problem	Who scores the highest on the exam?
Algorithm	Quicksort
Program	Human-readable language (Java, C)
Circuit	Electrons, Resistors, Capacitors, etc.





Problem

Algorithm

Program

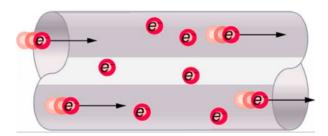
Instruction Set Architecture highest on the exam?

Who scores the

Quicksort

Human-readable language (Java, C)

> Machine Language



Circuit

Electrons, Resistors, Capacitors, etc.



Problem

Algorithm

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Instruction Set Architecture Who scores the highest on the exam?

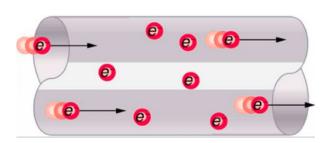
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Human-readable language (Java, C)

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Hardware Design

Electrons, Resistors, Capacitors, etc.



Circuit



e

e

Problem

Algorithm

Program

Instruction Set Architecture

Microarchitecture

Circuit

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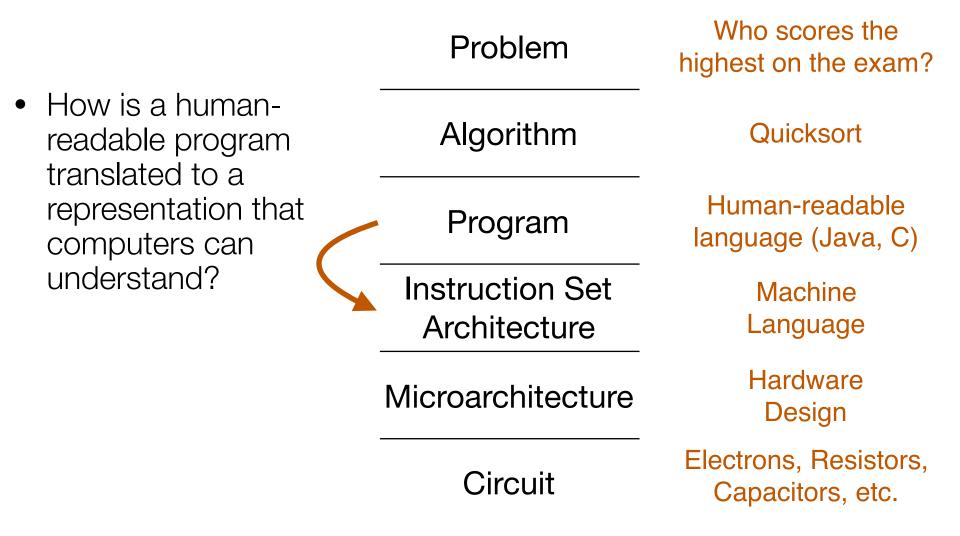
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Computer Systems Mar Requirements to Hardw		7
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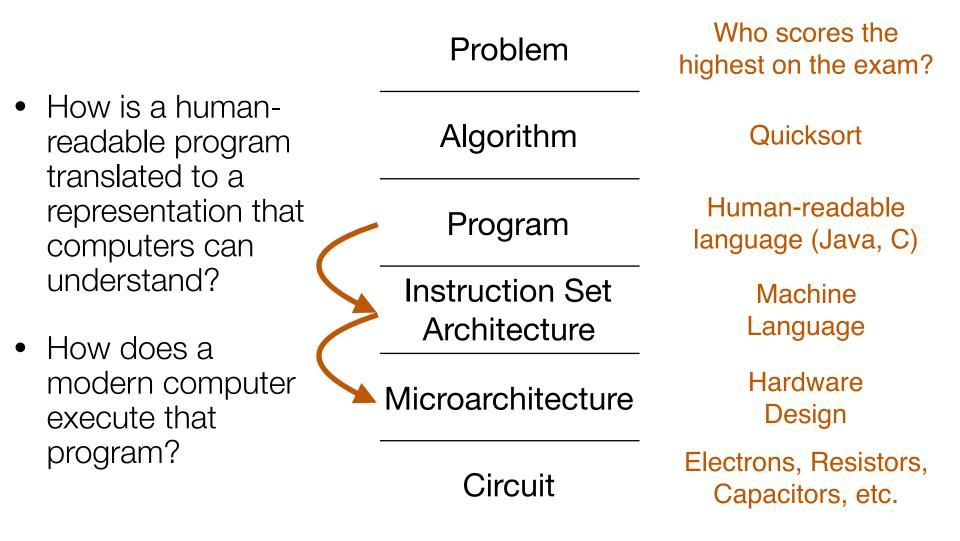
Two Fundamental Aspects of Computer Systems

Who scores the Problem highest on the exam? Algorithm Quicksort Human-readable Program language (Java, C) Instruction Set Machine Architecture Language Hardware Microarchitecture Design Electrons, Resistors, Circuit Capacitors, etc.

Two Fundamental Aspects of Computer Systems



Two Fundamental Aspects of Computer Systems



The "Translation" Process, a.k.a., Compilation

C Program

void add() {
 int a = 1;
 int b = 2;
 int c = a + b;
}

Pre-processor Compiler

Assembly program

movl \$1, -4(%rbp) movl \$2, -8(%rbp) movl -4(%rbp), %eax addl -8(%rbp), %eax

The "Translation" Process, a.k.a., Compilation

Assembly program

movl \$1, -4(%rbp) movl \$2, -8(%rbp) movl -4(%rbp), %eax addl -8(%rbp), %eax Assembler Linker



Executable Binary

- 00011001 ...
- 01101010 ...
- 11010101 ...
- 01110001 ...

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·····

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Executable Binary

01110001 ...

- It translates a text file to an executable binary file (a.k.a., executable) consisting of a sequence of instructions
- Why binary? Computers understand only 0s and 1s
 - The subject of next lecture

Back to Layers of Transformation...

Who scores the Problem highest on the exam? How is a human-Algorithm Quicksort readable program translated to a Human-readable representation Program language (Java, C) that computers can understand? Instruction Set Machine **Architecture** Language How does a modern computer Hardware Microarchitecture execute that Design program? Electrons, Resistors, Circuit Capacitors, etc.

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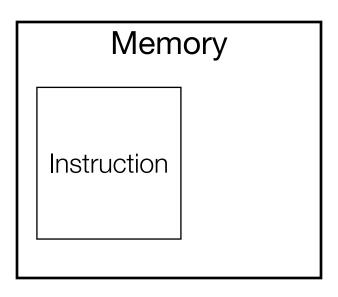
- Executables (i.e., instructions) are stored in "memory"
- Processors read instructions from memory and execute instructions one after another

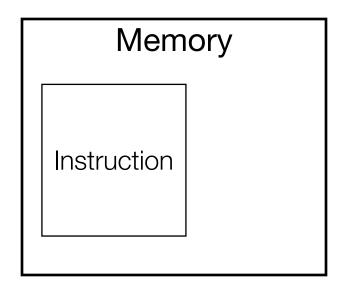
Assembly program: add.s

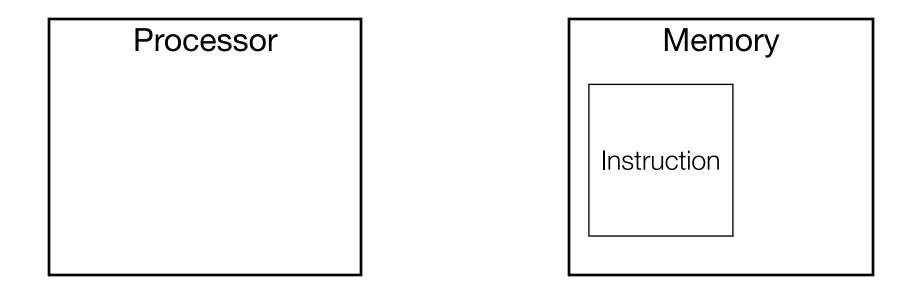
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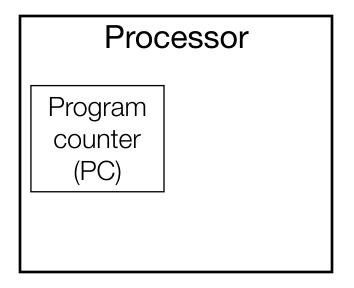
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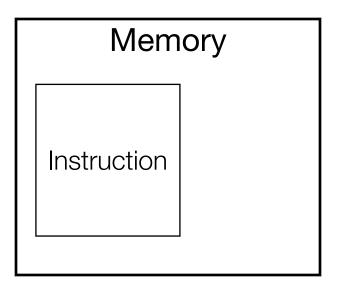
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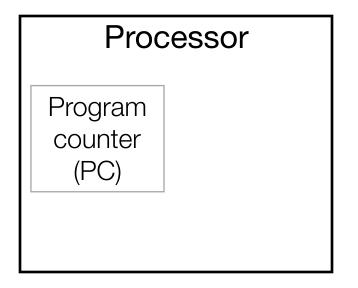


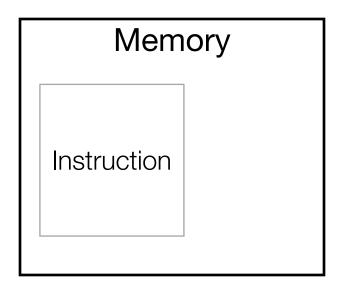


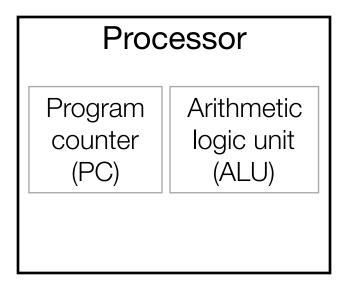


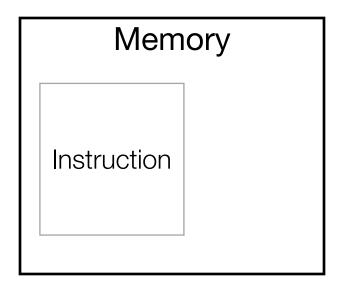


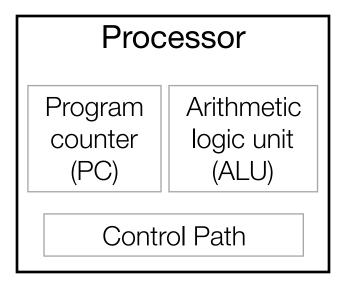


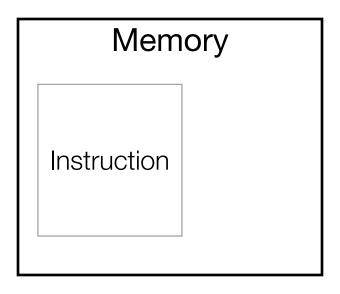


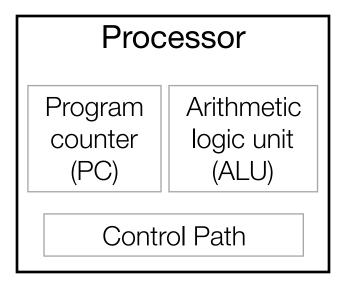


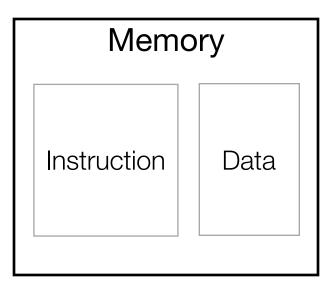


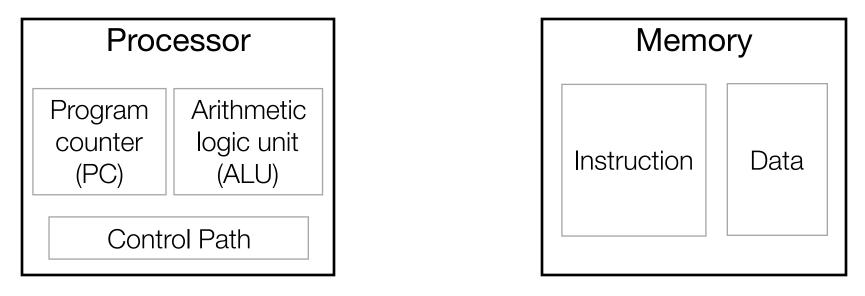


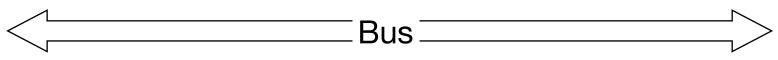




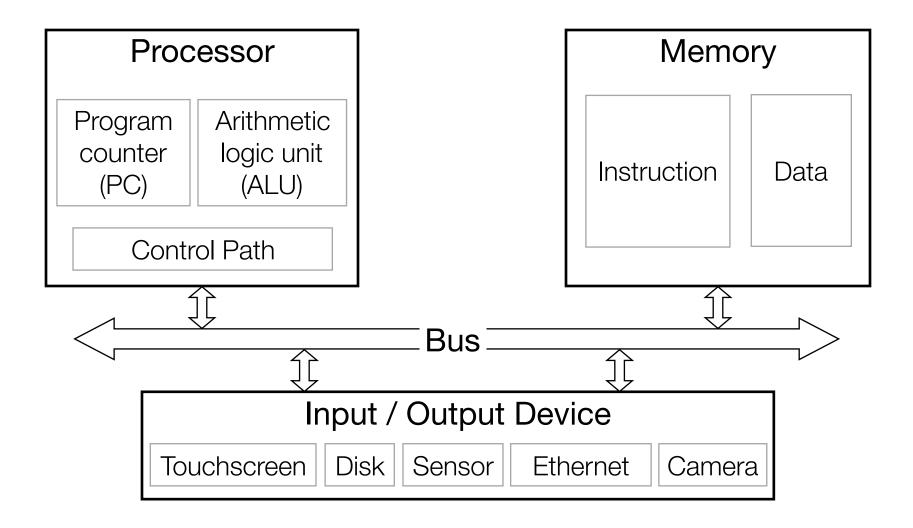


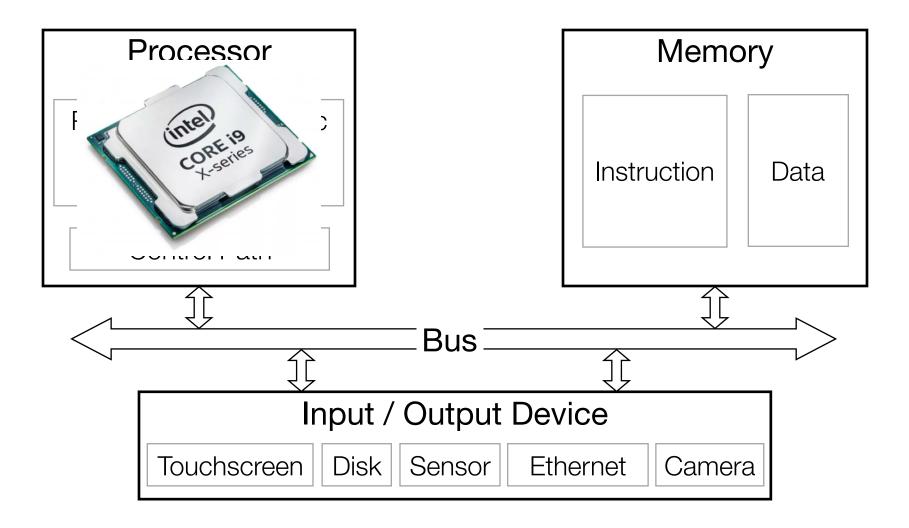


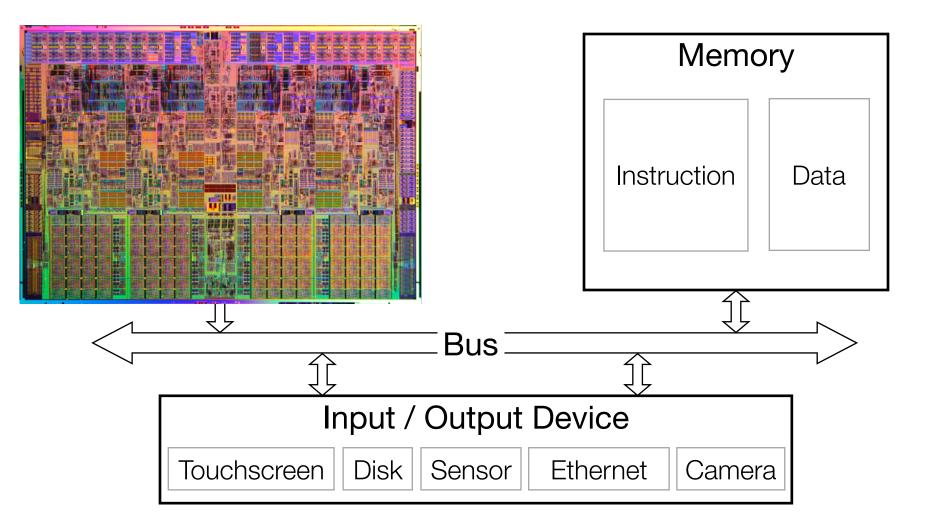


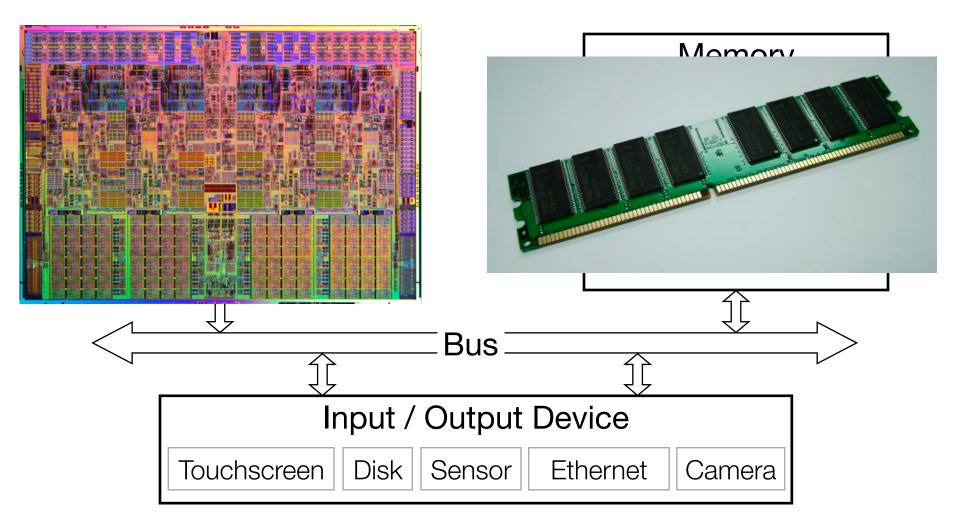


Input / Output Device				
Touchscreen	Disk	Sensor	Ethernet	Camera







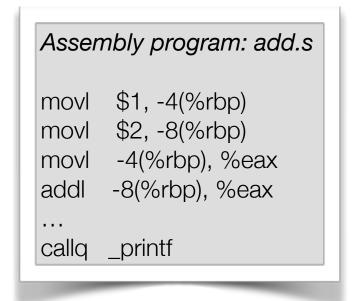


Back to Layers of Transformation...

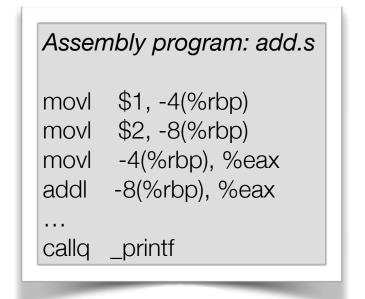
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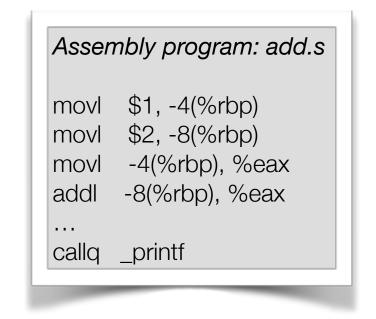
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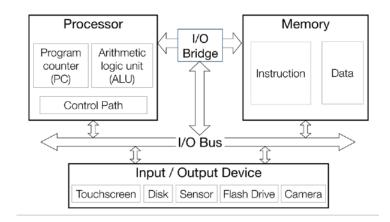


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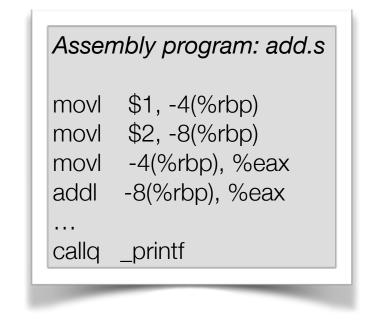


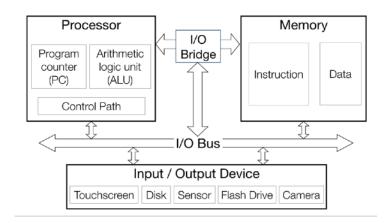
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- Implementation of an ISA is called *microarchitecture*
- ISAs *abstract* away details of microarchitecture





Abstraction

• Think of car versus engine, transmission, brakes, ...

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Abstraction

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- Leaving out one or more properties of a complex object so as to focus on others
 - ISA leaves out *how* "ADD" is implemented
 - ISA also leaves our *how long* an "ADD" instruction takes
- Bad abstractions throw away essential features of problem
 - Topologist is someone who does not know the difference between a doughnut and coffee-cup
 - Bad ISAs don't tell you the hardware can do multiplication

Every Layer in CS is an Abstraction

Who scores the Problem highest on the exam? Algorithm Quicksort Human-readable Program language (Java, C) Instruction Set Machine **Architecture** Language Hardware Microarchitecture Design Electrons, Resistors, Circuit Capacitors, etc.

Every Layer in CS is an Abstraction

Who scores the Depend on which Problem highest on the exam? layer you want to live at, you have Algorithm Quicksort different views of the computer Human-readable Program language (Java, C) Instruction Set Machine Architecture Language Hardware Microarchitecture Design Electrons, Resistors, Circuit Capacitors, etc.

Every Layer in CS is an Abstraction

- Depend on which layer you want to live at, you have different views of the computer
- This course expands your layers of abstractions

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Algorithm	Quicksort	
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Instruction Set Architecture	Machine Language	
Microarchitecture	Hardware Design	
Circuit	Electrons, Resistors, Capacitors, etc.	

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 - x86, ARM, Power/PowerPC, Sparc, MIPS, IA64, z
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 - Intel and AMD have different microarchitectures for x86

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 - Apple/Samsung/Qualcomm have their own microarchitecture (implementation) of the ARM ISA
 - Intel and AMD have different microarchitectures for x86
- ISA is lucrative business: ARM's Business Model
 - Patent the ISA, and then license the ISA
 - Every implementer pays a royalty to ARM
 - Apple/Samsung pays ARM whenever they sell a smartphone

- Little research on ISA, much more microarch. research
 - ISA is stable now. "One ISA rules them all."
 - Free, open ISA: RISC V (UC Berkeley, https://riscv.org/)



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 - E.g., Transmeta executes x86 ISA programs on their own ISA

Problem

Algorithm

Program

Instruction Set Architecture

Microarchitecture

Circuit

Look Up (Nature of the problems)

Problem

Algorithm

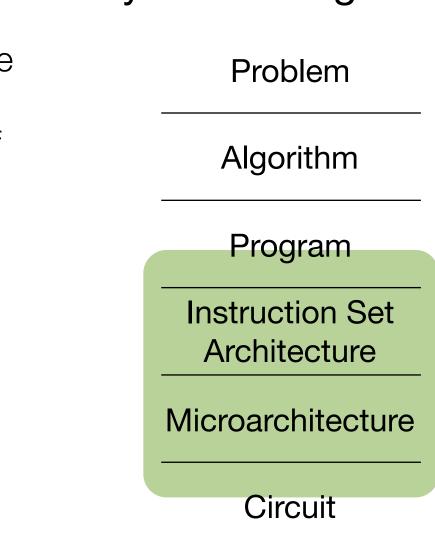
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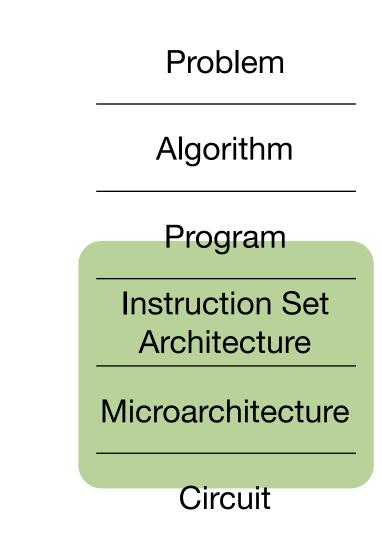
Circuit

- Look Up (Nature of the problems)
- Look Down (Nature of the circuit technology and physics)

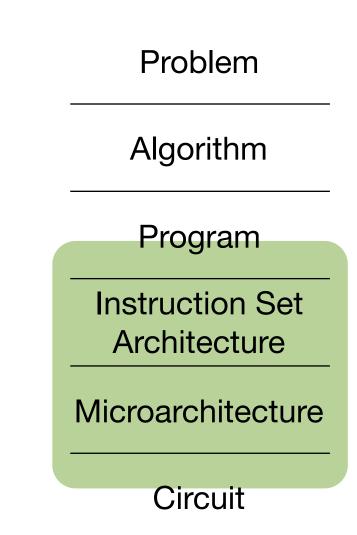


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- Look Backward

 (Evaluating old ideas in light of new technologies)

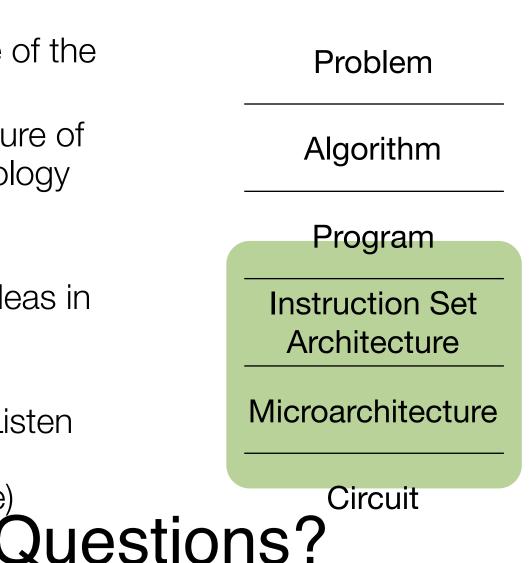


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- Look Forward (Listen to dreamers and predict the future)



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 - WH 3501, yzhu@rochester.edu
 - Office hours Friday 11am -12pm (not this week. Sorry!)
 - Got a good education
 - Got some industry experience
 - Interested in computer systems for Augmented/Virtual Reality, Computational Phototography, etc.

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 - Really care about you learning the material and succeeding

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Coming to office hours does NOT mean you are weak!

A Word about Lectures and Medieval Times

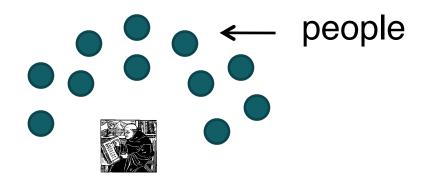
- Lecture: It's a large part of what you pay for
- But why do we have the "lecture" format?
 - Why does someone stand at the front and tell you things?
 - Why do you take "notes" on what they say?

All The Way Back to Medieval Times..



All The Way Back to Medieval Times..



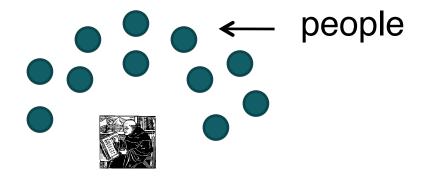


All The Way Back to Medieval Times..



Lecture Halls





Modern Times

- You don't have to trust the monk!
 - The printing press: a revolutionizing development
 - The web: order your knowledge up for yourself on Amazon!
- Read books and analyze for YOURSELF!
 - If I rephrase it for you, what purpose does that serve?



FAQ: "But, wouldn't it be more efficient if you just taught us with the right answer to begin with?"

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- Have you ever heard of a workout class where the instructor did all the exercises while everyone else just watched attentively?
- To learn, you must do the work with your own muscle (brain)!



Textbook

- Required textbook
 - Bryant and O'Hallaron's Computer Systems: A Programmer's Perspective (3rd edition)
 - In the past, students got As without owning the textbook
- Some recommended (but not required) textbooks
 - Introduction to Computing Systems: From Bits and Gates to C and Beyond, 2/e. This is where I learnt Computer Systems.
 - Structured Computer Organization, 6/e. More emphasis on SW.
 - Computer Organization and Design: The Hardware Software Interface, ARM Edition. More emphasis on hardware.

- Programming Assignments: 48%
 - 6 assignments, 8% each

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- 1 midterm exam, 22%
- 1 comprehensive final exam, 30%

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 - Thinking/design time
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- You could work in pairs
 - Only 1 submission per pair

- Check syllabus and labs to figure out when they are due (there is a date and a time specified)
- They take time, so start early!
 - Thinking/design time
 - Programming time
 - Test design + debug (and repeat)
- 3 slip days. Use it wisely!
 - Other than slip days, late submission counts 0 point
- You could work in pairs
 - Only 1 submission per pair
- Share ideas but not artifacts (e.g., code, sketch)

Programming Environment

- Develop code (or at least test it) on the CSUG Linux boxes (csug.rochester.edu)
 - Microsoft Visual Studio could be nice, but it's not what we use
 - The lack of Unix knowledge is a major problem according to our industry contacts
- Projects will be mostly in C and x86 assembly.
- We only accept ANSI-C that can be compiled by the default GCC on the CSUG Linux boxes

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 - Midterm covers everything up until the second last lecture
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Loved how the final exam was set up. Actually made me THINK.

Programming Assignments and Exams

The assignments were often very different from what we were learning in the course, causing a lot of frustration in the beginning. People had to rely on outside sources to get a grounding of how to do an assignment.

The exams are structured to be significantly harder than the examples covered in class which means there is strong need for critical thinking on the spot during exams.

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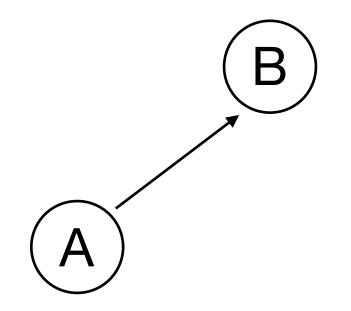
This is a feature, not a bug.

Lectures teach you how to go from A to B

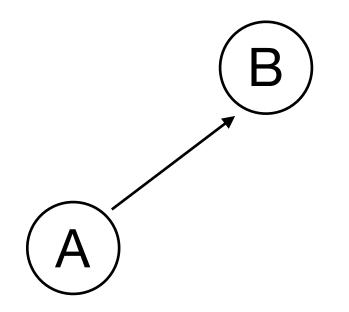
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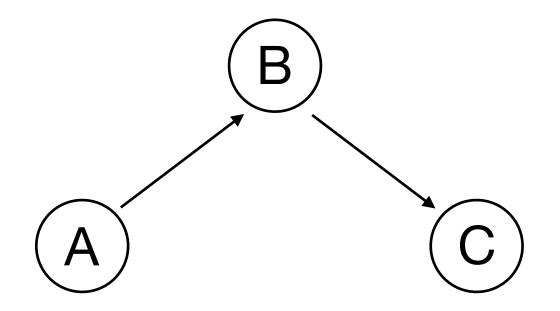
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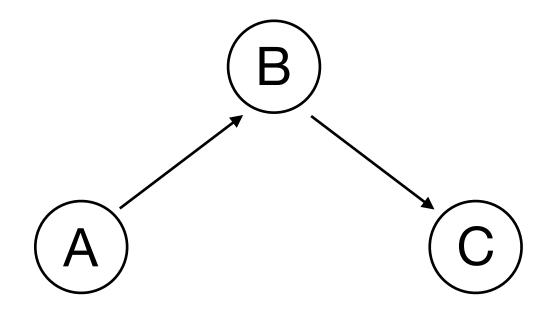
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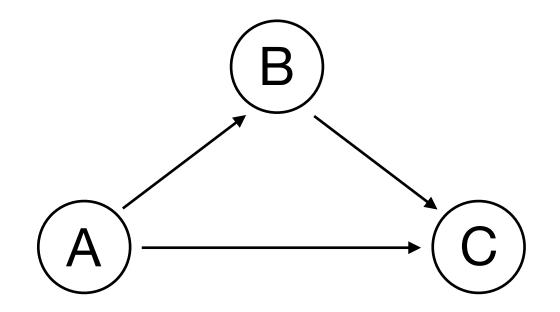
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- Lectures teach you how to go from **A** to **B**
- Projects ask you to go from B to C
- Exams test whether you can go from A to C



- Lectures teach you how to go from **A** to **B**
- Projects ask you to go from B to C
- Exams test whether you can go from **A** to **C**

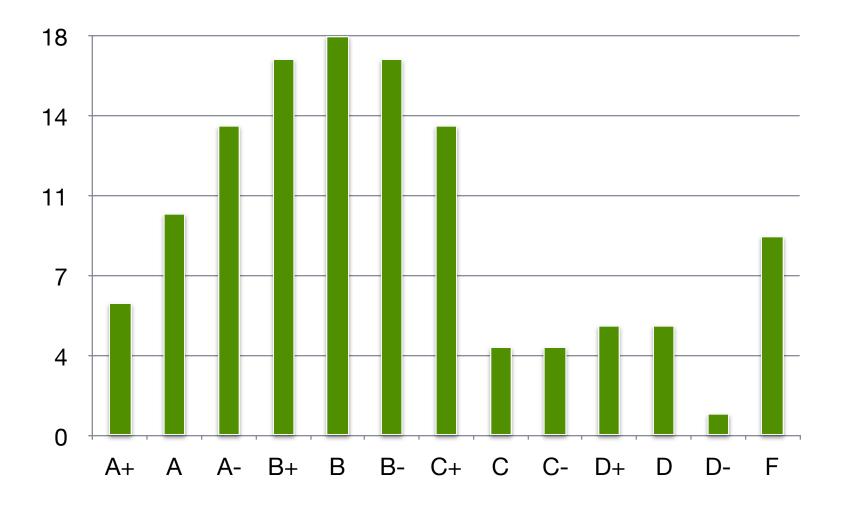


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 - So stop me and ask questions during the lectures.

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- But I *do* care about whether you get it
 - So stop me and ask questions during the lectures.
- I *don't* care about your grade
 - I'd love to give you an A, so give me a reason to do that
 - Think of me and TAs as your friends, not enemies

Final Grades Spring 2018



Final Grades Spring 2019

