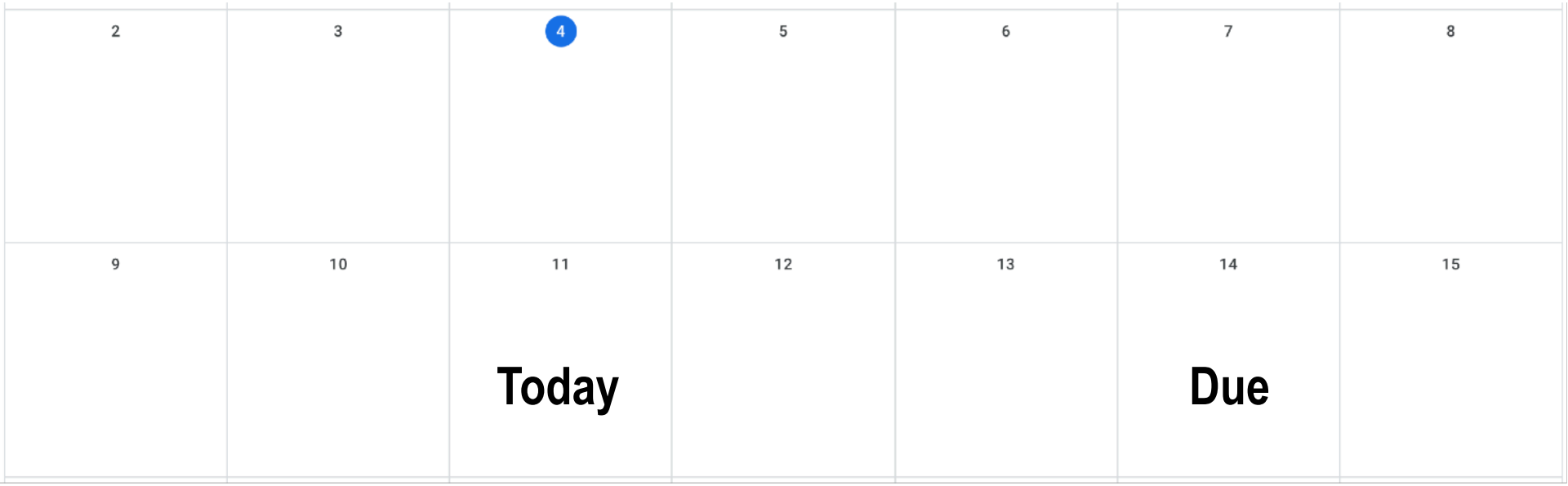
CSC 252: Computer Organization
Spring 2020: Lecture 8

Instructor: Yuhao Zhu

Department of Computer Science
University of Rochester

Announcement

- Programming assignment 2 is DUE SOON
  - Details: https://www.cs.rochester.edu/courses/252/spring2020/labs/assignment2.html
  - Due on **Feb. 14**, 11:59 PM
  - You (may still) have 3 slip days



Announcement

- Read the instructions before getting started!!!
  - You get 1/4 point off for every wrong answer
  - Maxed out at 10
- Request one bomb per group using one person's email and ID. Email Shuang and Sudhanshu who you are working with.

Announcement

- Final exam schedule is posted: May 6, 19:15, WH1400
- Programming assignment 2 is in x86 assembly language. Seek help from TAs.
- TAs are best positioned to answer your questions about programming assignments!!!
- Programming assignments do NOT repeat the lecture materials. They ask you to synthesize what you have learned from the lectures and work out something new.

Announcement

- A problem set for arithmetics: http://www.cs.rochester.edu/courses/252/spring2020/handouts.html
- Not to be turned in
- Form study groups

Implementing Switch Using Jump Table

Switch Form

```
switch(x) {
  case val_0:
    Block 0
  case val_1:
    Block 1

  ....
  case val_n-1:
    Block n–1
}
```

Implementing Switch Using Jump Table

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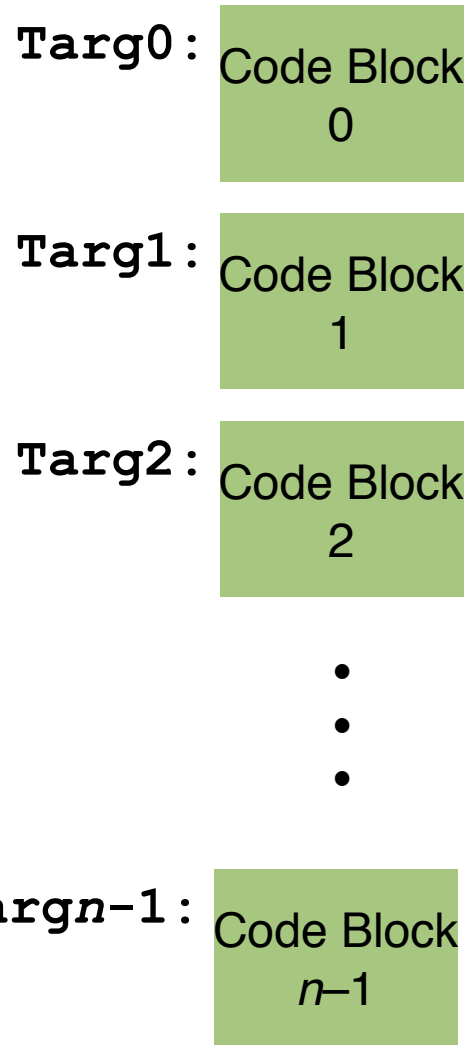
Jump Targets



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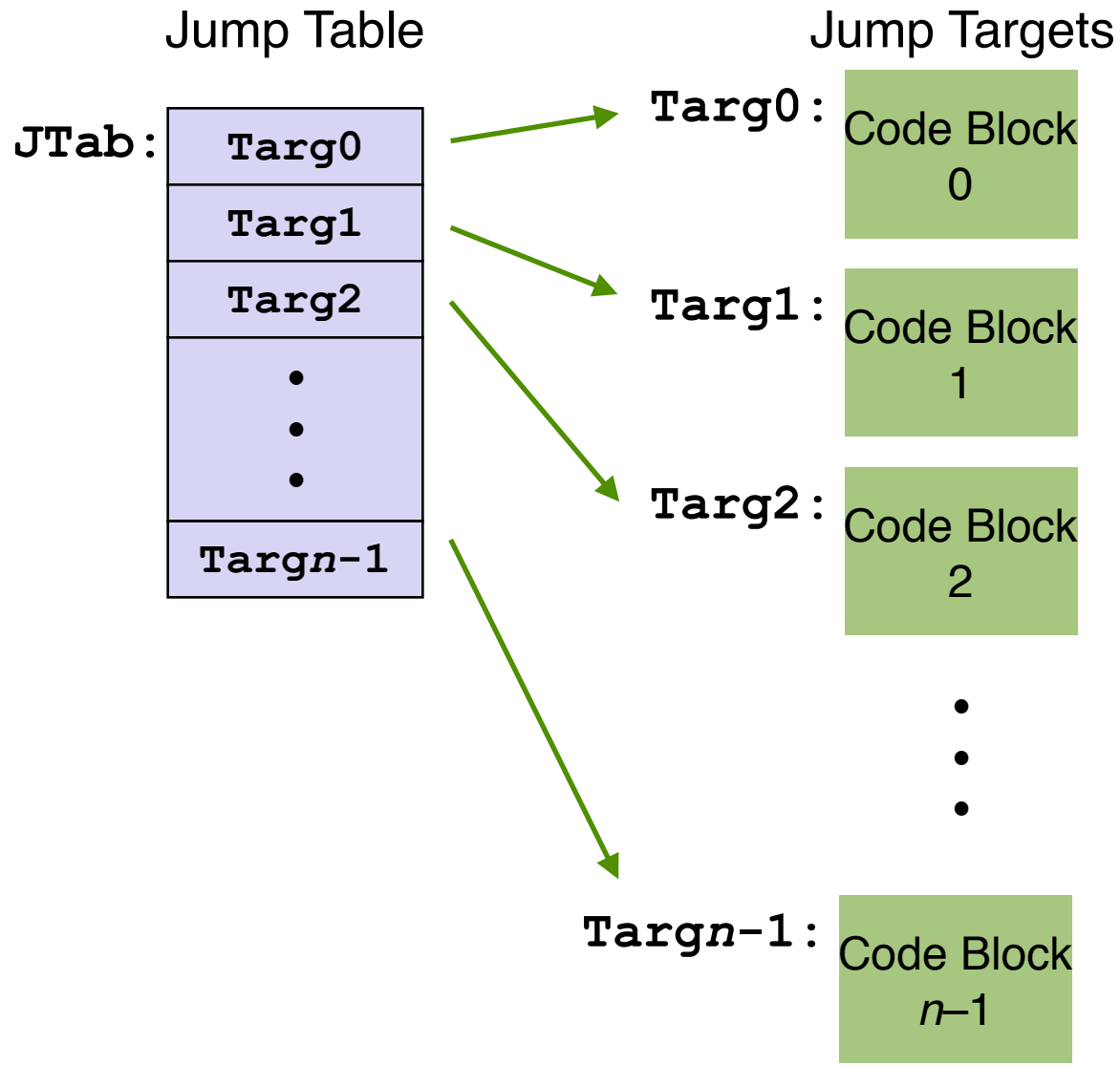

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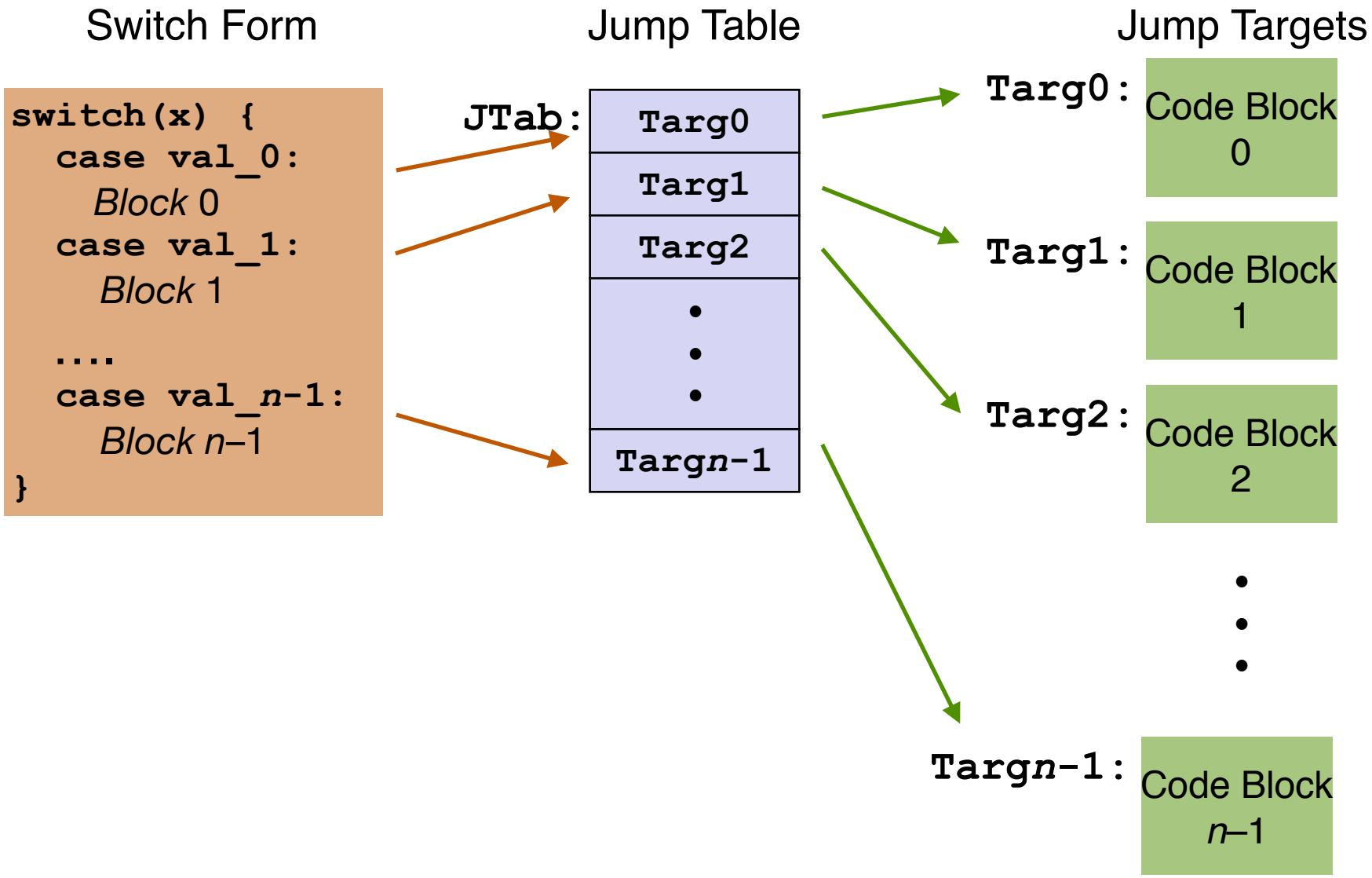
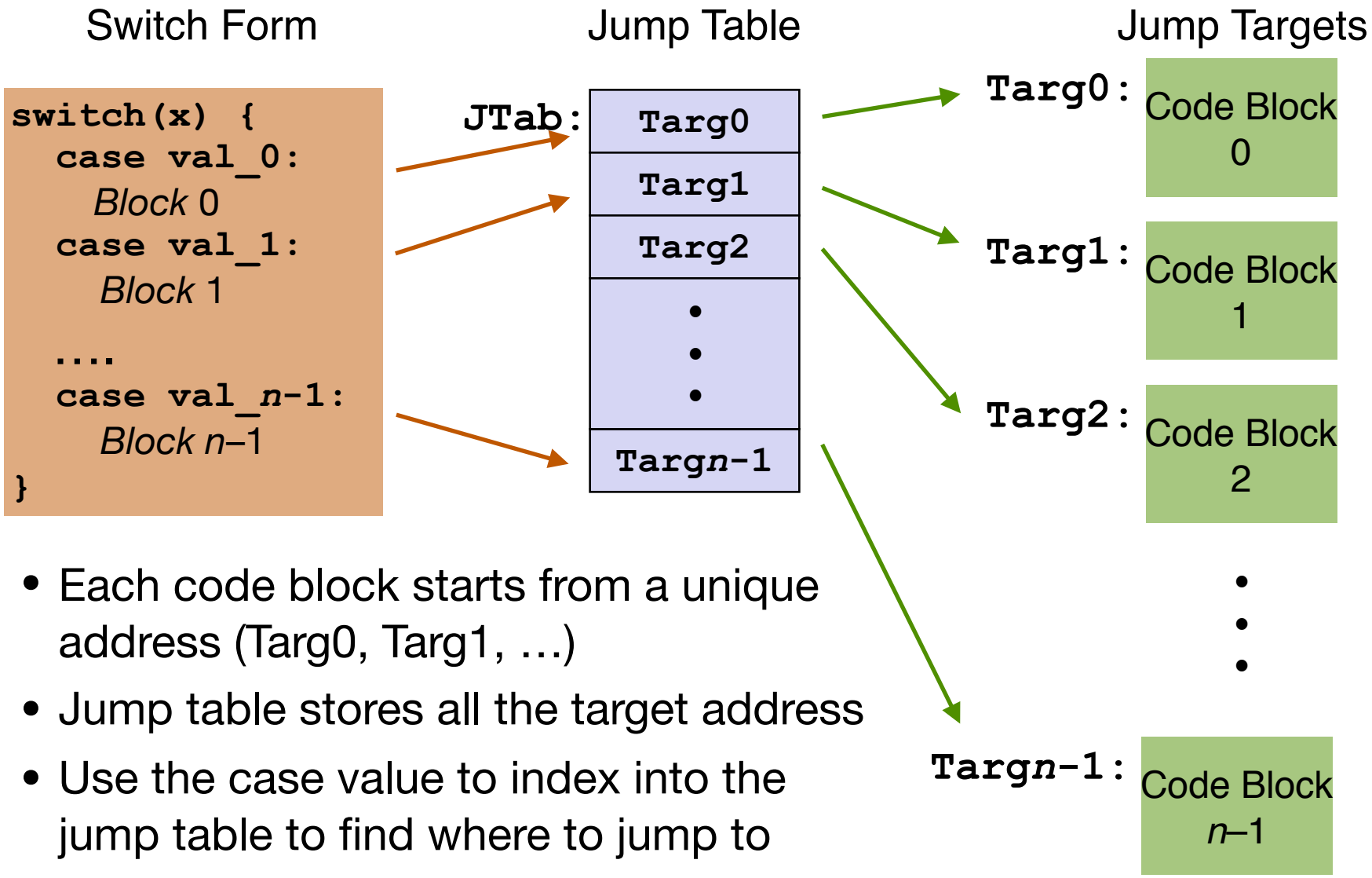


Implementing Switch Using Jump Table



- Each code block starts from a unique address (Targ0, Targ1, …)
- Jump table stores all the target address
- Use the case value to index into the jump table to find where to jump to

Jump Table and Jump Targets

Jump Table

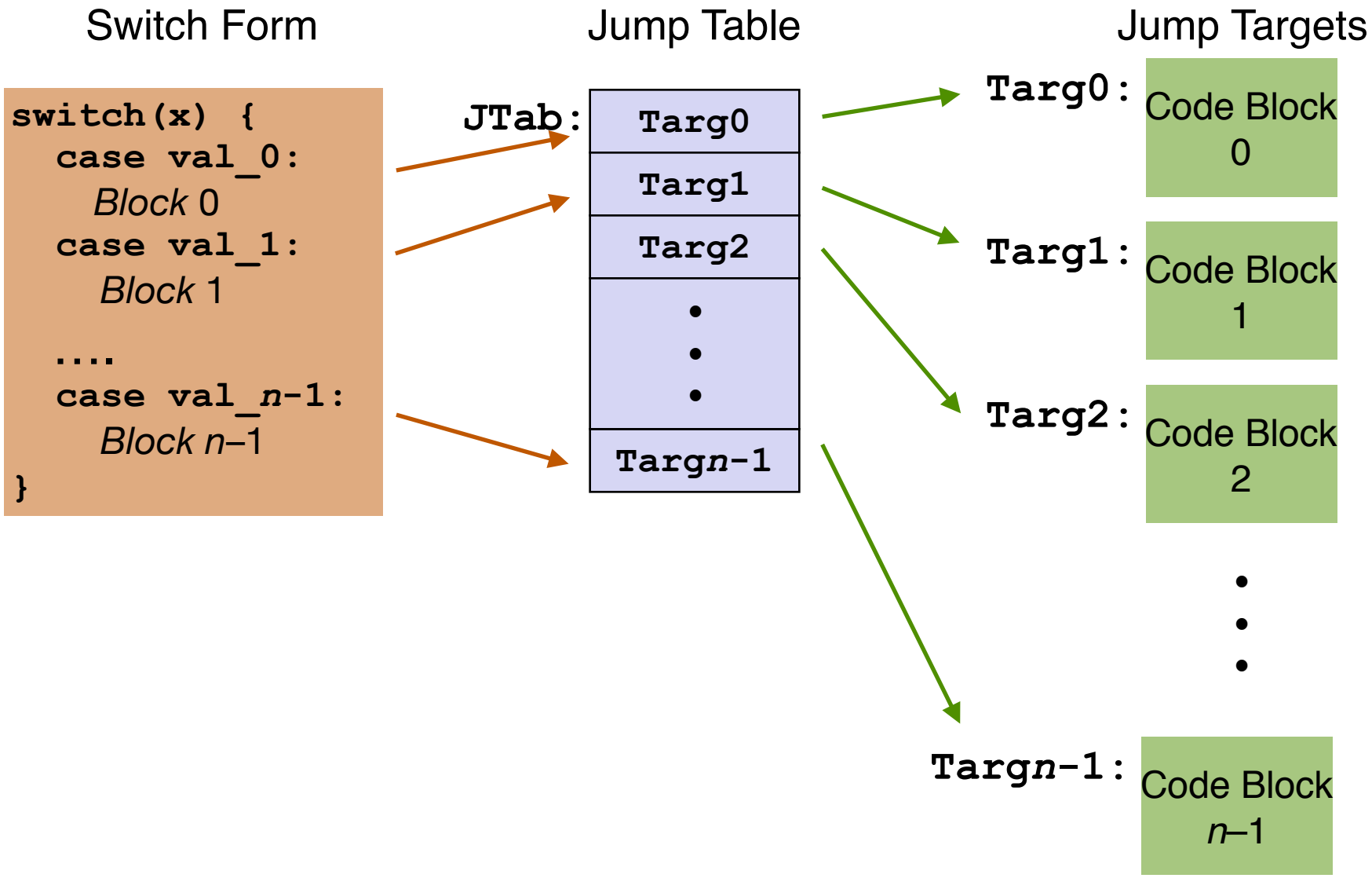
```
.section  .rodata
  .align 8
.L4:
  .quad .LD # x = 0
  .quad .L1 # x = 1
  .quad .L2 # x = 2
  .quad .L3 # x = 3
  .quad .LD # x = 4
  .quad .L5 # x = 5
  .quad .L5 # x = 6
```

`jmp .L3` will go to `.L3` and start executing from there

Jump Targets

```
.L1:                    # Case 1
   movq   %rsi, %rax
   imulq  %rdx, %rax
   jmp    .done
.L2:                    # Case 2
   movq   %rsi, %rax
   cqto
   idivq  %rcx
.L3:                    # Case 3
   addq   %rcx, %rax
   jmp    .done
.L5:                    # Case 5,6
   subq   %rdx, %rax
   jmp    .done
.LD:                    # Default
  movl    $2, %eax
  jmp    .done
```

Implementing Switch Using Jump Table



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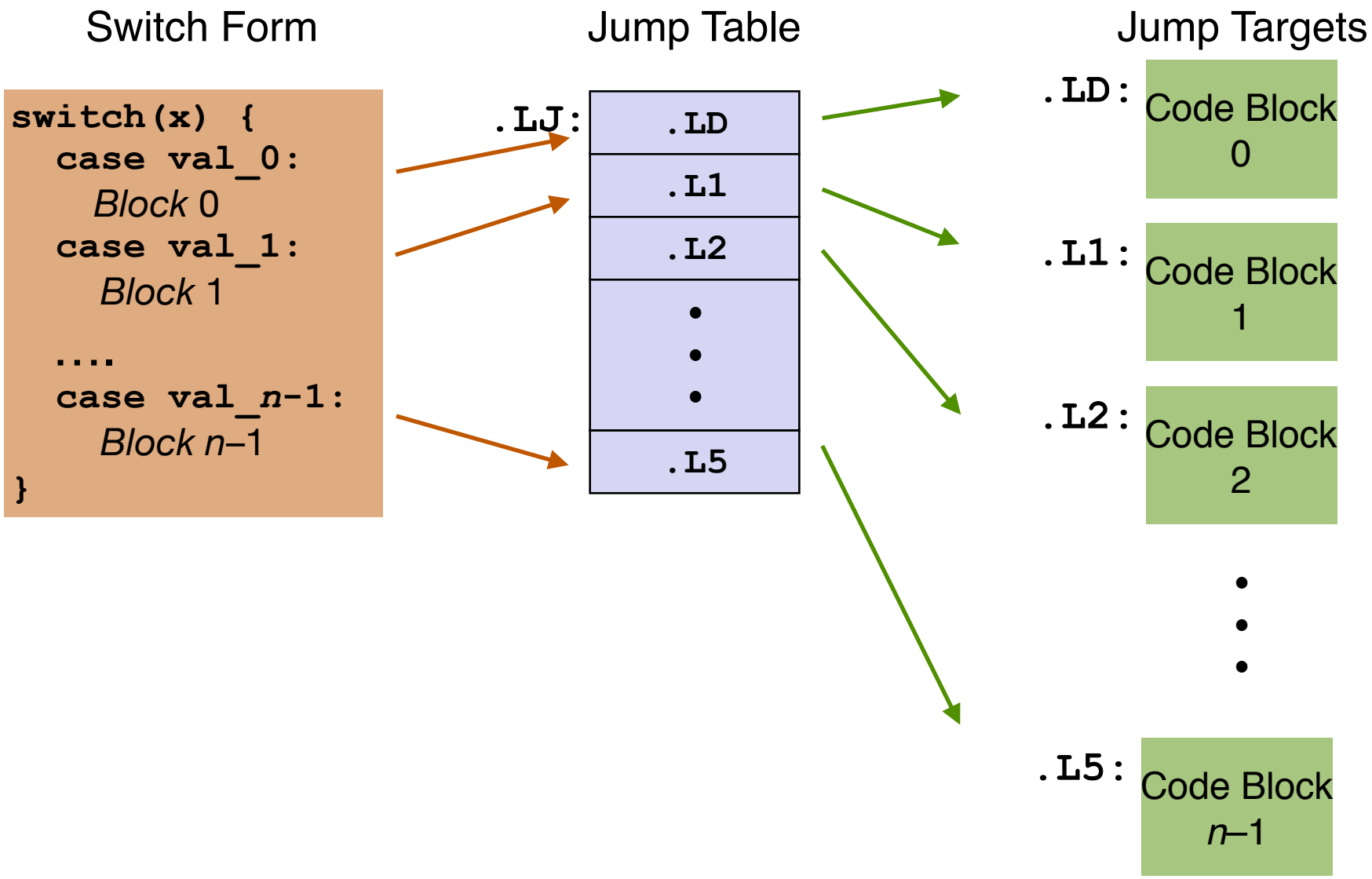
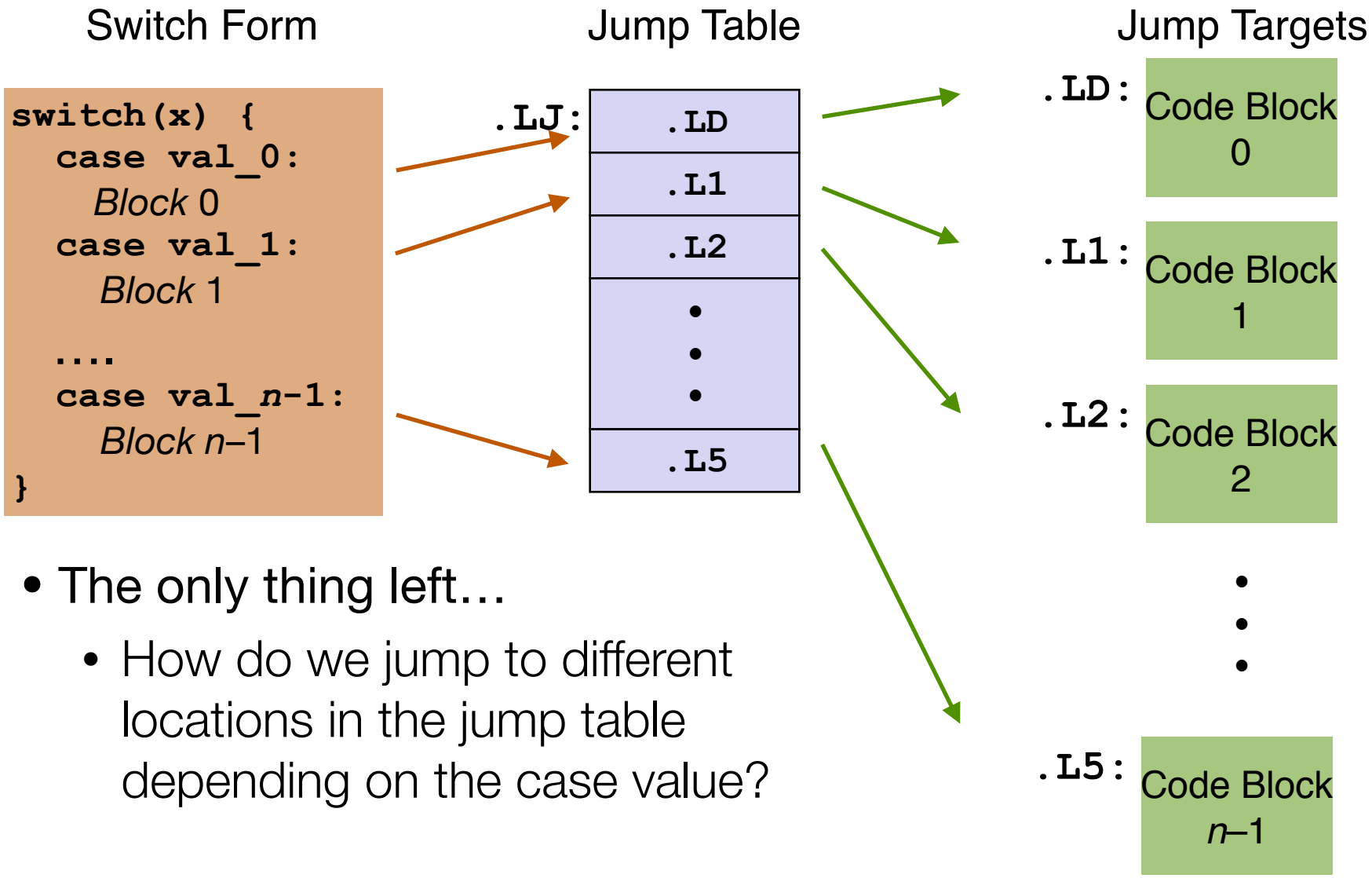


Implementing Switch Using Jump Table



- The only thing left…
  - How do we jump to different locations in the jump table depending on the case value?

Indirect Jump Instruction

```
.section  .rodata
  .align 8
.LJ:
  .quad .LD # x = 0
  .quad .L1 # x = 1
  .quad .L2 # x = 2
  .quad .L3 # x = 3
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Indirect Jump Instruction

The address we want to jump to is stored at `.LJ + 8 * x`

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```

```
# assume x in %rdi
movq  .LJ(,%rdi,8), %rax
jmp   *%rax
```

Indirect Jump Instruction

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- Indirect Jump: **`jmp *%rax`**
  - `%rax` specifies the address to jump to (PC = `%rax`)

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- Direct Jump (**`jmp .LJ`**), directly specifies the jump address

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- Indirect Jump specifies where the jump address is located

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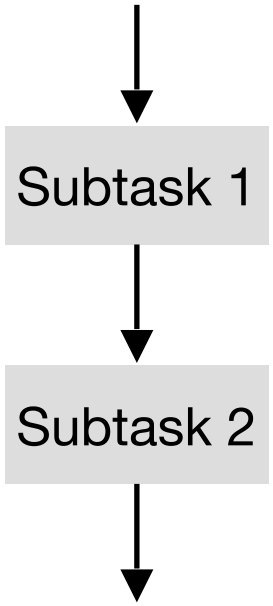
An equivalent syntax in x86:

```
jmp      *.LJ(,%rdi,8)
```

Summary

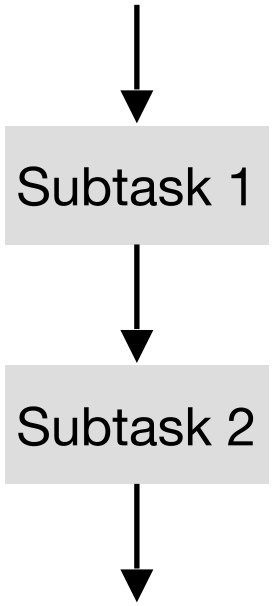
Sequential




```
a = x + y;
y = a - c;
...
```

Summary

Sequential




```
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…
```
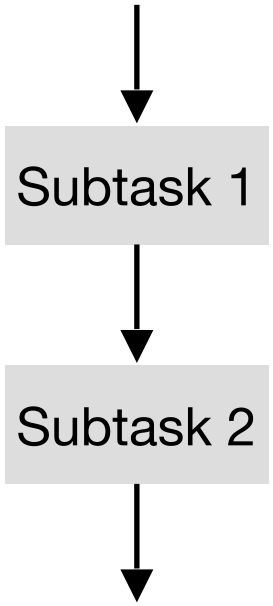

Memory

```
.section .text

…

<subtask 1>

…

…

<subtask 2>

…
```

Summary

Sequential

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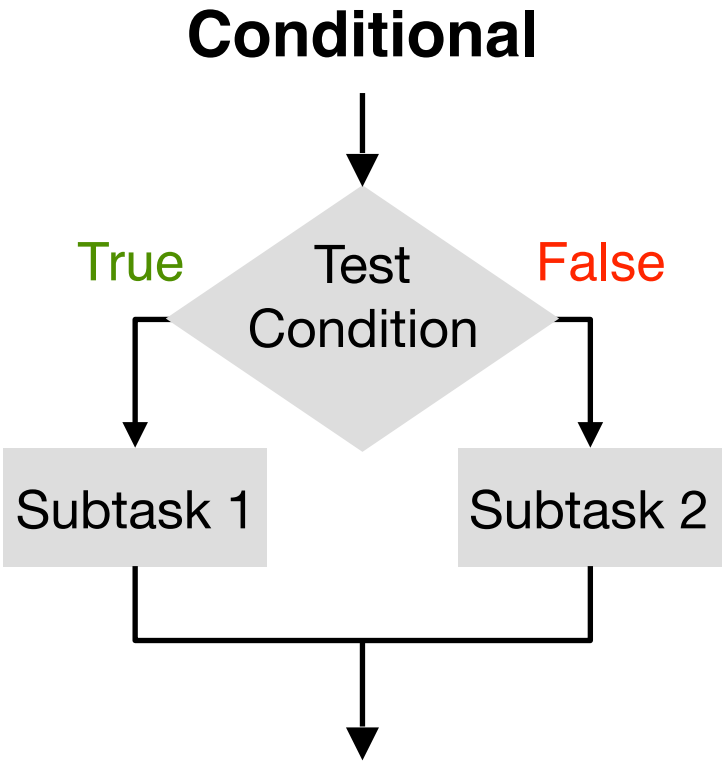
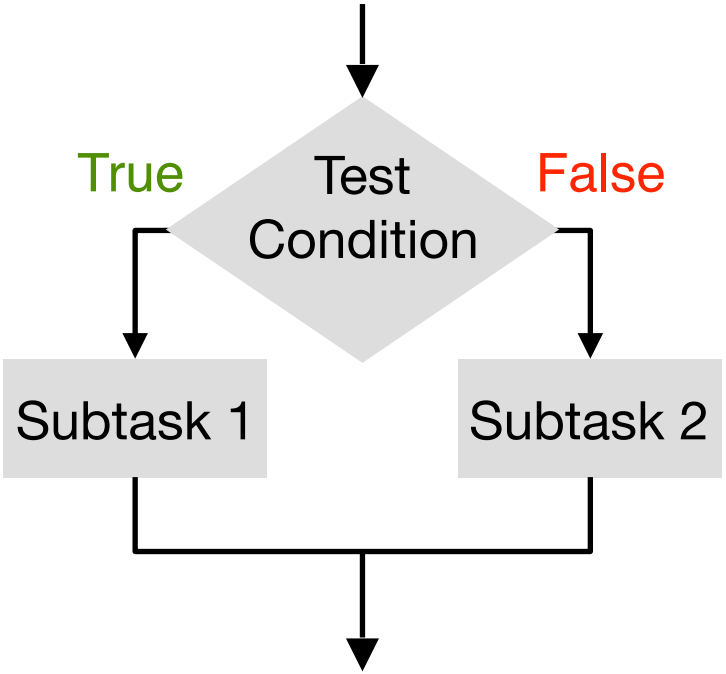



```
if (x > y) r = x - y;
else r = y - x;
```

Summary

Conditional

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if (x > y) r = x - y;
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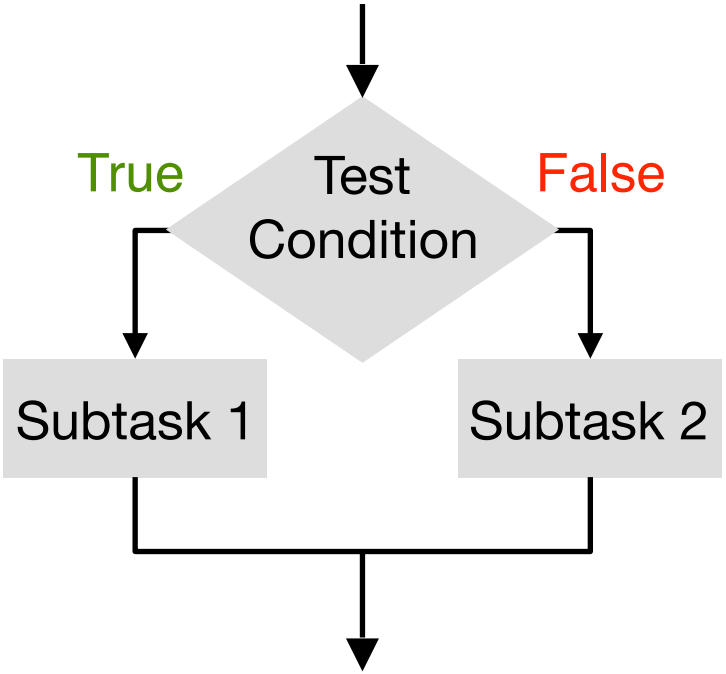
Memory

```
.section .text

…
cmpq
jle .L2
.L1 <subtask 1>

…

jmp .done
.L2 <subtask 2>

…
.done
```

Summary

Conditional

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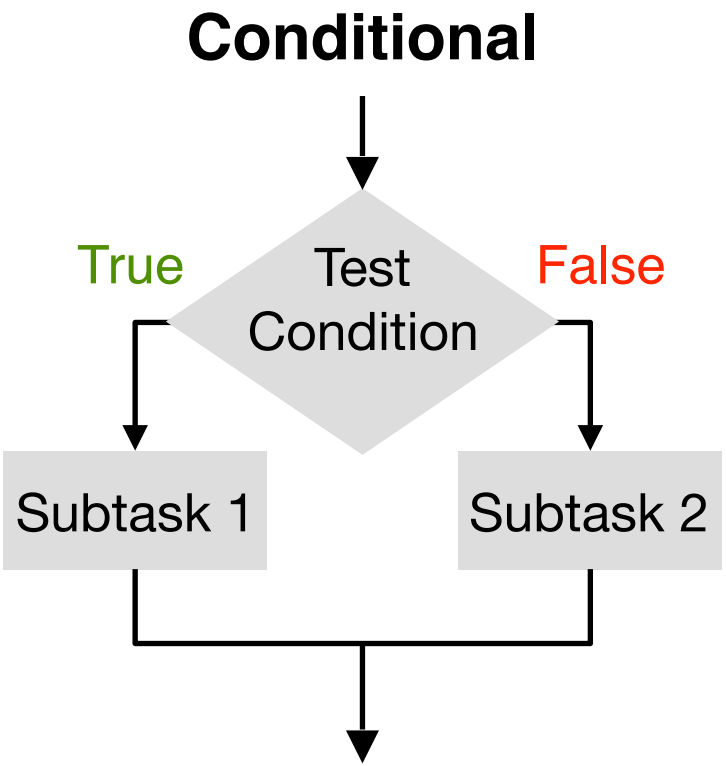


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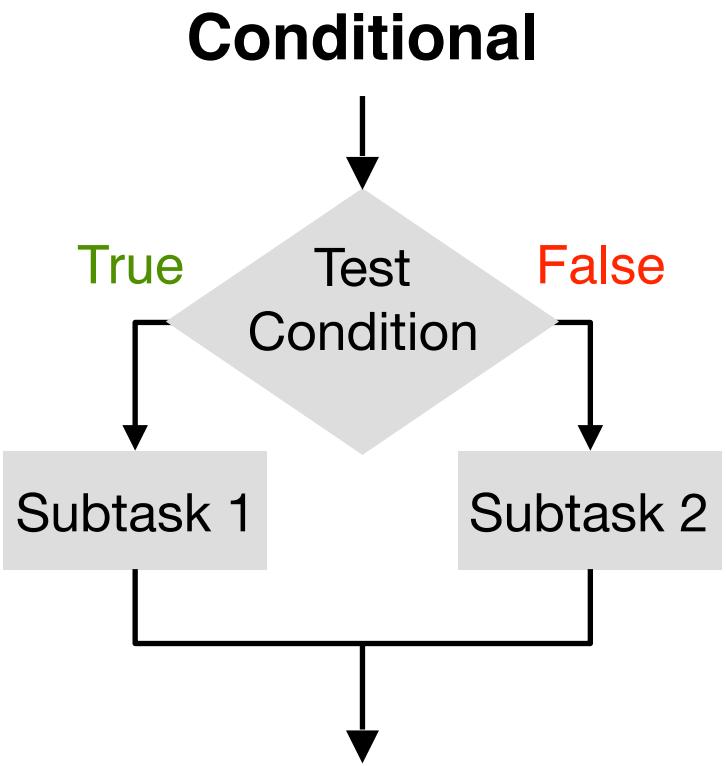


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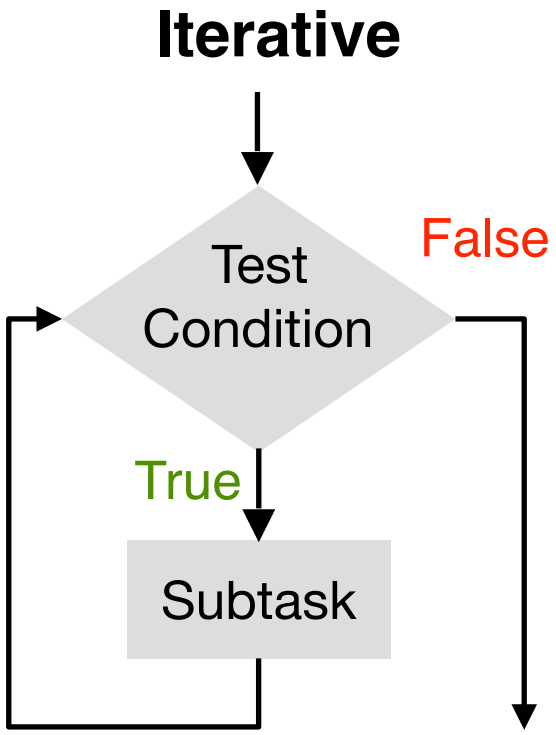
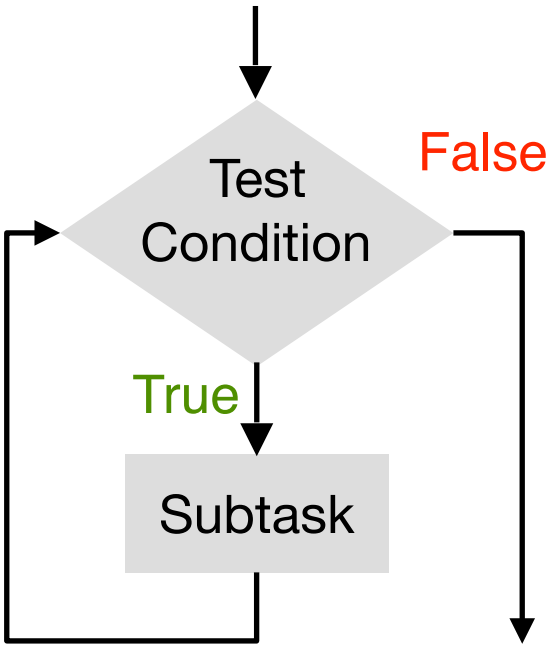



```
while (x > 0) {
  x--;
}
```

Summary

Iterative

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Memory

```
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…
addq
jmp .L2
.L1:

…
  <subtask>
…
.L2:
  cmpq A, B
  jg .L1
…
…
```
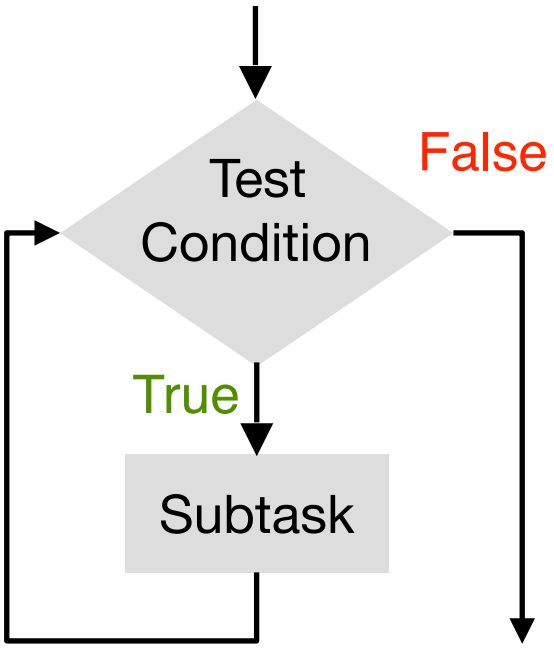

Summary

Iterative

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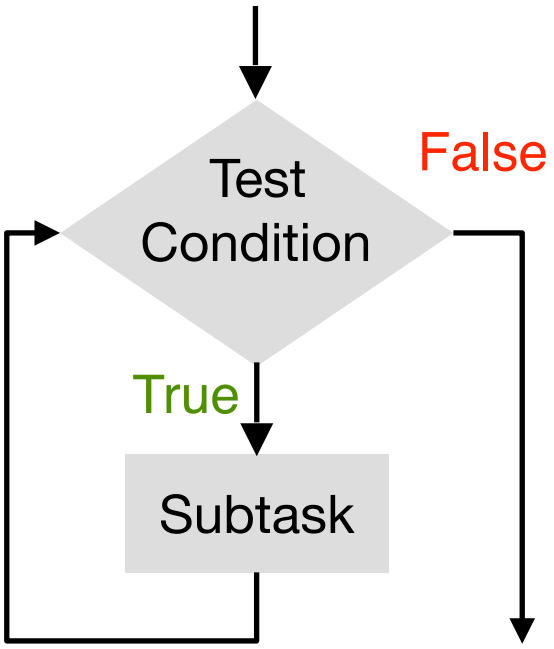


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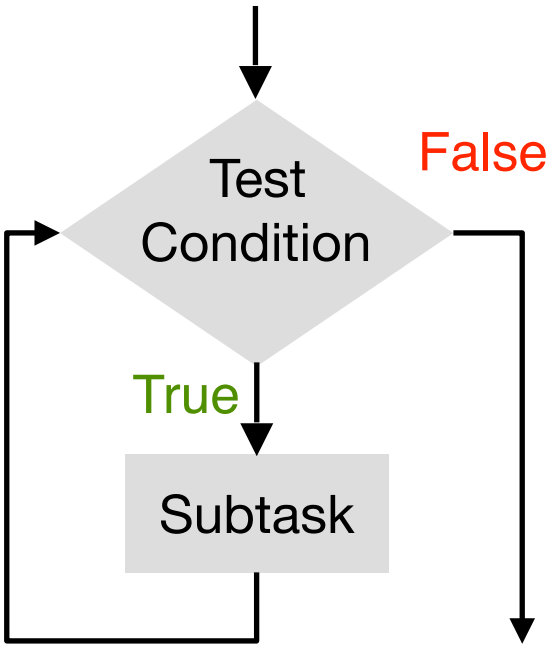


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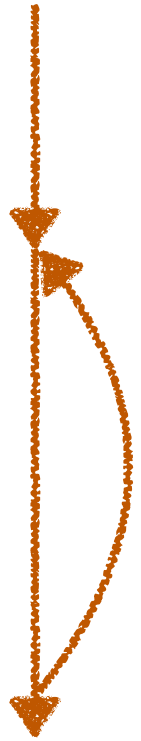

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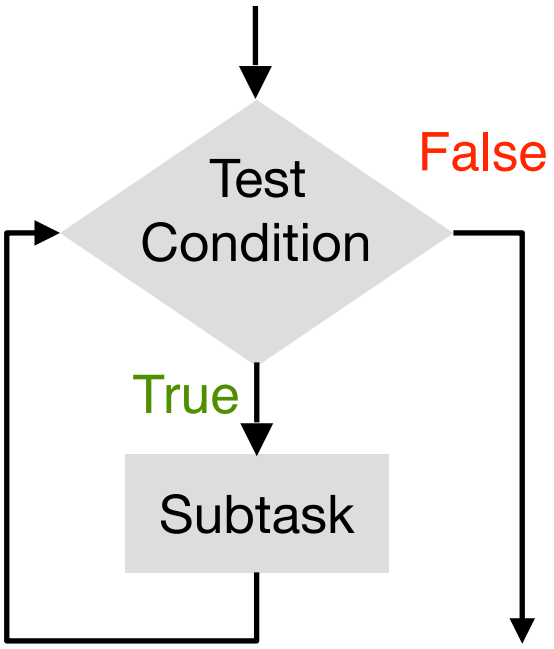


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…
…
```

Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Example of a Go Program Structure

```
main()
{
  /* place pieces on board */
  SetupBoard();

  /* choose black/white */
  DetermineSides();

  /* Play game */
  do {
    WhitesTurn();
    BlacksTurn();
  } while (NoOutcomeYet());
}
```

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}
```

Structure of program is evident, even without knowing implementation.

Functions Declaration in C

Declaration (also called prototype)

- States return type, name, types of arguments

```
int Factorial(int);
```

type of return value

name of function

types of all arguments

Function Definition

- Must match function declaration
- Implement the functionality of the function

```
int Factorial(int n)
{
   int i;
   int result = 1;
   for (i = 1; i <= n; i++)
     result *= i;
   return result;
}
```

gives control back to calling function and returns value

Mechanisms in Procedures

```
P(…) {
…
…
   y = Q(x);
   print(y)
…
}
```

```
int Q(int i)
{
   int t = 3*i;
   int v[10];
…
…
   return v[t];
}
```

Mechanisms in Procedures

- Passing control
  - To beginning of procedure code
  - Back to return point

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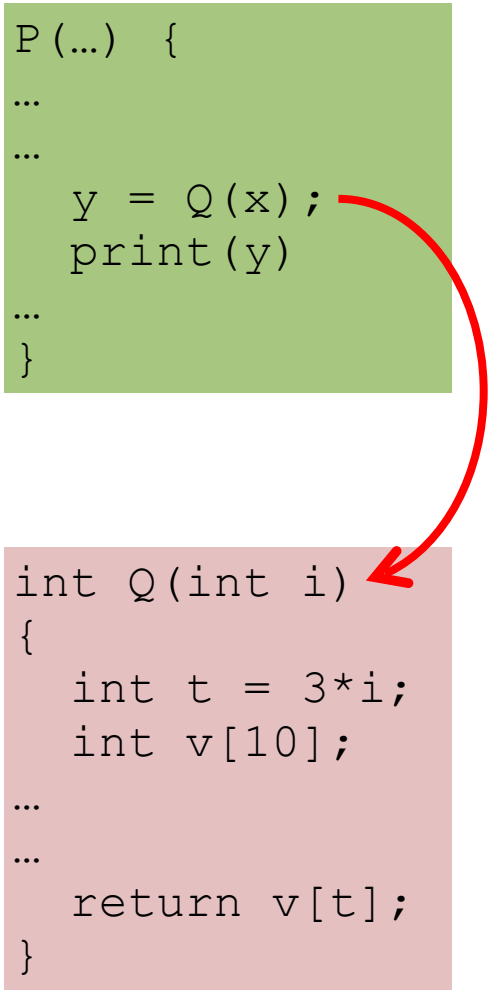

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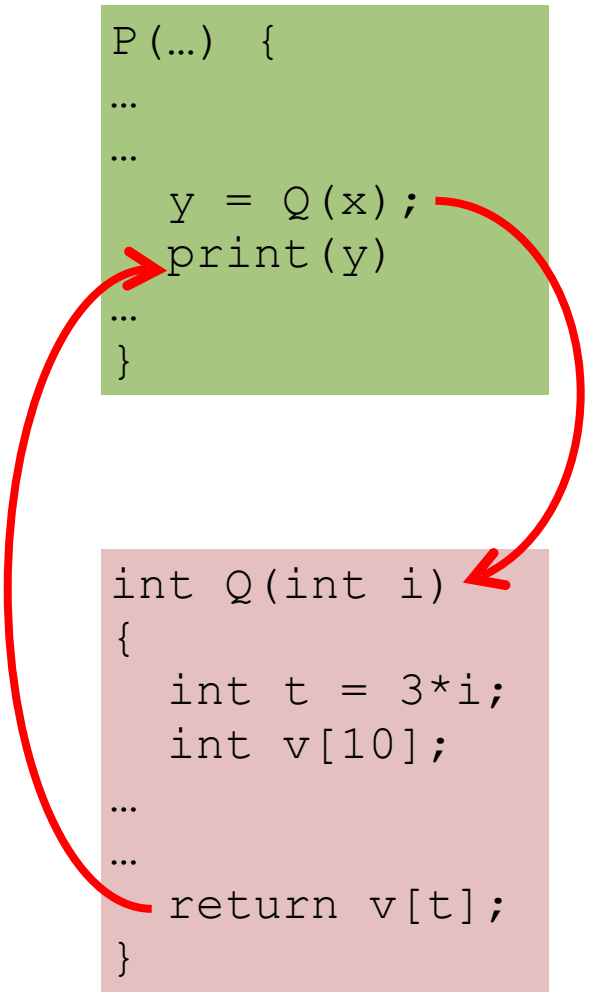

Mechanisms in Procedures

- Passing control
  - To beginning of procedure code
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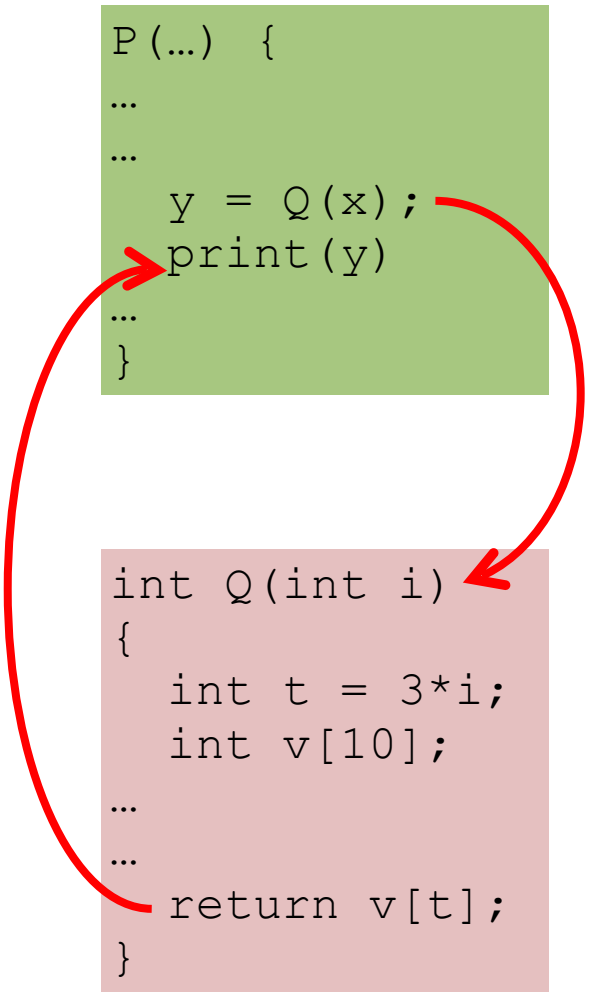
- Passing data
  - Procedure arguments
  - Return value



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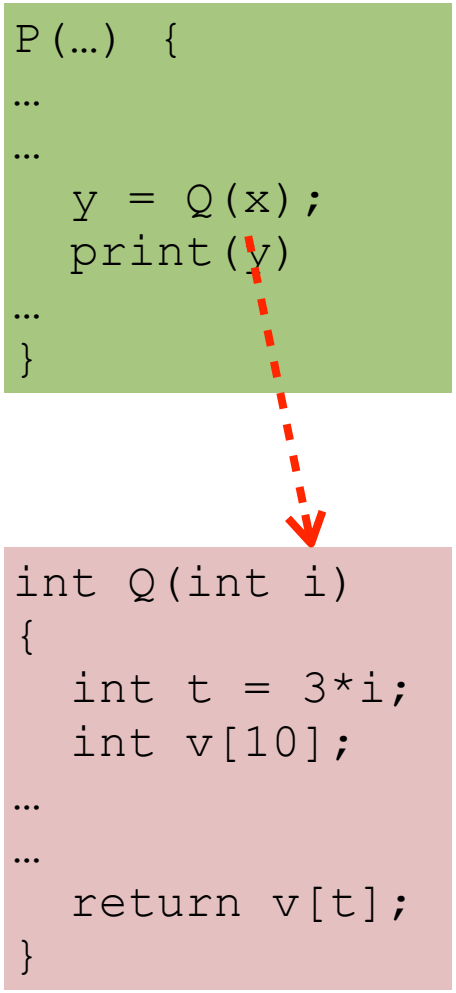

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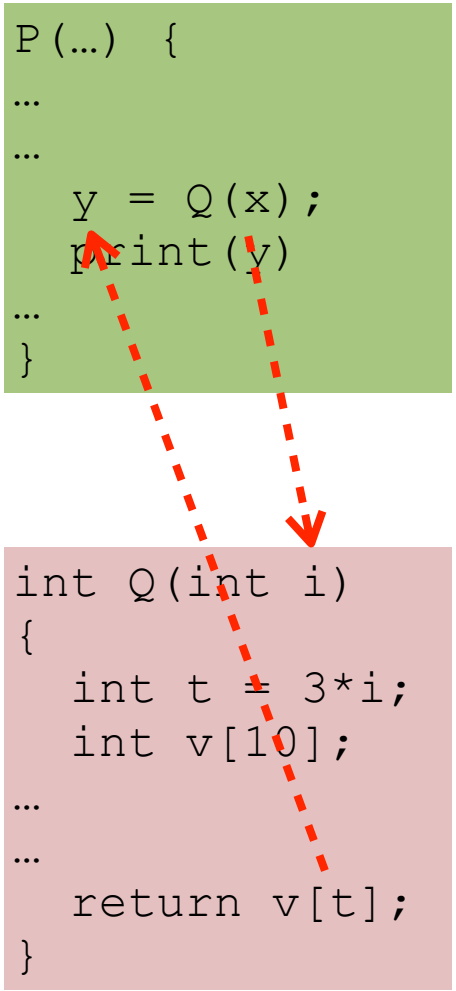

Mechanisms in Procedures

- Passing control
  - To beginning of procedure code
  - Back to return point
- Passing data
  - Procedure arguments
  - Return value
- Local Memory management
  - Allocate during procedure execution
  - Deallocate upon return

```
P(…) {
…
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```

```
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```

Mechanisms in Procedures

- Passing control
  - To beginning of procedure code
  - Back to return point
- Passing data
  - Procedure arguments
  - Return value
- Local Memory management
  - Allocate during procedure execution
  - Deallocate upon return
- Mechanisms all implemented with machine instructions

```
P(…) {
…
…
   y = Q(x);
   print(y)
…
}
```

```
int Q(int i)
{
   int t = 3*i;
   int v[10];
…
…
   return v[t];
}
```

Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

General Idea

- Frame (Active Record)
  - A frame refers to a piece of memory that contains (almost) all the information needed to execute a function, e.g., arguments and local variables

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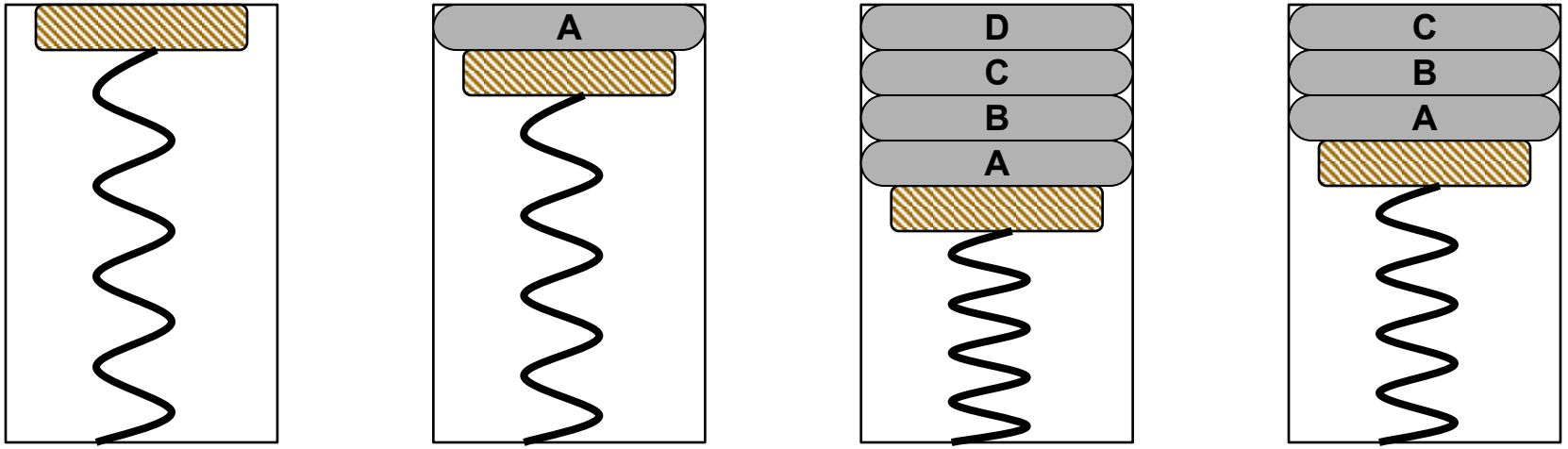
- When a function is called, create a frame, and push it to the memory
- When a function is returned, pop the frame out of the memory
- Frames are stored in memory in a *stack* fashion

A Physical Stack: A Coin Holder

First quarter out is the last quarter in.



Initial State

After
One Push

After Three
More Pushes

After
One Pop

- Stack is the right data structure for function call / return
  - If A calls B, then B returns before A

Run-Time Stack During Function Call

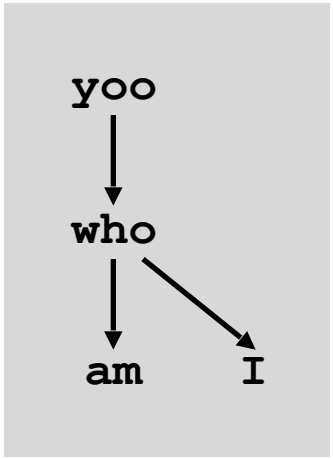
```
yoo(…)
{
   •
   •
   who();
   •
   return
}
```

```
who(…)
{
   • • •
   am();
   • • •
   I();
   return;
}
```

```
am(…)
{
   •
   •
   •
   return;
}
```

```
I(…)
{
   •
   •
   •
   return;
}
```

Example Call Chain



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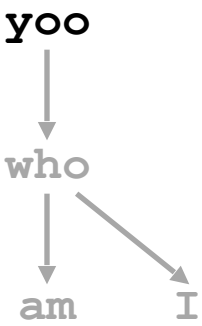

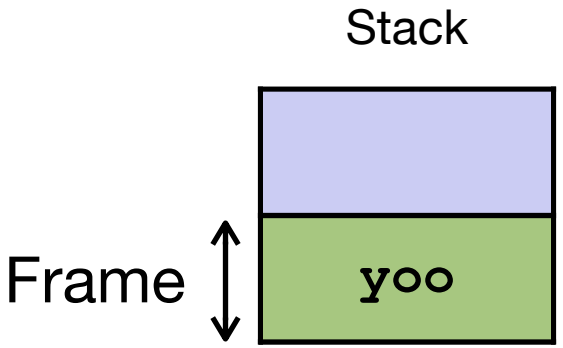

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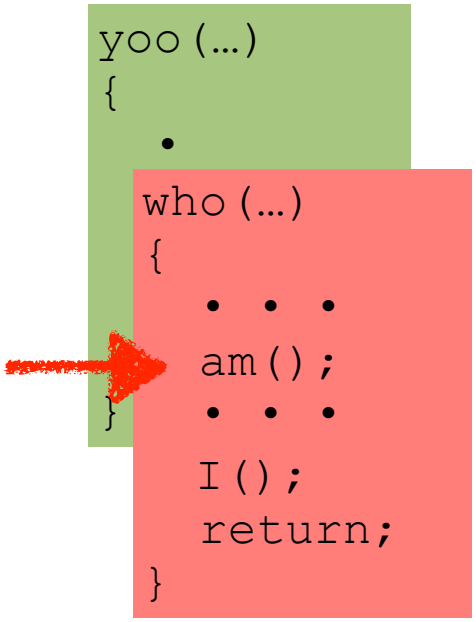

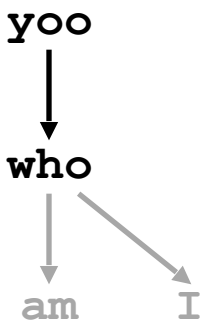

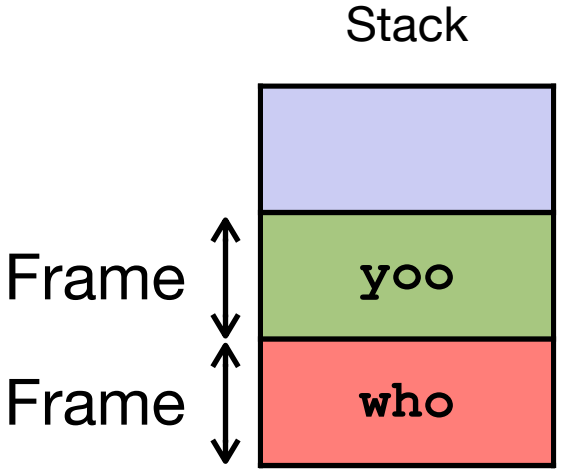

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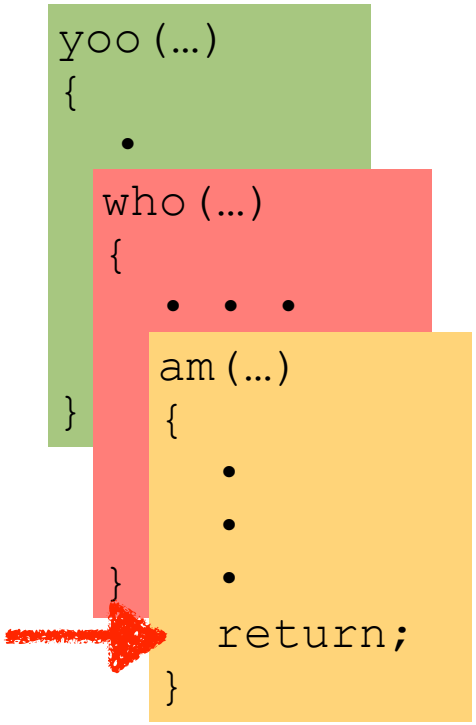

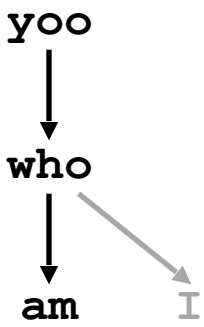

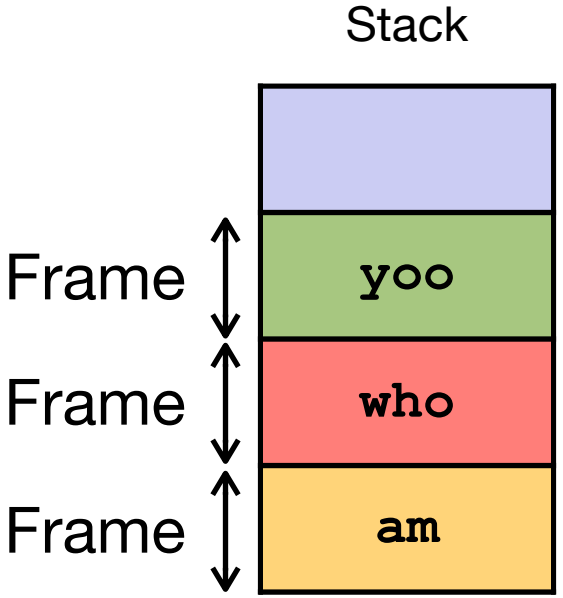

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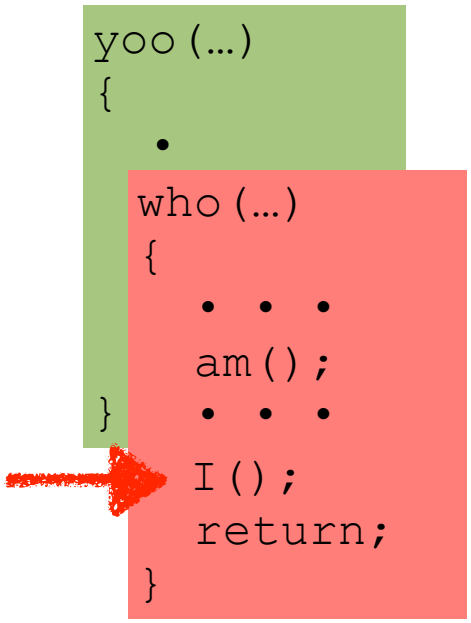

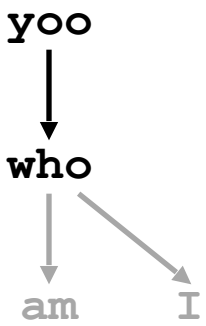

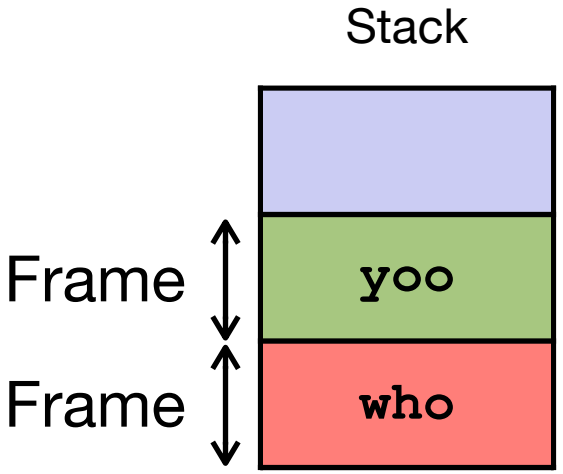

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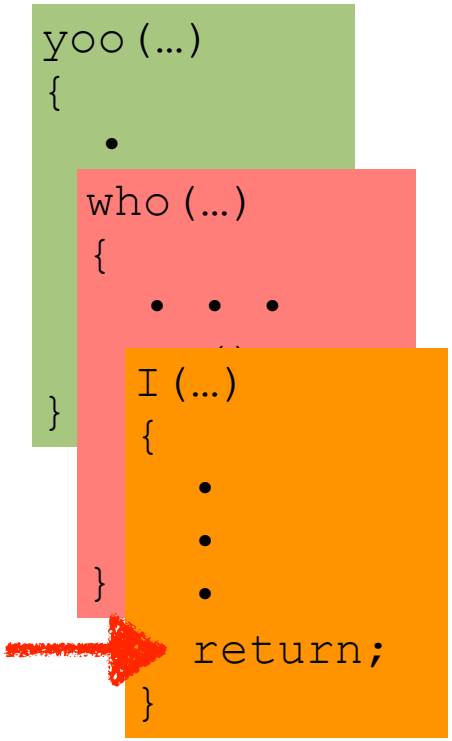

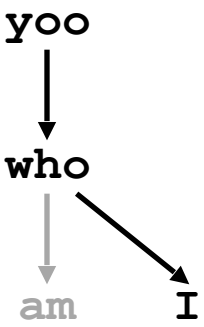

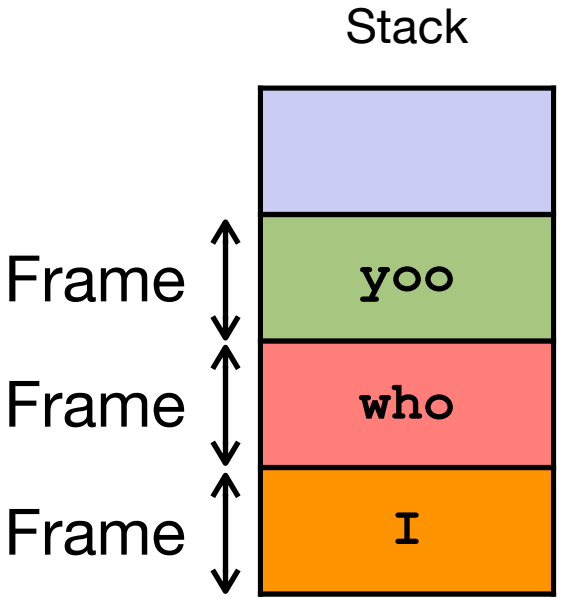

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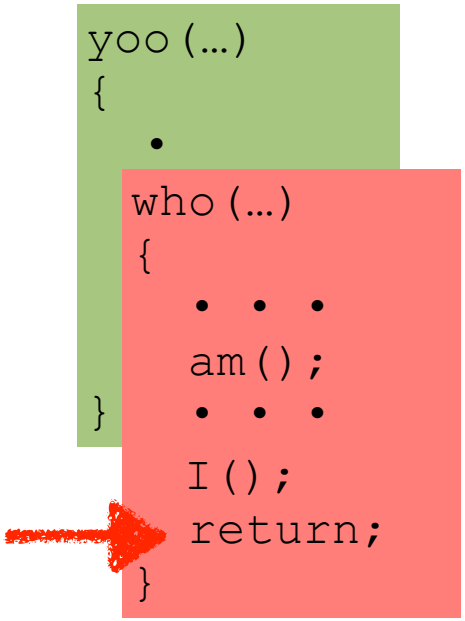

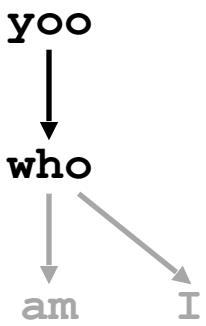

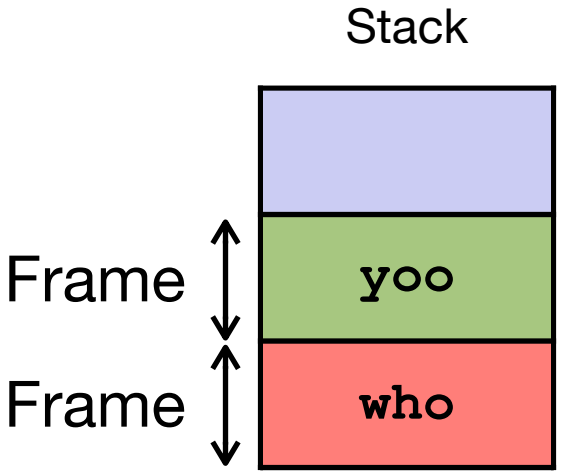

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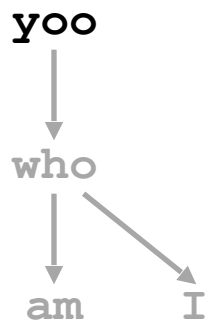
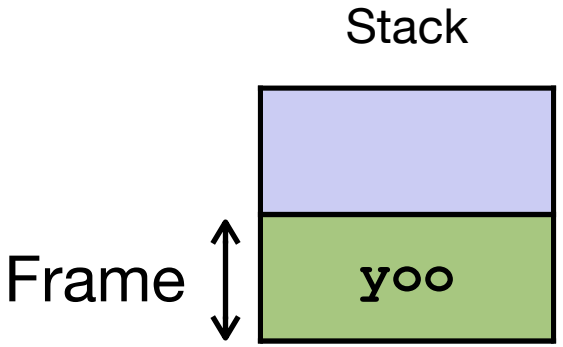
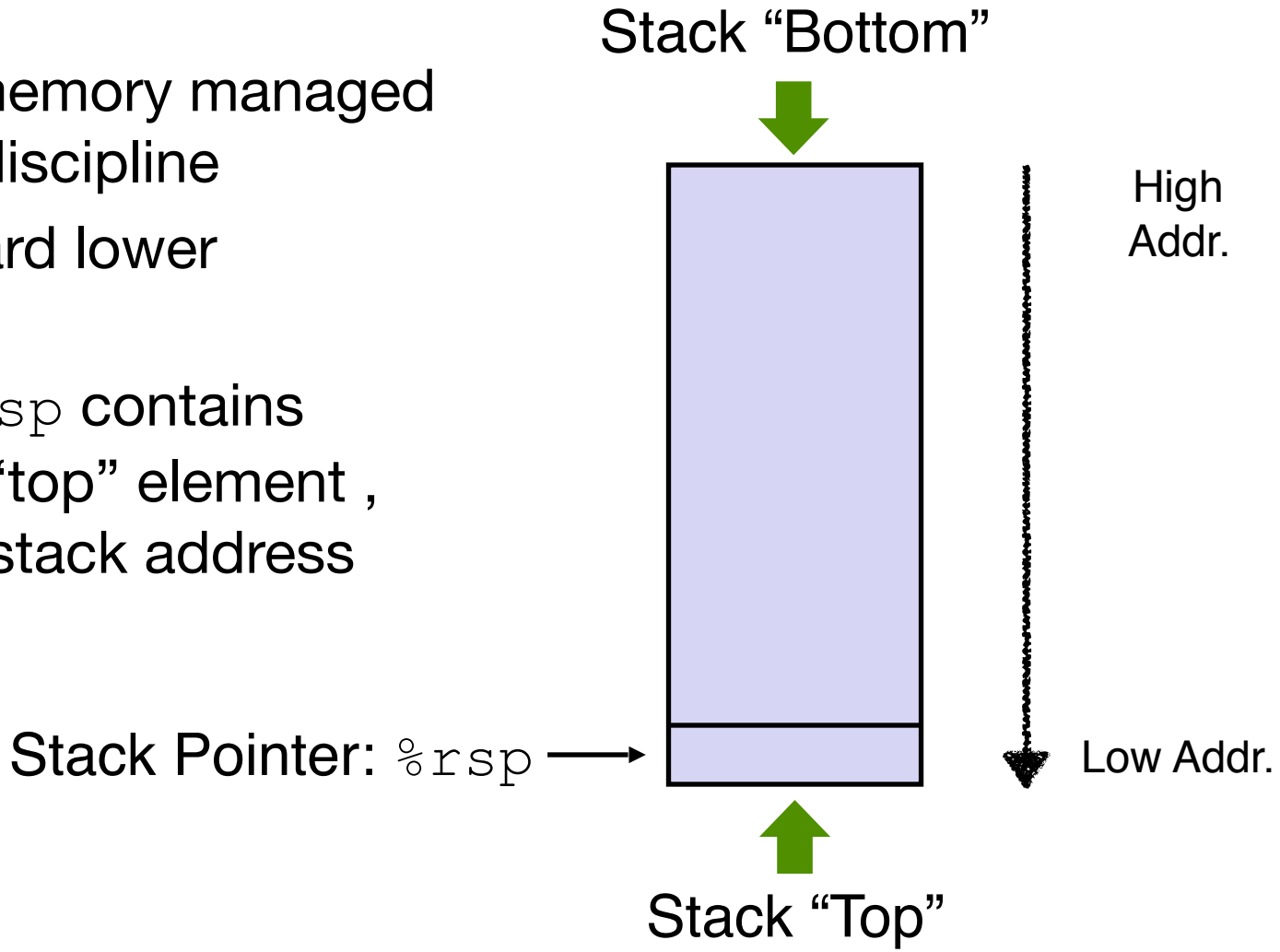




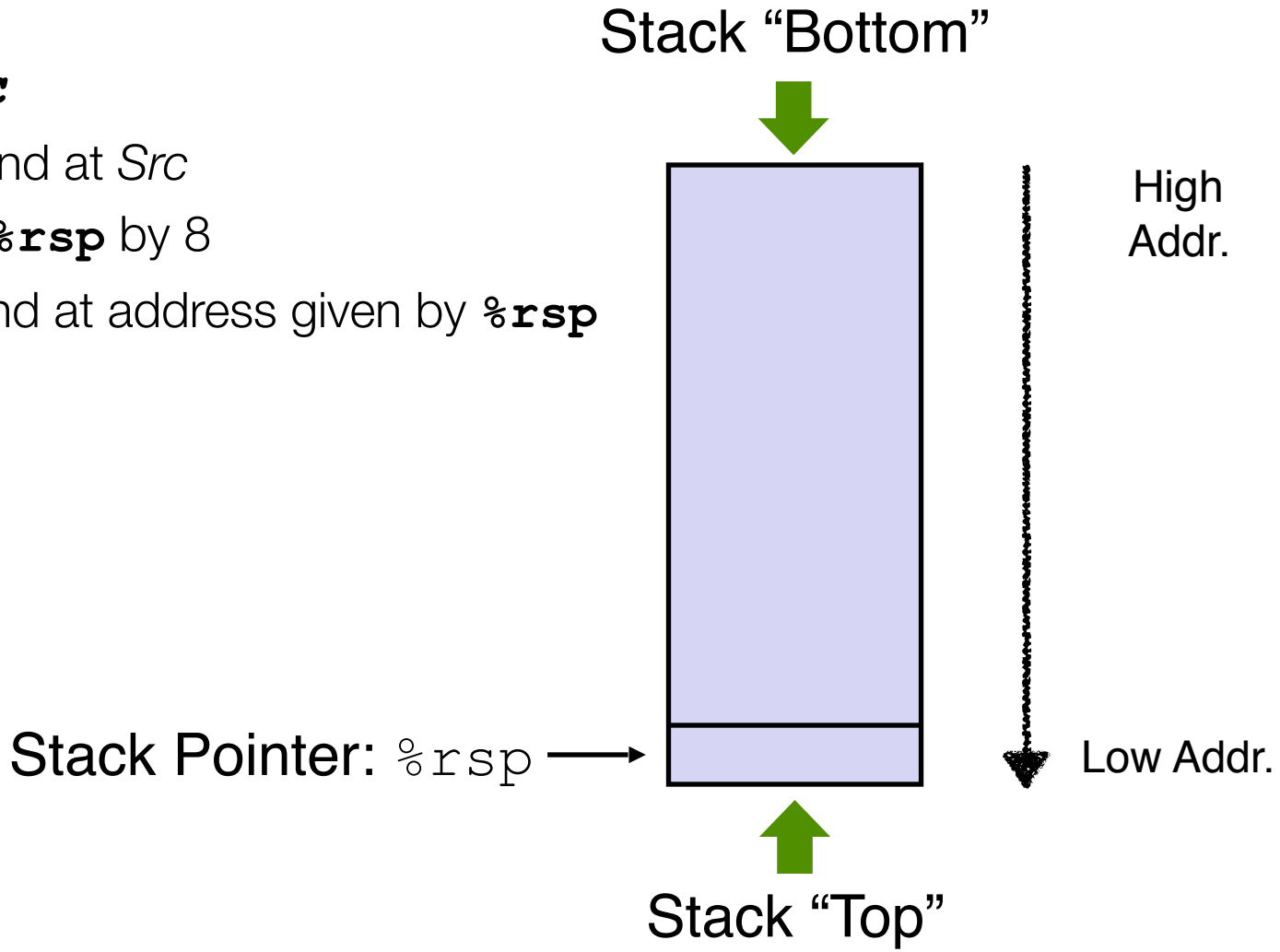
Stack in X86-64

- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register `%rsp` contains address of “top” element , i.e., lowest stack address



x86-64 Stack: Push

- **`pushq` *`Src`***
  - Fetch operand at *Src*
  - Decrement **`%rsp`** by 8
  - Write operand at address given by **`%rsp`**



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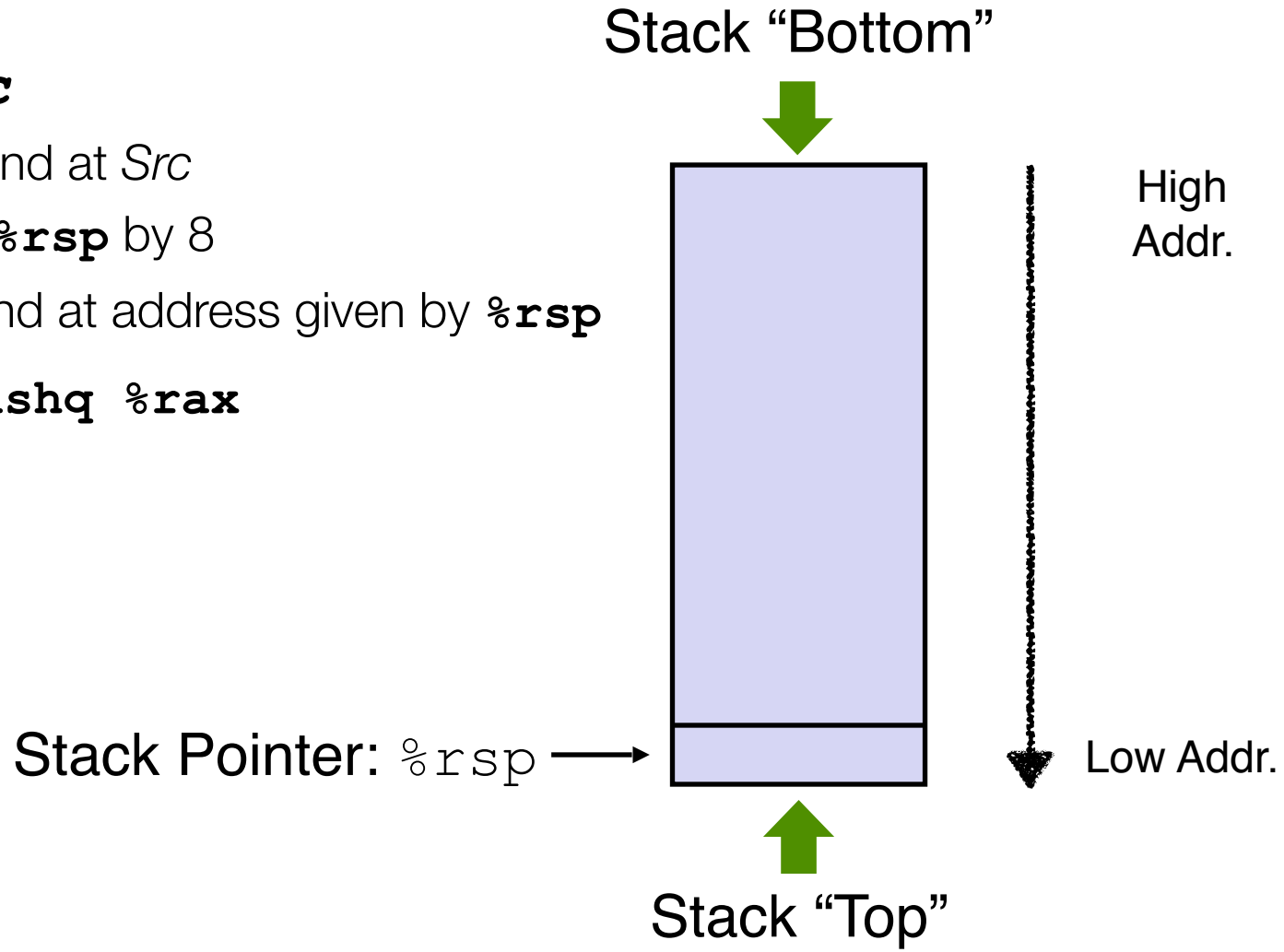

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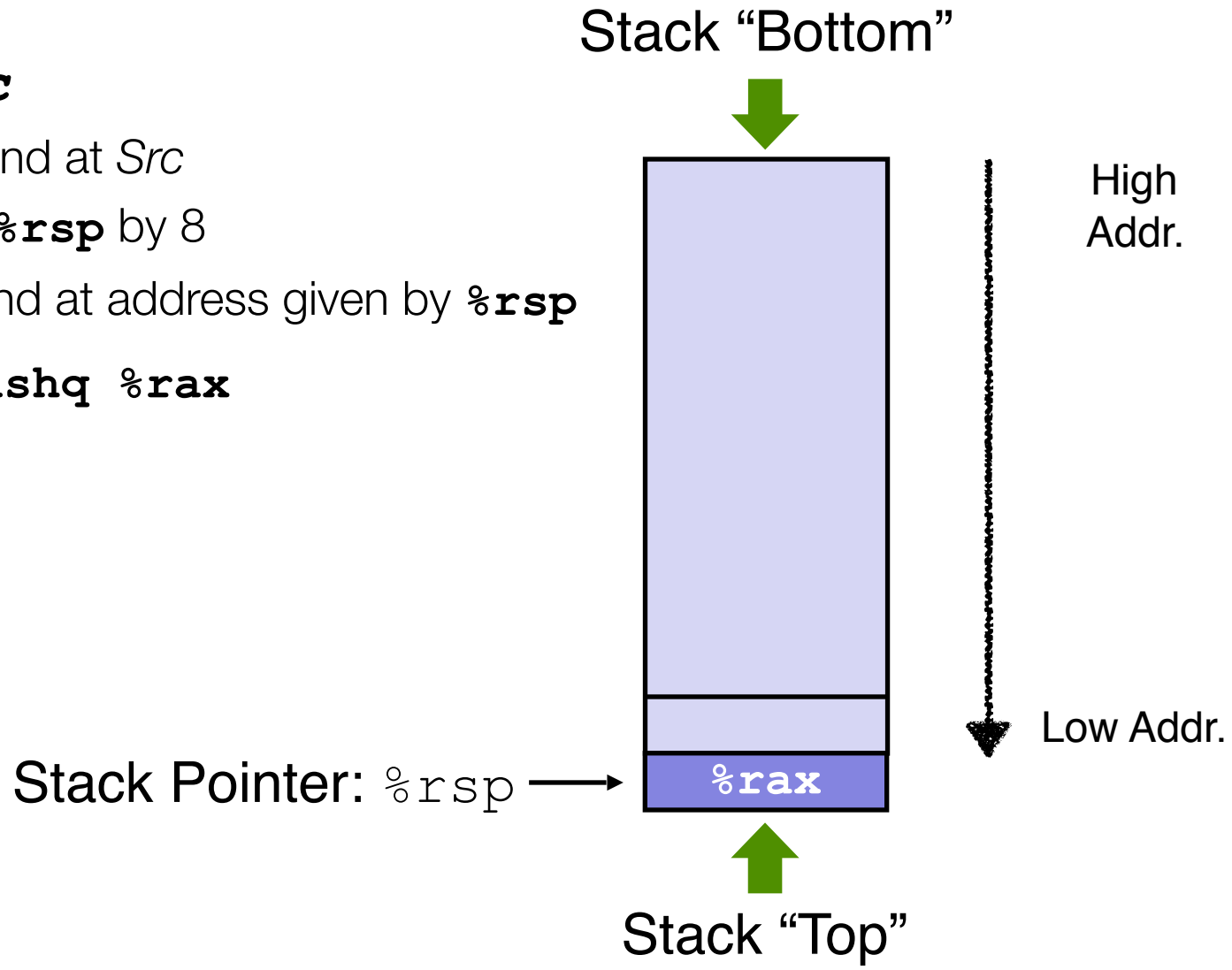

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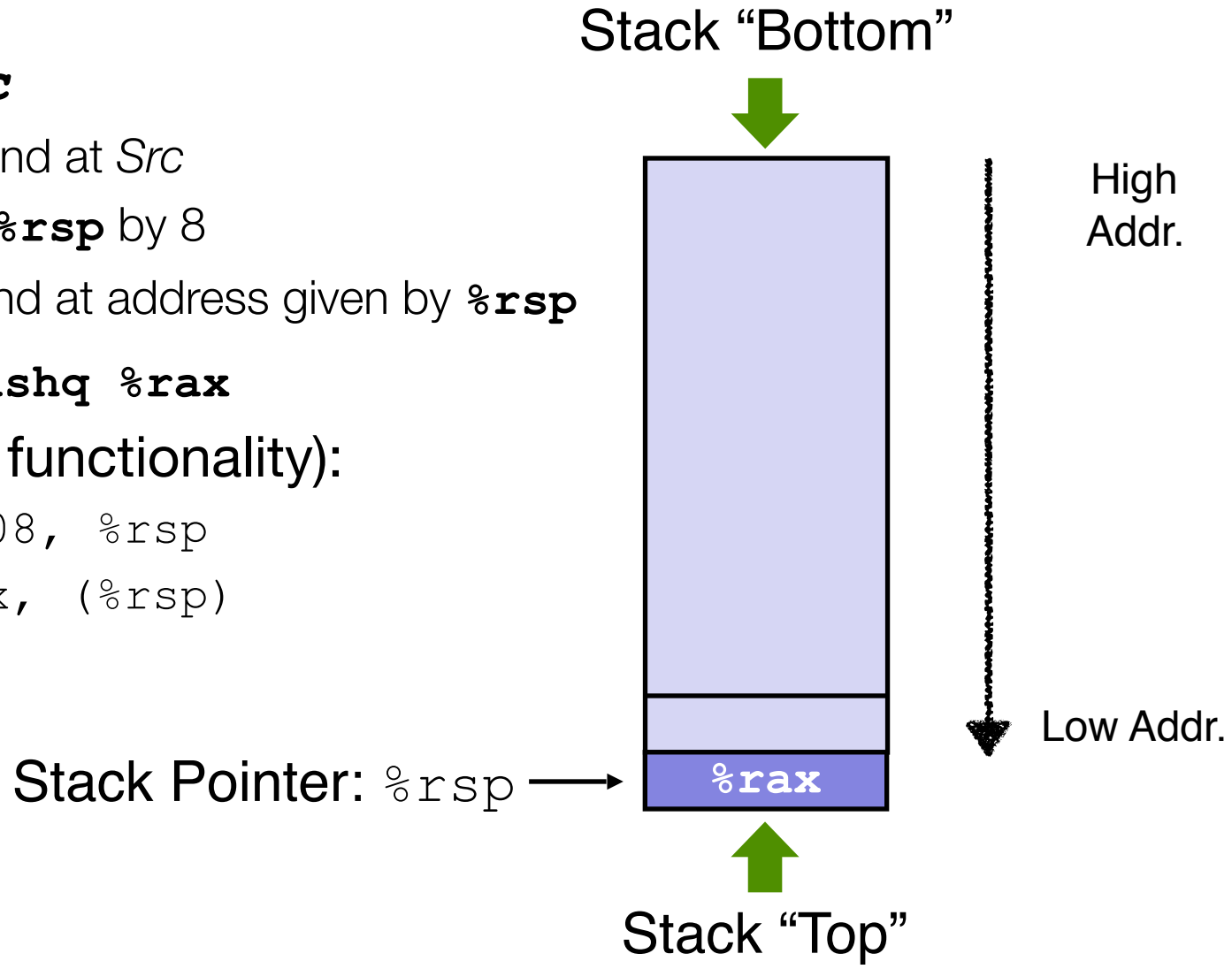
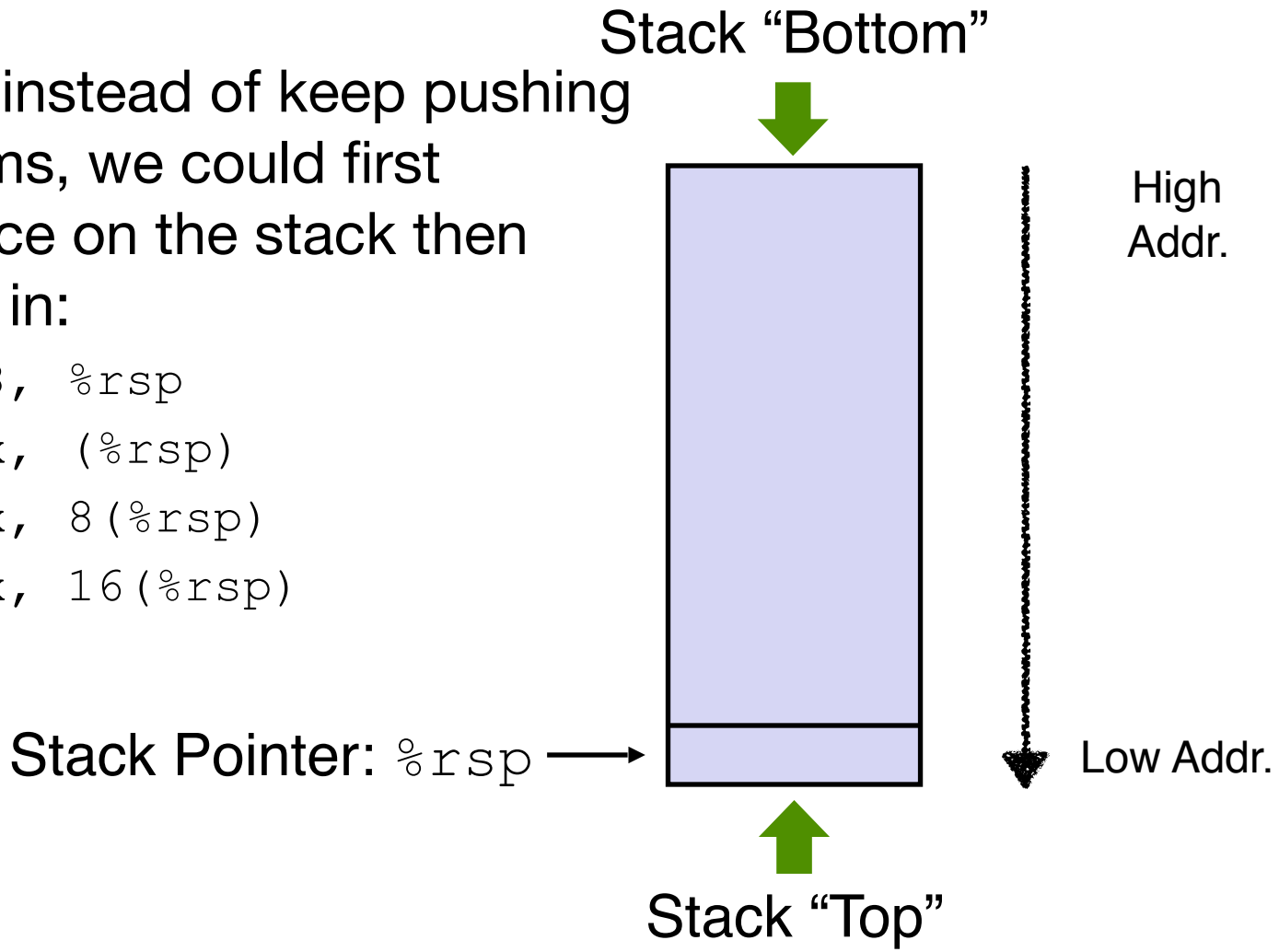
- Same as (in functionality):
  - `subq $0x08, %rsp`
  - `movq %rax, (%rsp)`



x86-64 Stack: Push

- Sometimes instead of keep pushing multiple items, we could first reserve space on the stack then move items in:
  - `subq 0x18, %rsp`
  - `movq %rax, (%rsp)`
  - `movq %rbx, 8(%rsp)`
  - `movq %rcx, 16(%rsp)`



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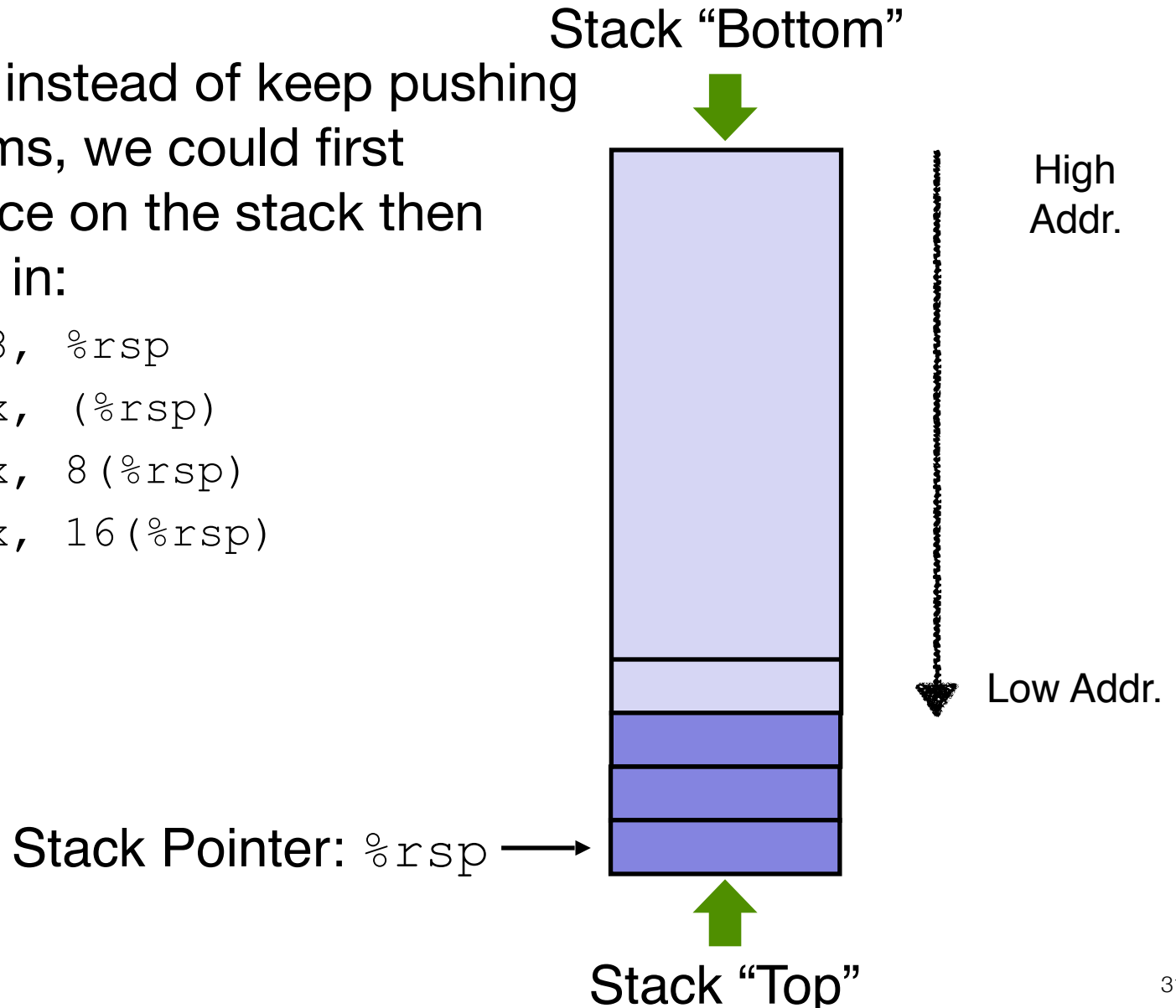

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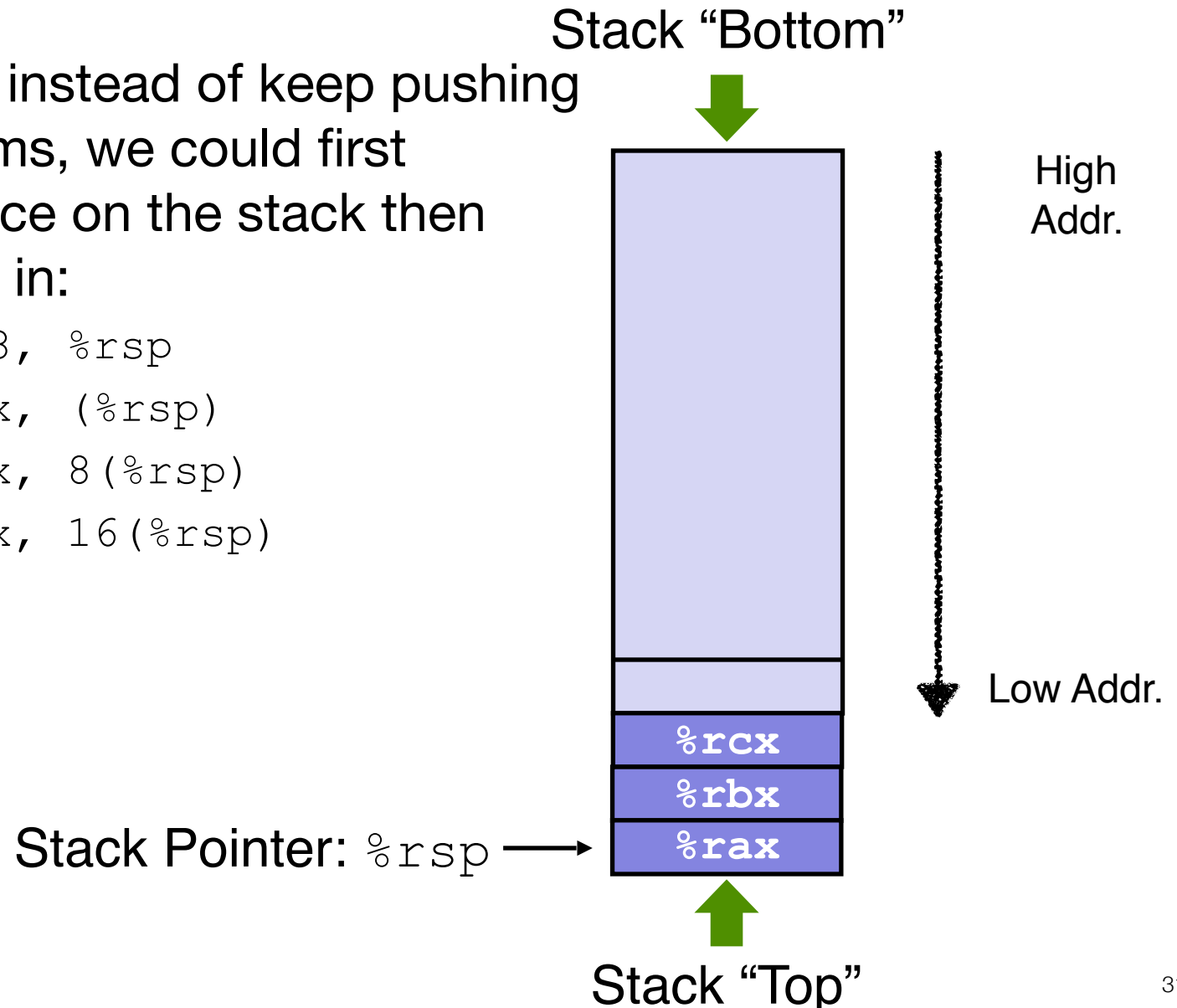
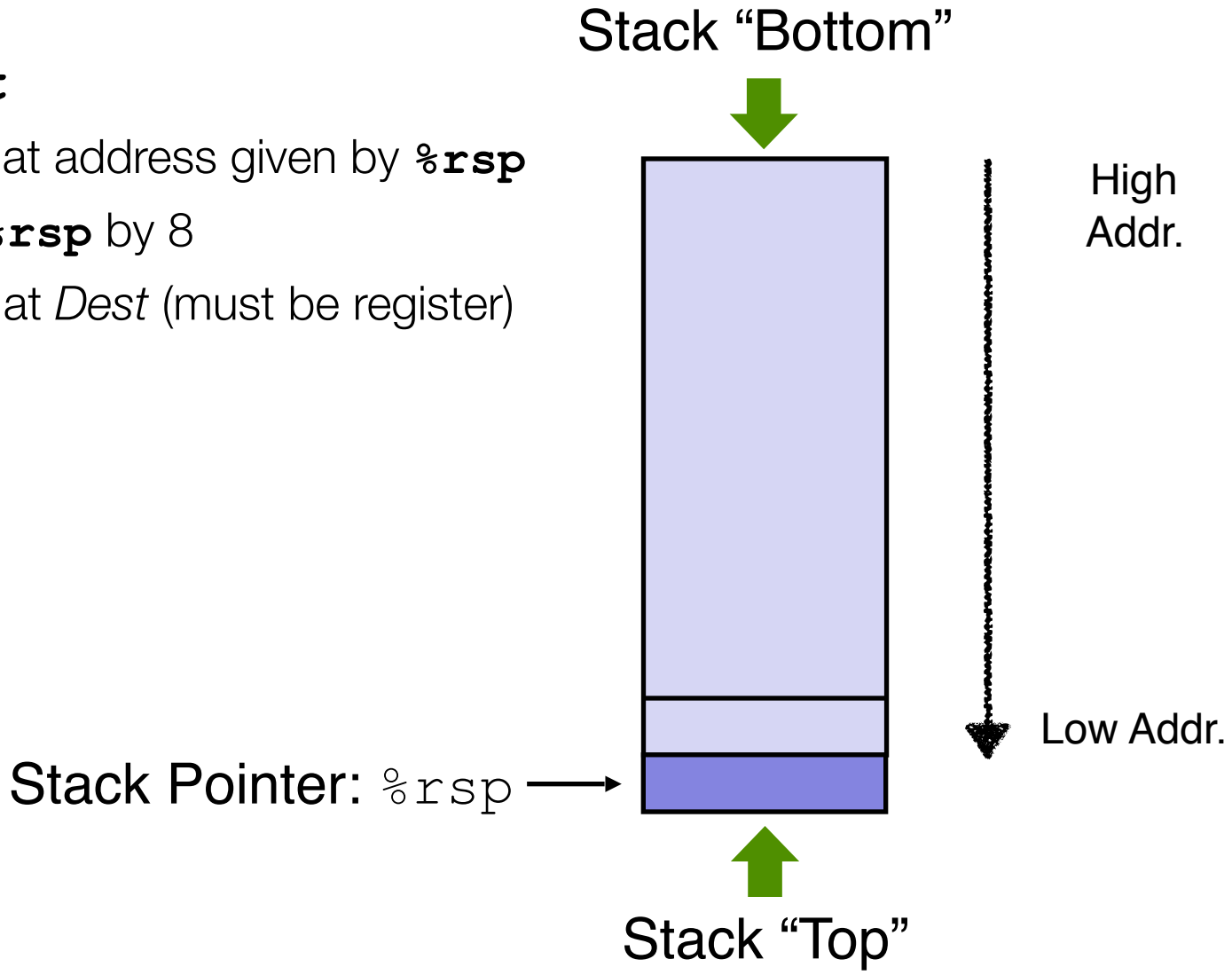


x86-64 Stack: Pop

- **`popq` *`Dest`***
  - Read value at address given by **`%rsp`**
  - Increment **`%rsp`** by 8
  - Store value at *Dest* (must be register)



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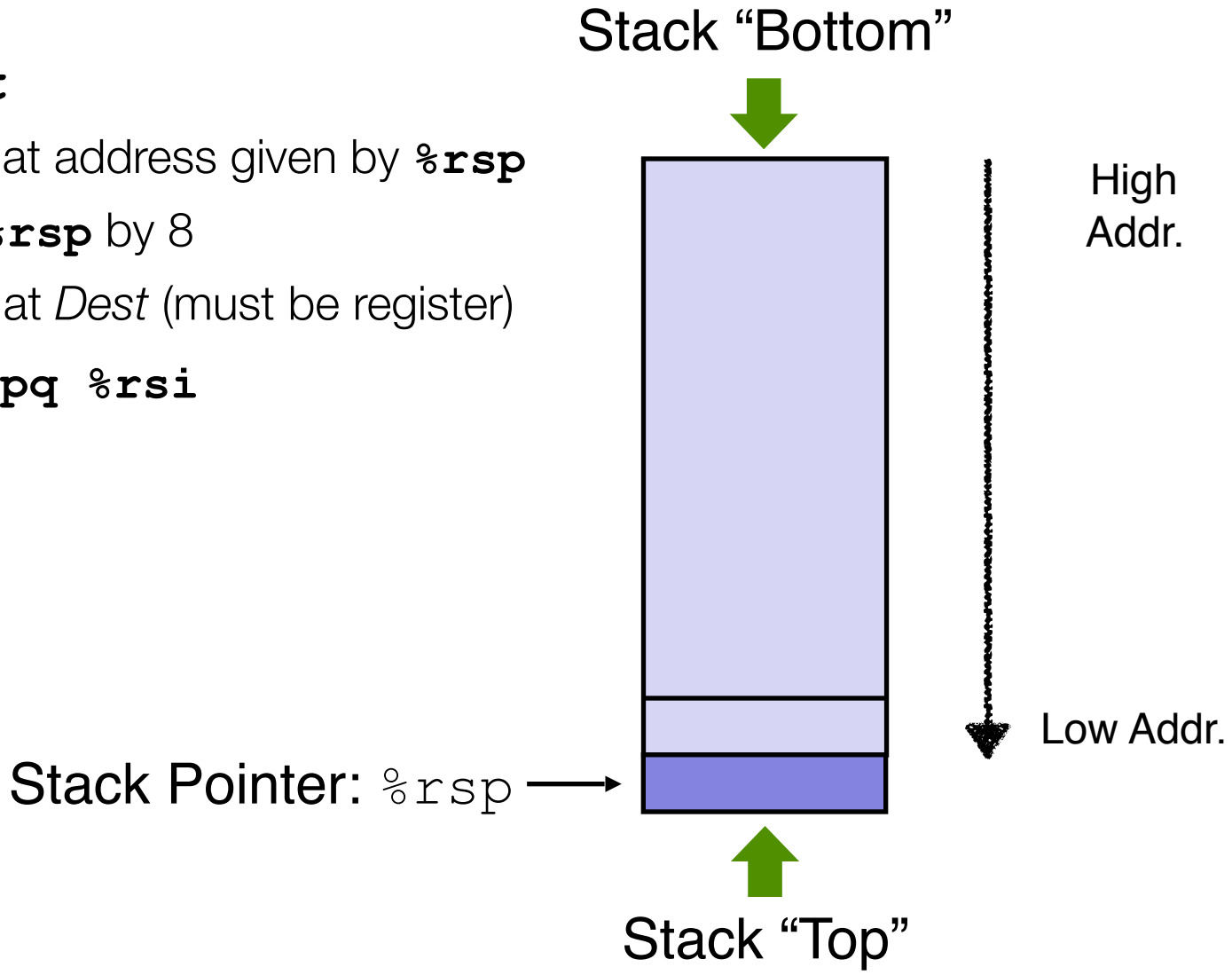

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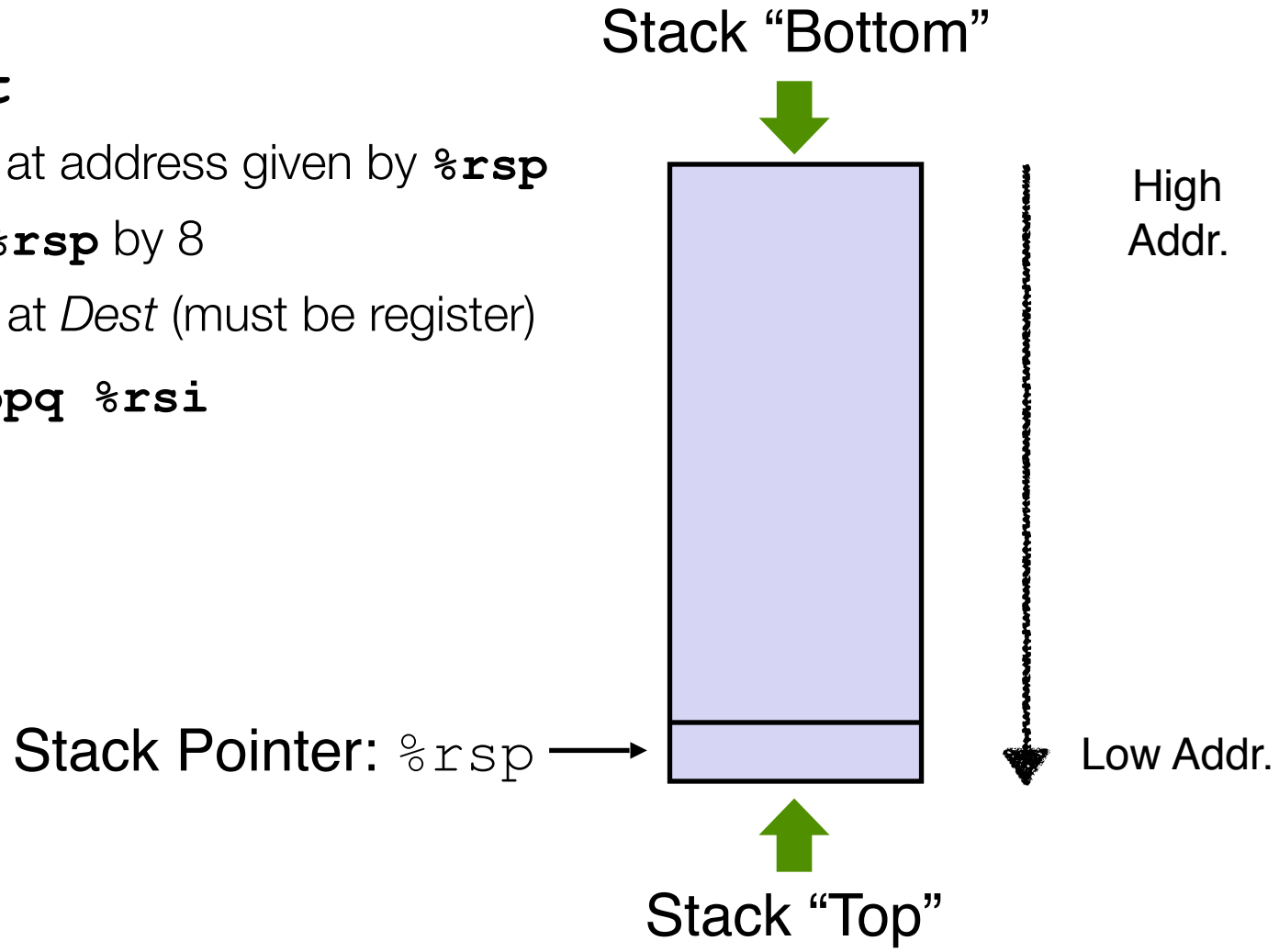

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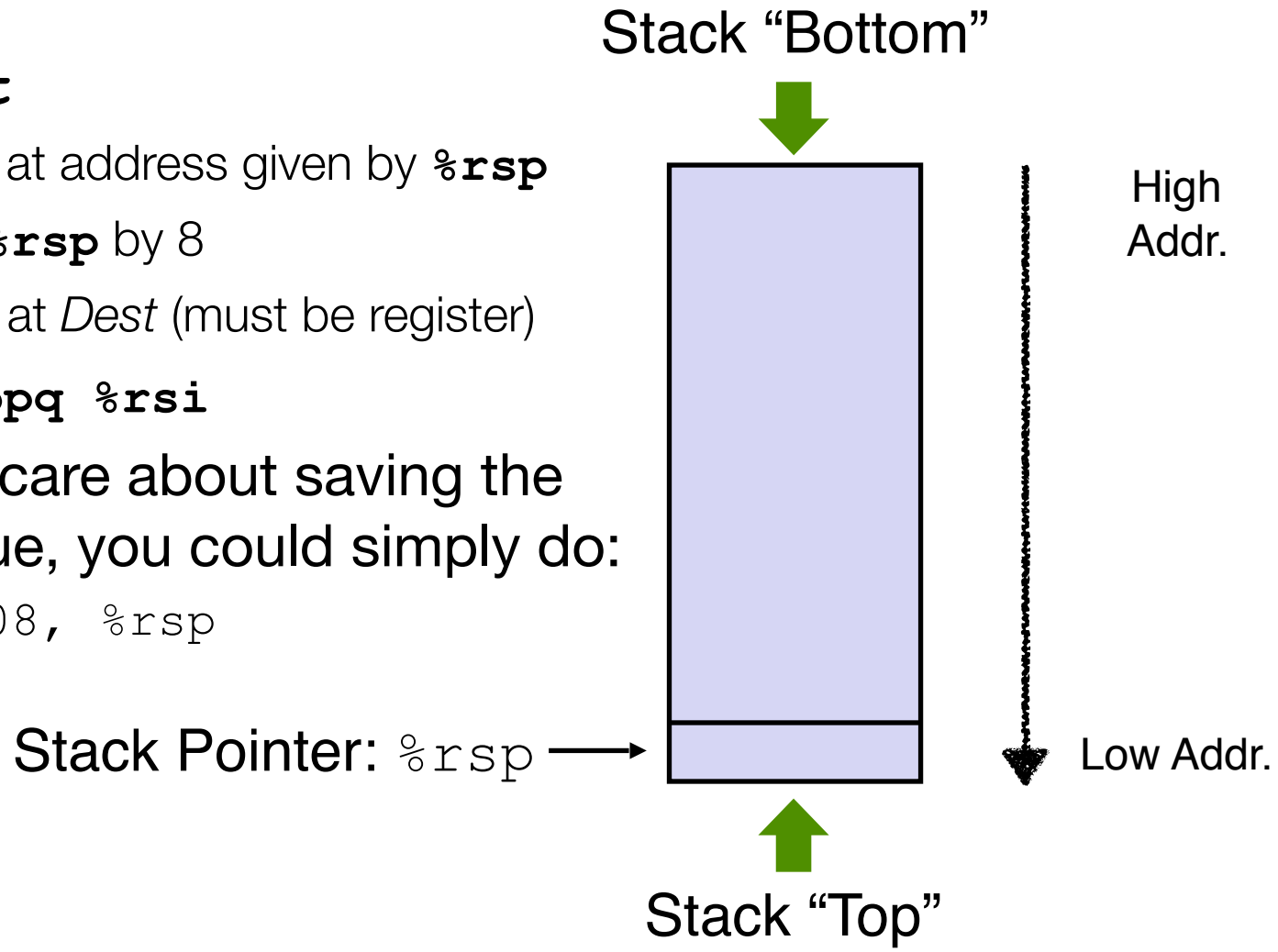
  - Read value at address given by **`%rsp`**
  - Increment **`%rsp`** by 8
  - Store value at *Dest* (must be register)
- Example: **`popq %rsi`**
- If you don't care about saving the popped value, you could simply do:
  - `addq $0x08, %rsp`



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Code Examples

```
void multstore
 (long x, long y, long *dest)
{
    long t = mult2(x, y);
    *dest = t;
}

…

long mult2 (long a, long b)
{
  long s = a * b;
  return s;
}
```

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```
400540 <multstore>:
  400540: push   %rbx
  400541: mov    %rdx,%rbx
  400544: callq  400550 <mult2>
  400549: mov    %rax,(%rbx)
  40054c: pop    %rbx
  40054d: retq

…

400550 <mult2>:
  400550:  mov    %rdi,%rax
  400553:  imul   %rsi,%rax
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```

`retq` returns to (by changing the PC) 400549.
But how would `retq` know where to return?

Non-Solution

- Replace `callq` with `jmp`
- assign a label to the instruction next to `callq` (e.g., `.L1`)
- replace `retq` with `jmq .L1`

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- Will this work?!
- How about when other functions call `mult2`?

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Using Stack for Function Call and Return

- Procedure call: `call label`
  - Push return address on stack
  - Jump to label
- Return address:
  - Address of the next instruction right after call (`400549` here)
- Procedure return: `ret`
  - Pop address from stack
  - Jump to address

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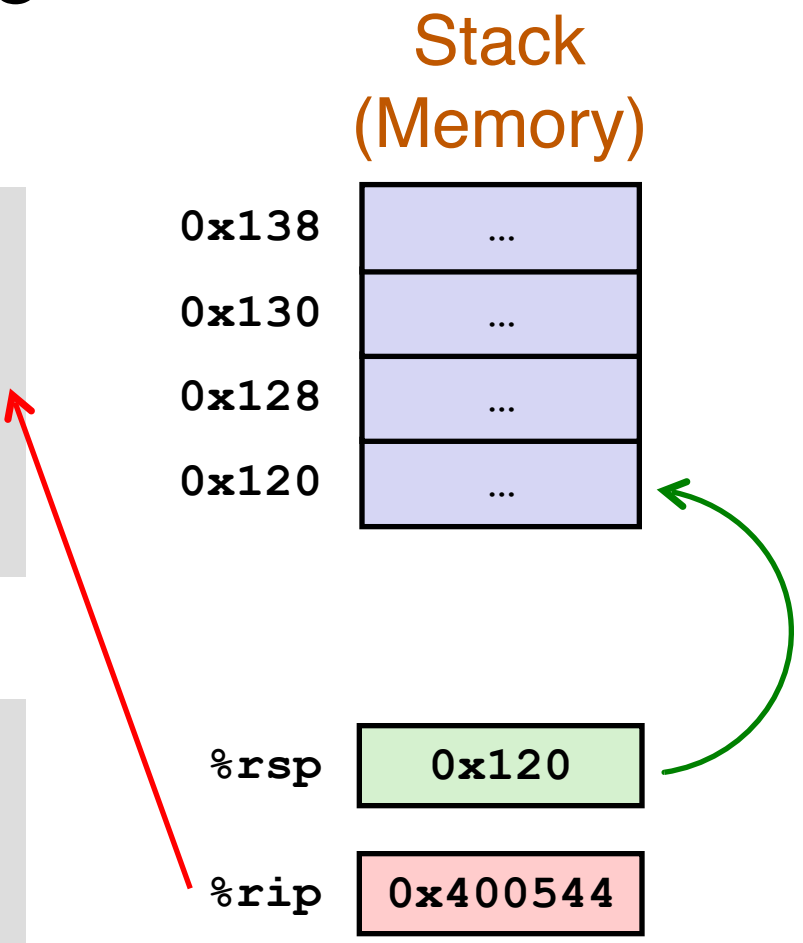

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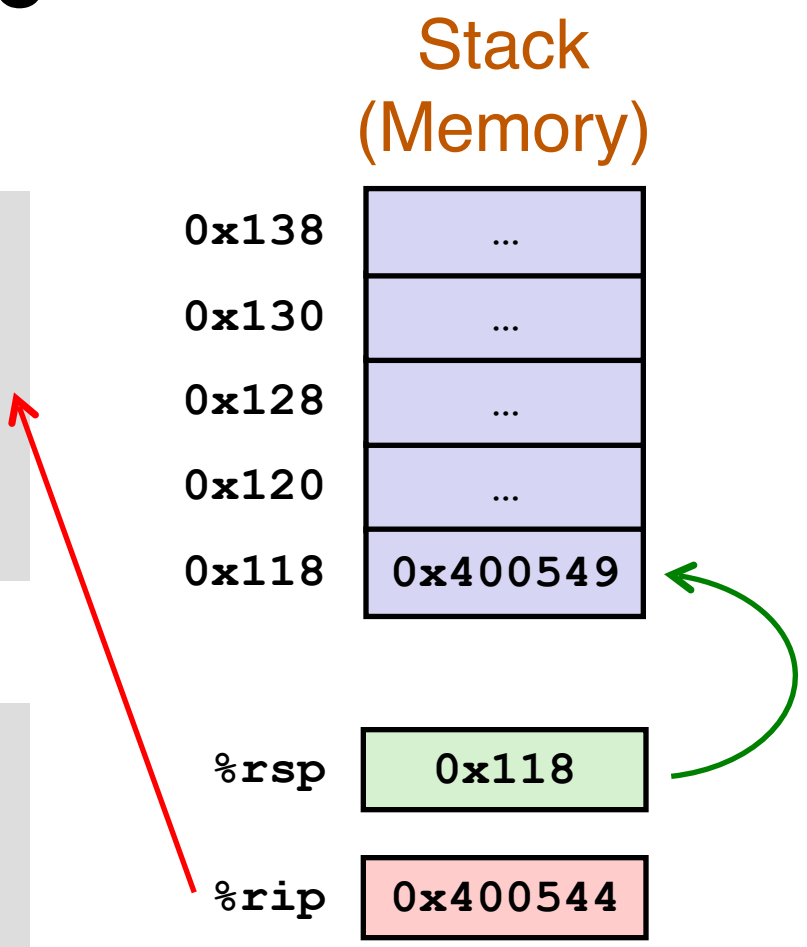

Function Call Example

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…
  400544: callq  400550 <mult2>
  400549: mov    %rax,(%rbx)
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…
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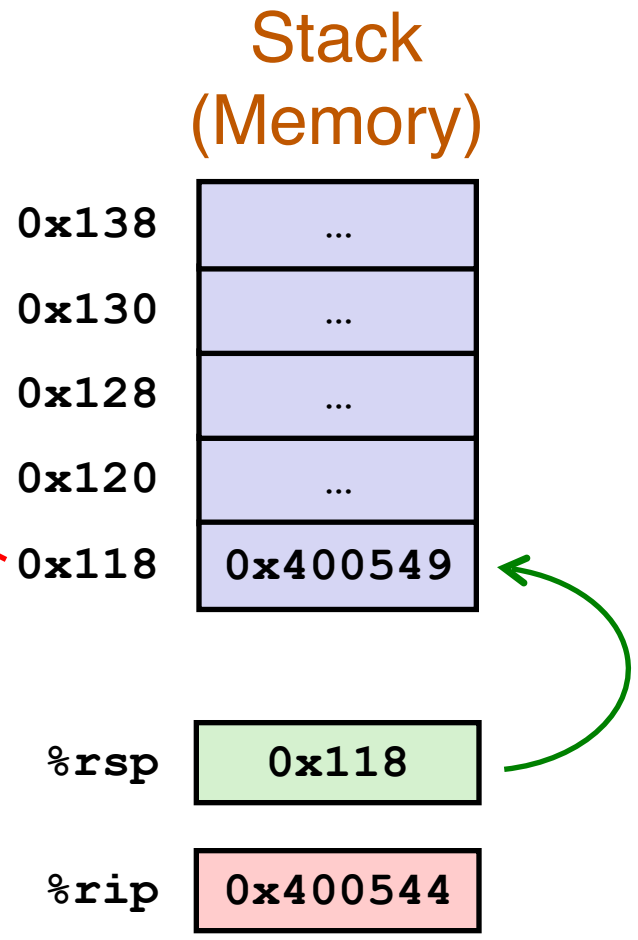

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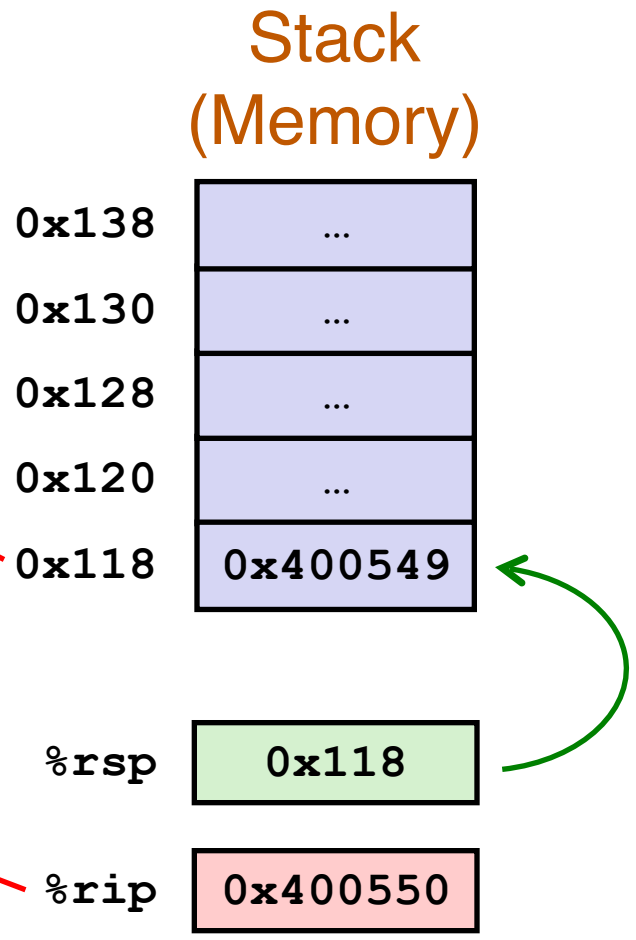

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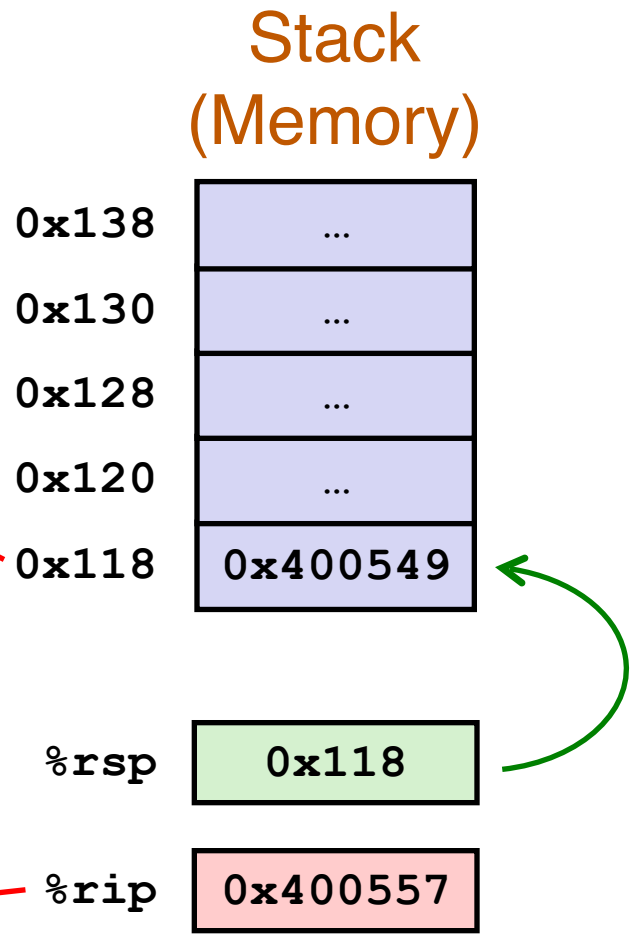

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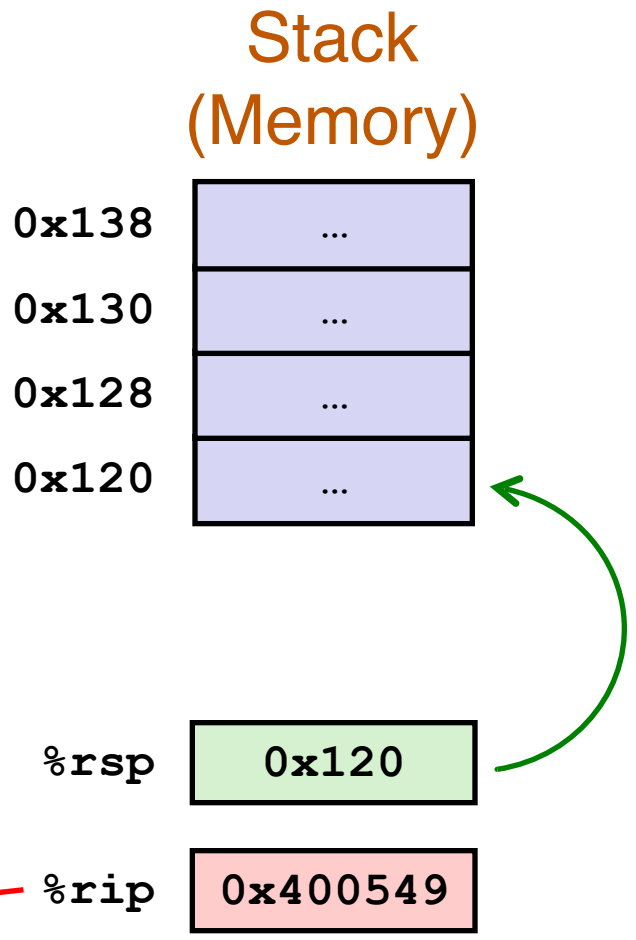

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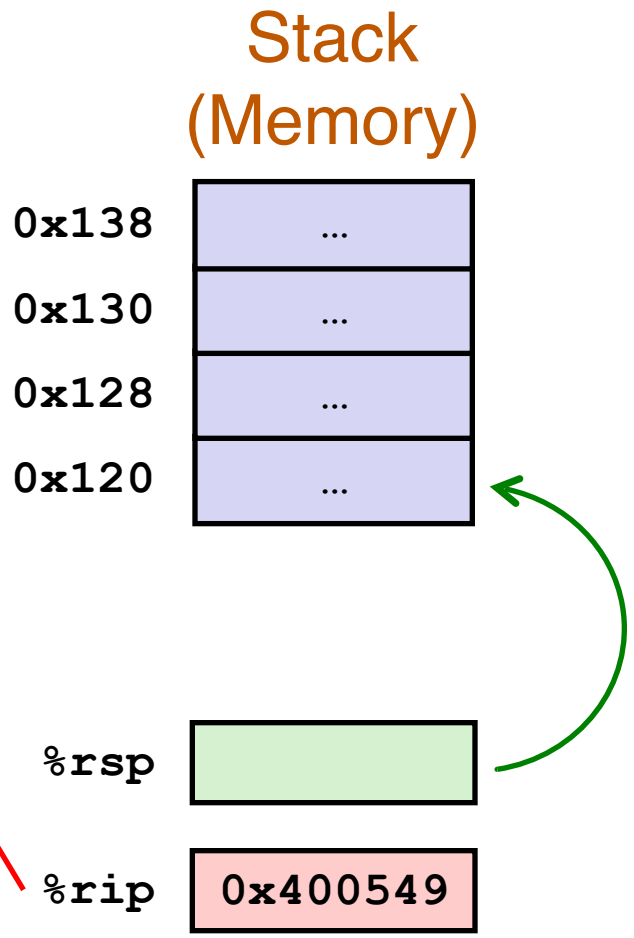
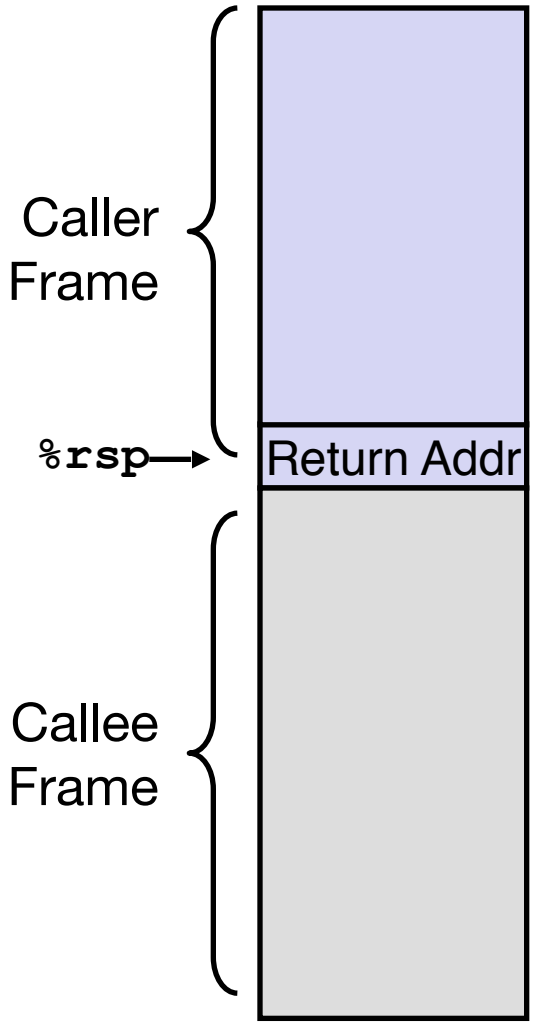


Stack Frame (So Far…)



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Passing Function Arguments

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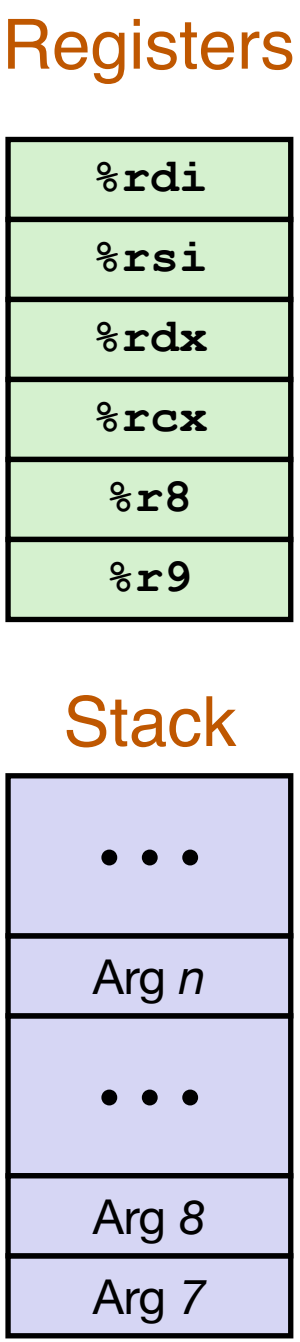
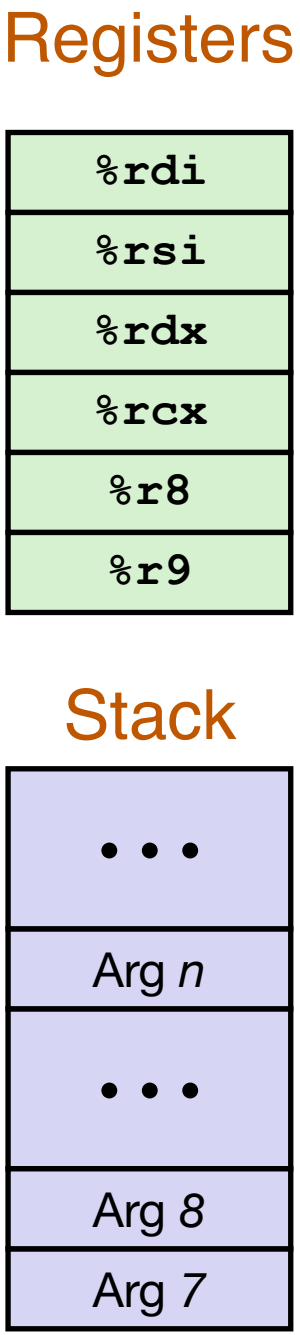


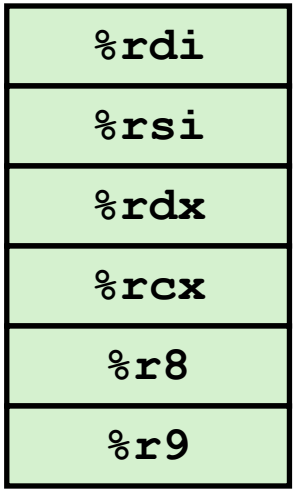
Passing Function Arguments

- Two choices: memory or registers
  - Registers are faster, but have limited amount
- x86-64 convention (Part of the *Calling Conventions*):
  - First 6 arguments in registers, in specific order
  - The rest are pushed to stack
  - *Return value is always in* `%rax`
- Just conventions, not laws
  - Not necessary if you write both caller and callee as long as the caller and callee agree
  - But is necessary to interface with others' code

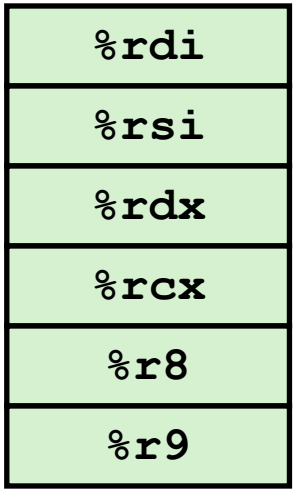


Function Call Data Flow Example

```
void multstore
 (long x, long y, long *res) {
    long t = mult2(x, y);
    *res = t;
}
…
long mult2
  (long a, long b)
{
  long s = a * b;
  return s;
}
```



Function Call Data Flow Example

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…
long mult2
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{
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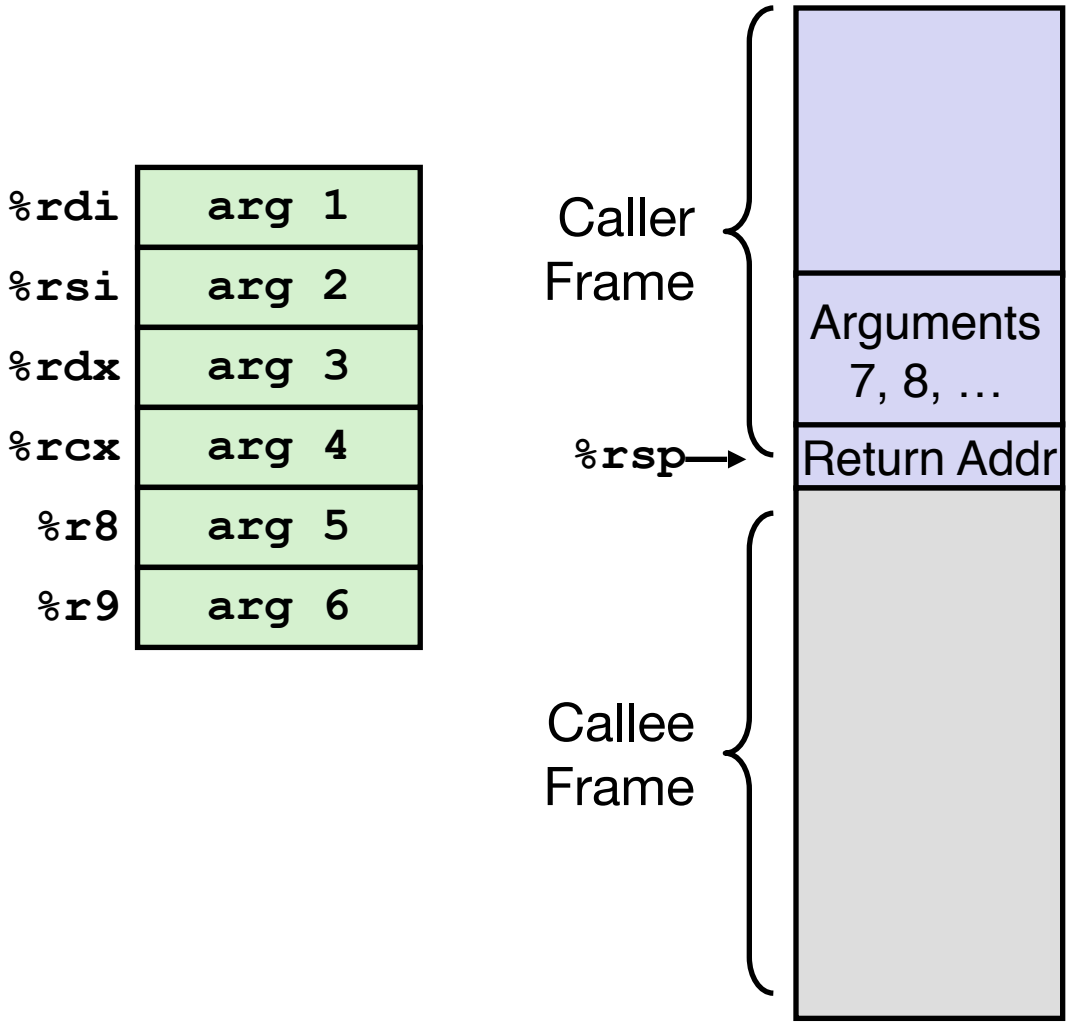
  return s;
}
```

```
0000000000400540 <multstore>:
  # x in %rdi, y in %rsi, res in %rdx
…
  400541: movq    %rdx,%rbx
  400544: callq  400550 <mult2>
  # t in %rax
  400549: movq    %rax,(%rbx)
…
0000000000400550 <mult2>:
  # a in %rdi, b in %rsi
  400550:  movq   %rdi,%rax
  400553:  imul   %rsi,%rax
  # s in %rax
  400557:  retq
```

Stack Frame (So Far…)



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Managing Function Local Variables

- Two ways: registers and memory (stack)
- Registers are faster, but limited. Memory is slower, but large. Smart compilers will optimize the usage.
- We will show different uses. Compiler optimizations later in the course.

```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

Register Example: `incr`

Register	Use(s)
`%rdi`	Argument `p`
`%rsi`	Argument `val`, `y`
`%rax`	`x`, Return value

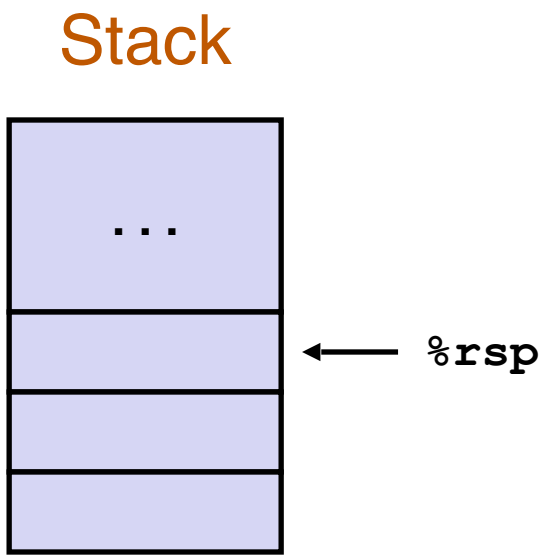
```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

```
incr:
  movq    (%rdi), %rax
  addq    %rax, %rsi
  movq    %rsi, (%rdi)
  ret
```

Stack Example: `call_add`

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
  subq    $16, %rsp
  movq    $15213, (%rsp)
  movq    $3000, 8(%rsp)
  leaq    (%rsp), %rdi
  leaq    8(%rsp), %rsi
  call    add
  addq    8(%rsp), %rax
  addq    $16, %rsp
  ret
```



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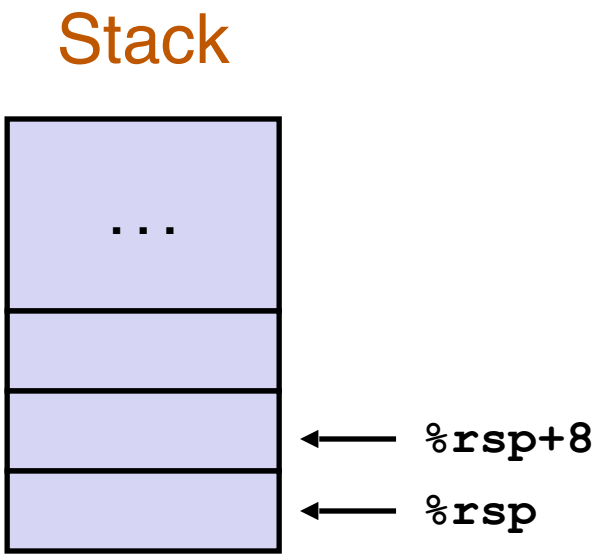

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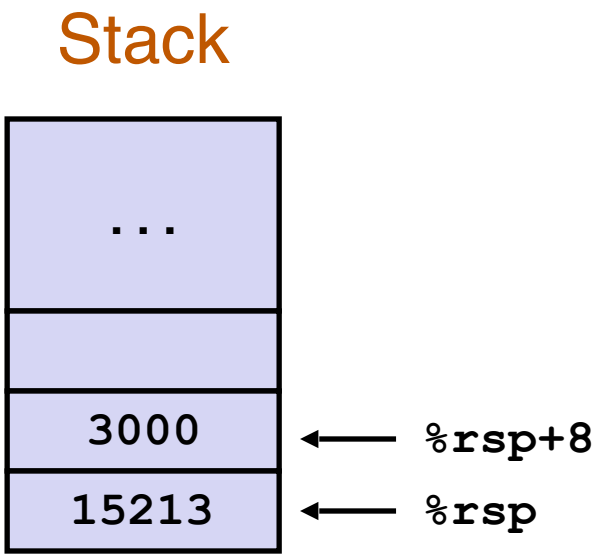

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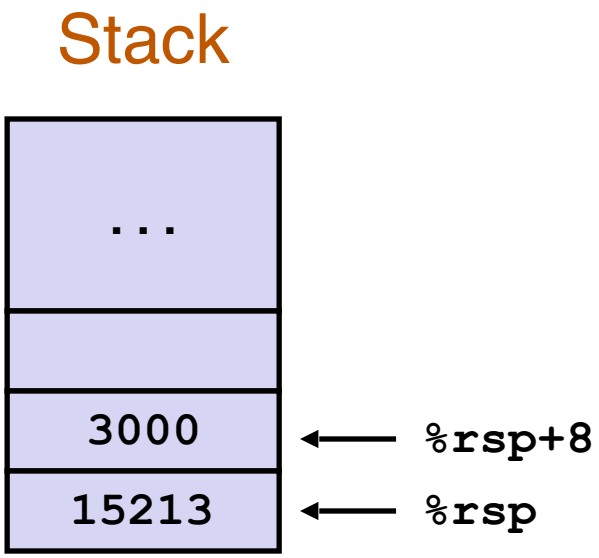

Register	Value(s)
%rdi	&v1
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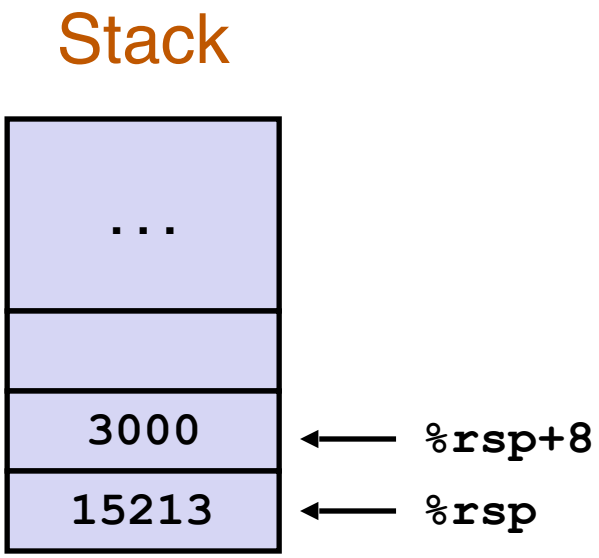

Register	Value(s)
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  call    add
  addq    8(%rsp), %rax
  addq    $16, %rsp
  ret
```

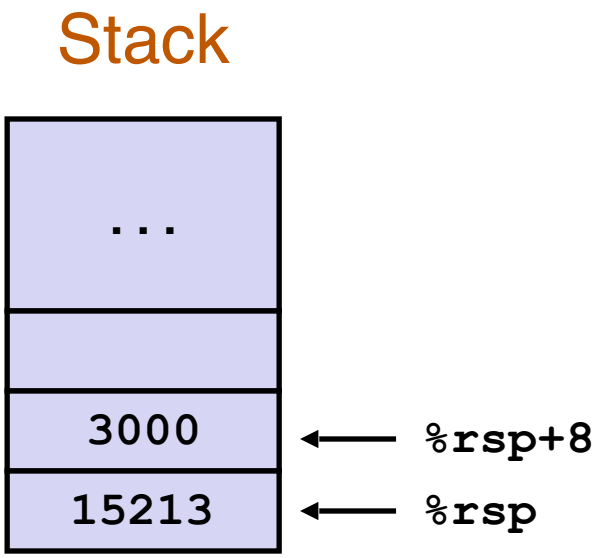

Register	Value(s)
%rdi	&v1
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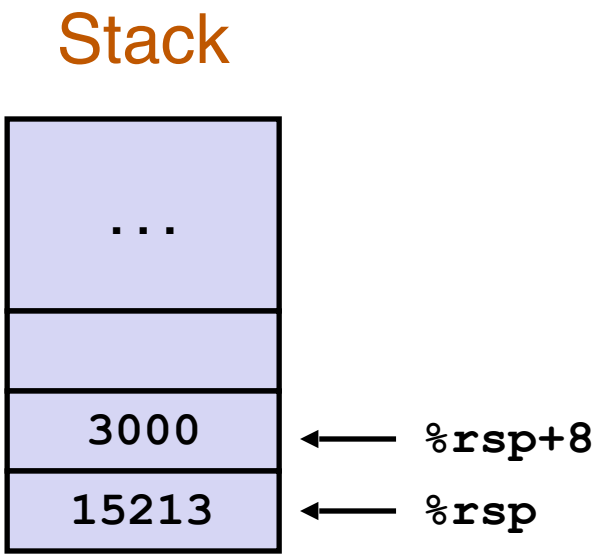

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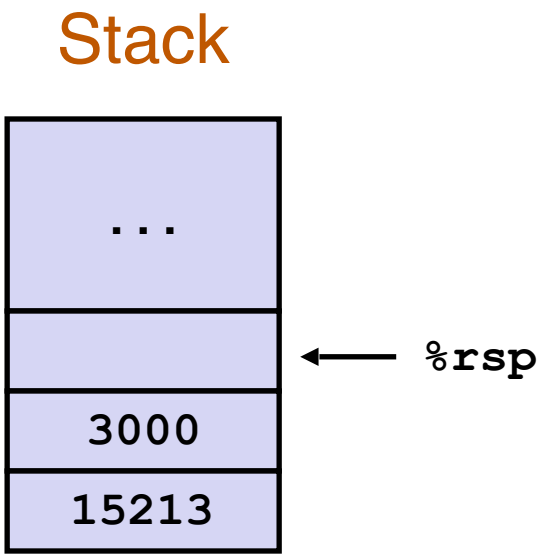

Stack Frame (So Far…)