CSC 252: Computer Organization
Spring 2022: Lecture 2

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Department of Computer Science
University of Rochester

Announcement

- Make sure you can access CSUG machines!!!
- Programming assignment 1 will be posted today.
  - I will send an announcement when it's out.
  - It is in C language. Seek help from TAs.
  - TAs are best positioned to answer your questions about programming assignments!!!
- Programming assignments do NOT repeat the lecture materials. They ask you to synthesize what you have learned from the lectures and work out something new.

Previously in 252…

Problem

Algorithm

Program

Instruction Set Architecture (ISA)

Microarchitecture

Circuit

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Instruction Set Architecture (ISA)

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ISA is the contract between software and hardware.

Previously in 252…

Problem

Algorithm

	Renting
Service provider	Landlord
Service receiver	YOU
Contract	Lease
Contract's language	Natural language (e.g., English)

Circuit

Previously in 252…

Problem

Algorithm

	Renting	Computing
Service provider	Landlord	Hardware
Service receiver	YOU	Software
Contract	Lease	ISA
Contract's language	Natural language (e.g., English)	Assembly programming language

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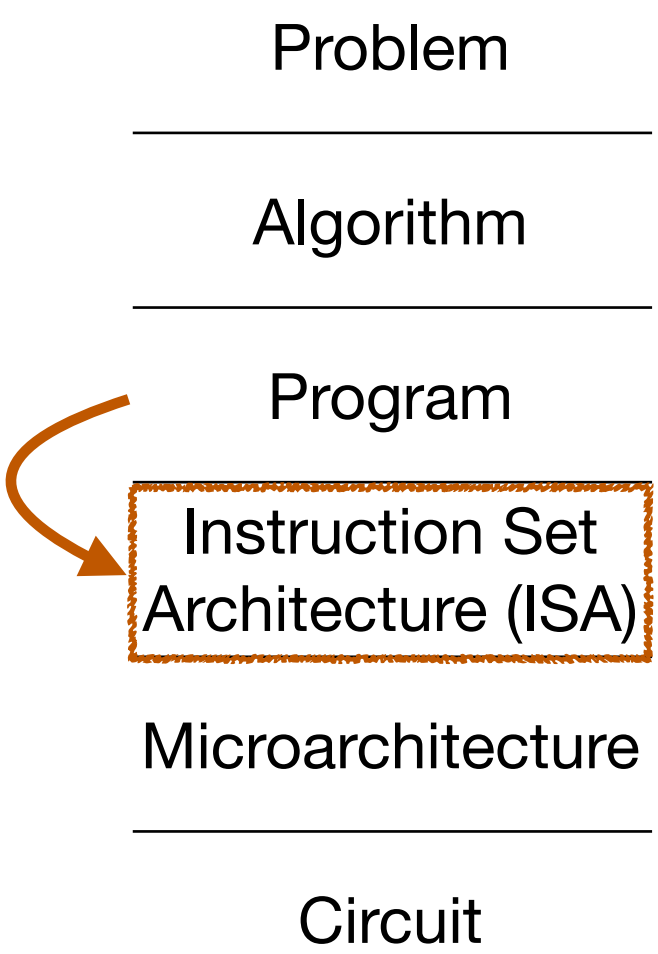
Circuit

Previously in 252…

- How is a human-readable program translated to a representation that computers can understand?



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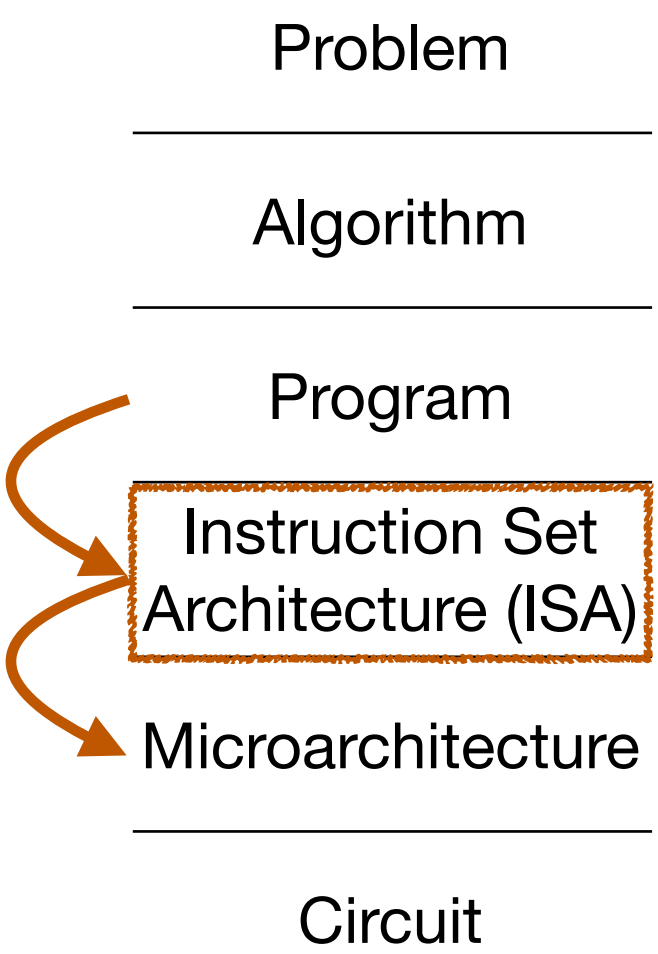
Previously in 252…

- How is a human-readable program translated to a representation that computers can understand?
- How does a modern computer execute that program?



ISA is the contract between software and hardware.

Previously in 252…

C Program

```
void add() {
  int a = 1;
  int b = 2;
  int c = a + b;
}
```

Assembly program

```
movl    $1, -4(%rbp)
movl    $2, -8(%rbp)
movl    -4(%rbp), %eax
addl    -8(%rbp), %eax
```

Previously in 252…

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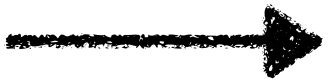

Executable Binary

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00011001   …
01101010   …
11010101   …
01110001   …
```

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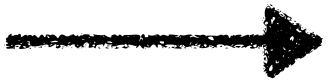

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- What's the difference between an assembly program and an executable binary?
  - They refer to the same thing — a list of instructions that the software asks the hardware to perform
  - They are just different representations
- Instruction = Operator + Operand(s)

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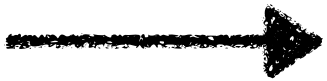

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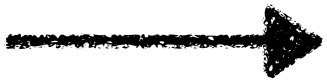

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Today: Representing Information in Binary

- Why Binary (bits)?
- Bit-level manipulations
- Integers
  - Representation: unsigned and signed
  - Conversion, casting
  - Expanding, truncating
  - Addition, negation, multiplication, shifting
  - Summary
- Representations in memory, pointers, strings

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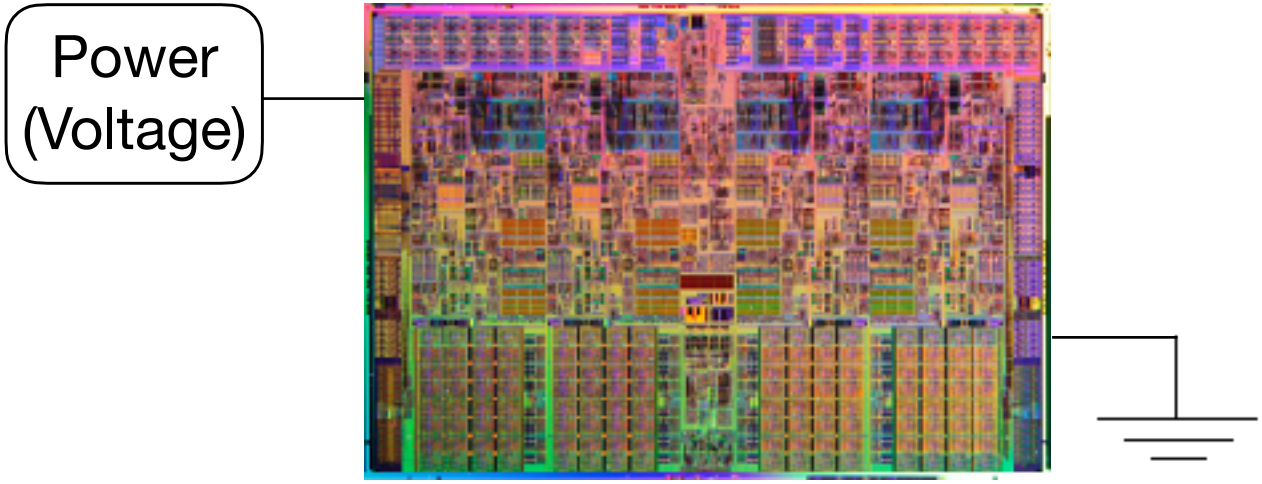

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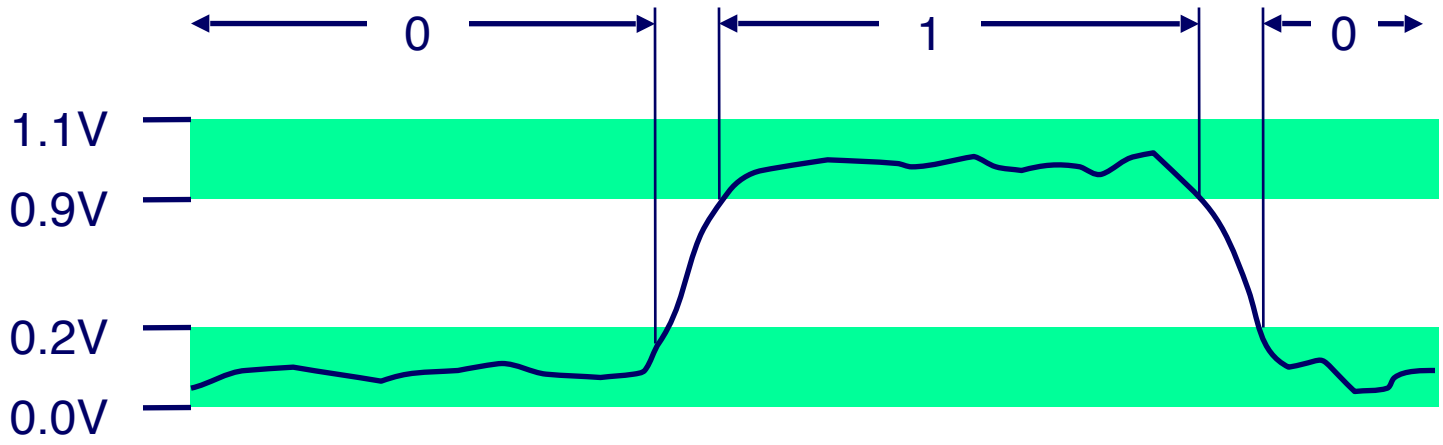

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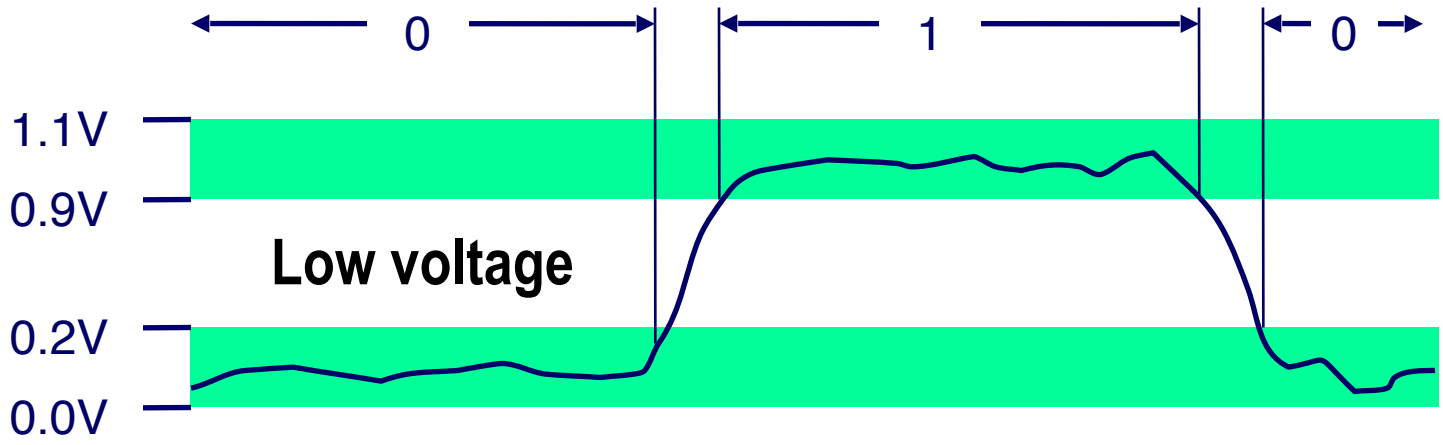

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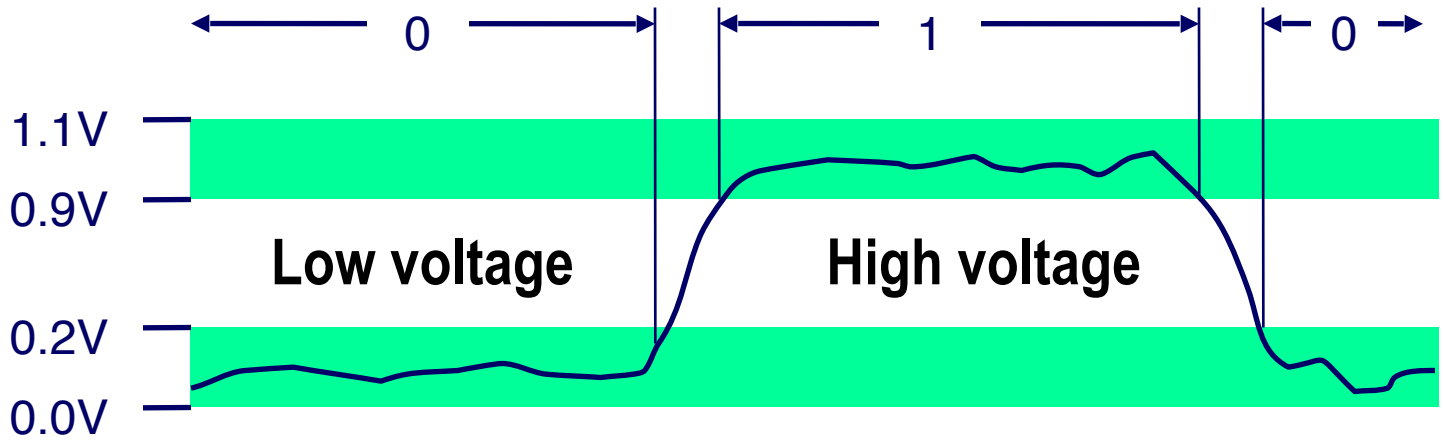

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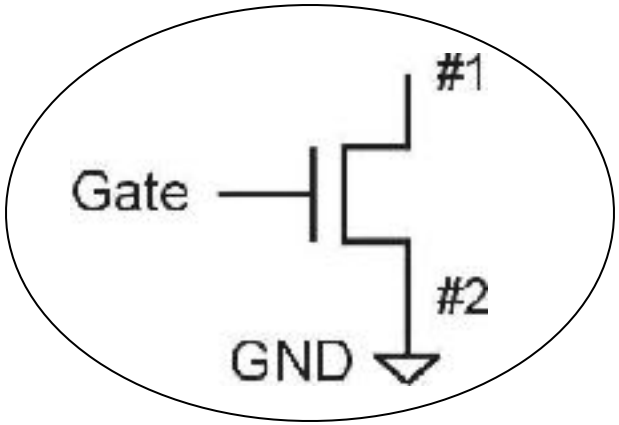

Terminal #2 must be connected to GND (0V).

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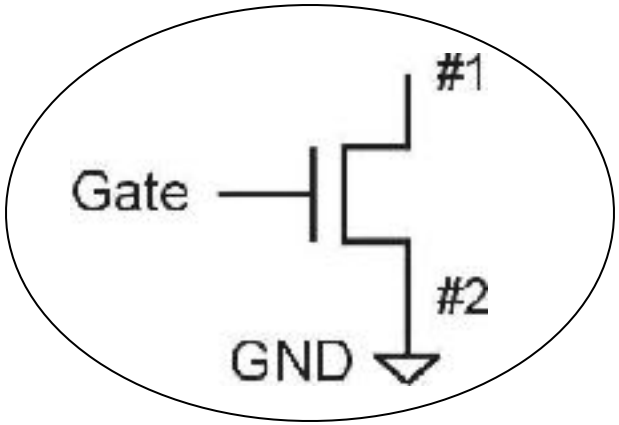

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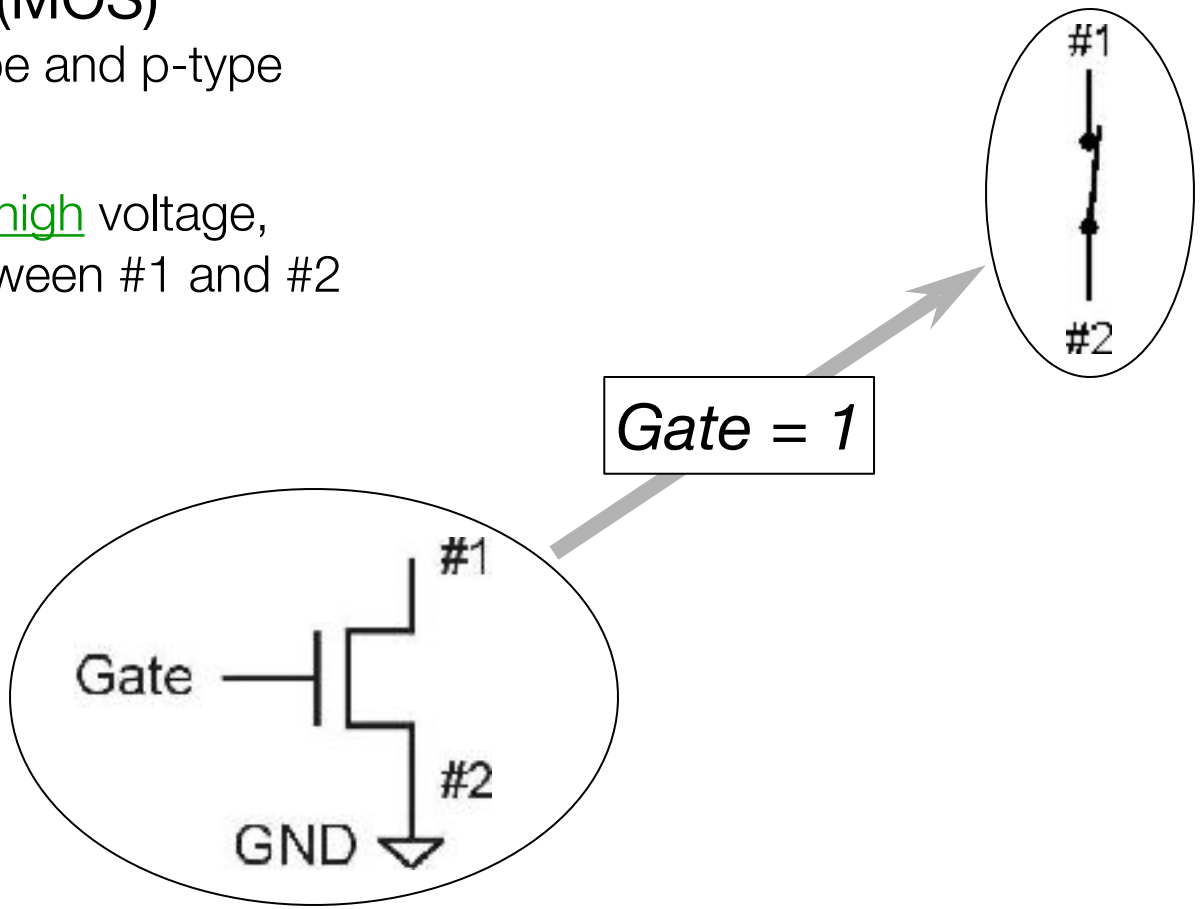

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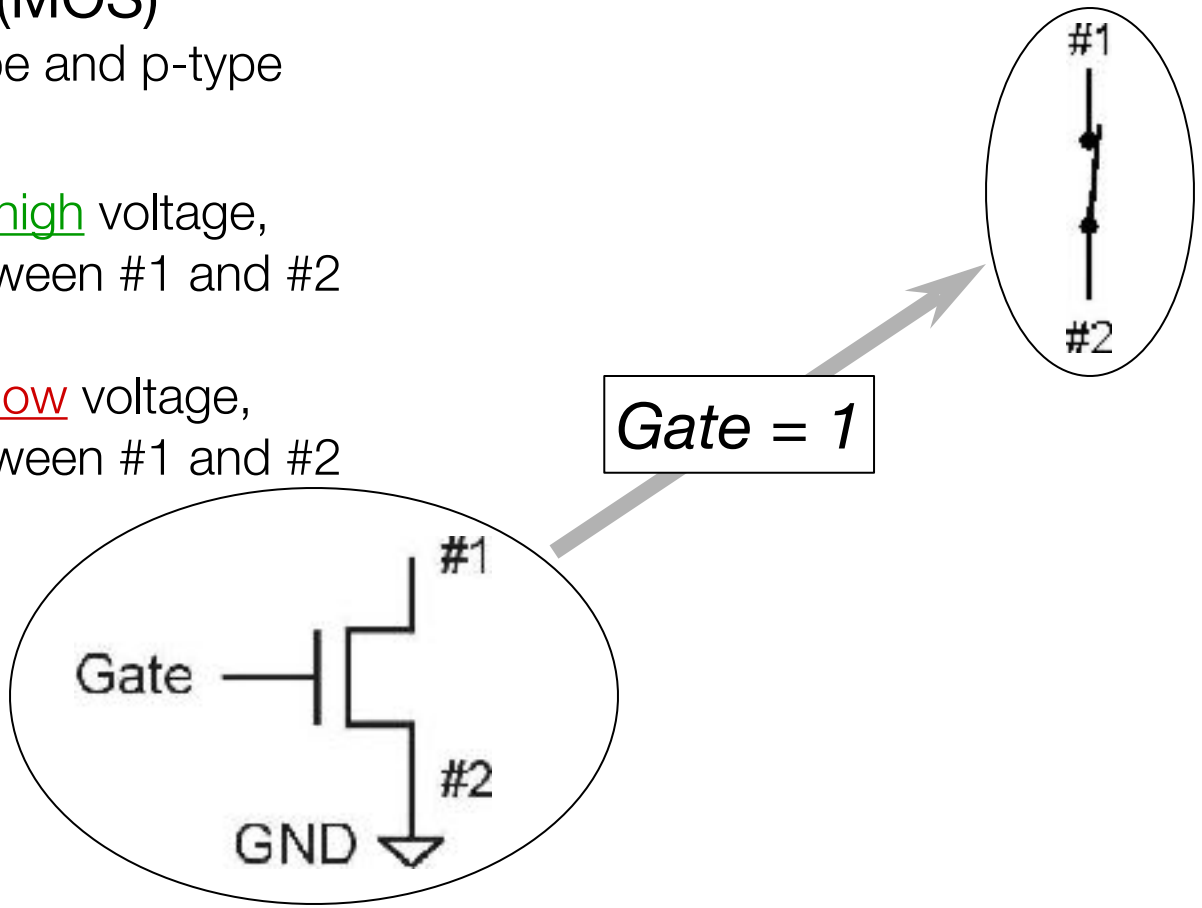

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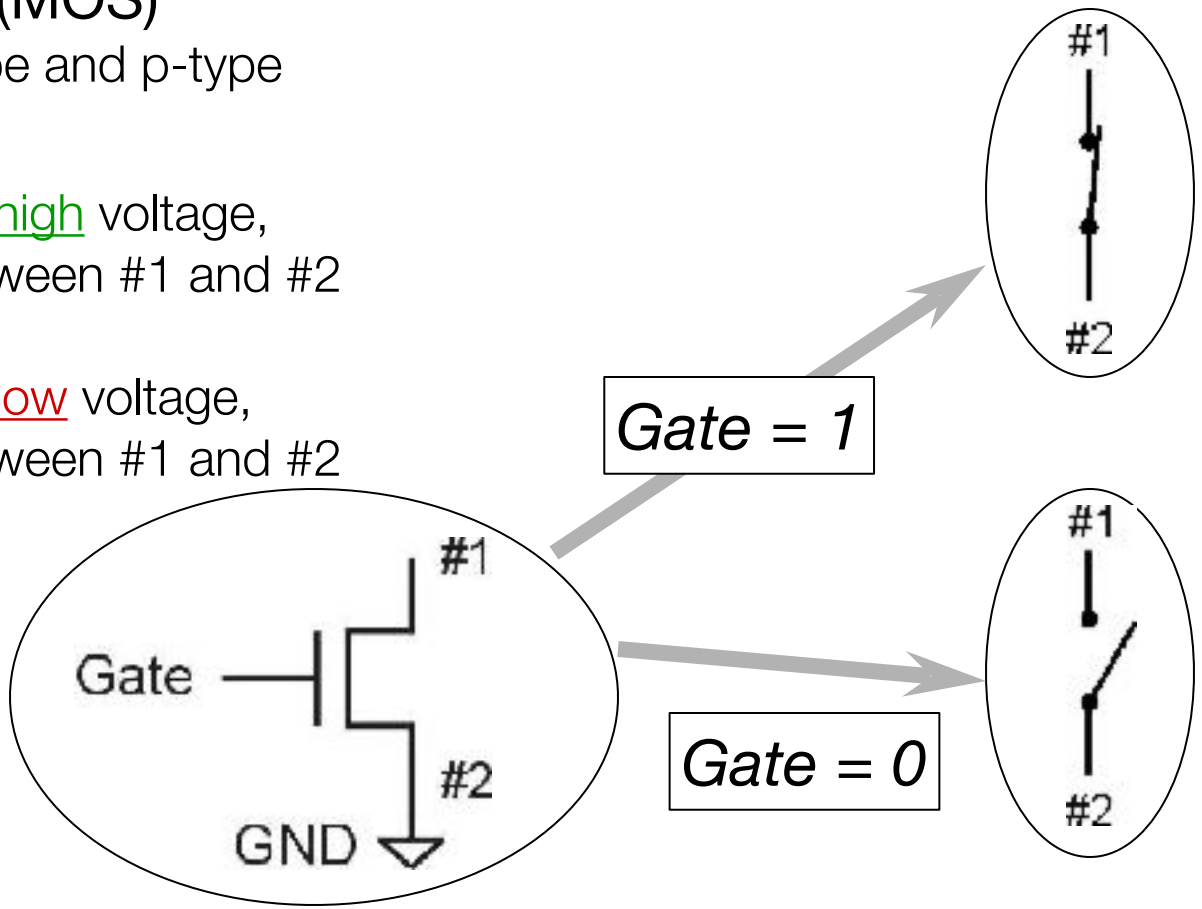

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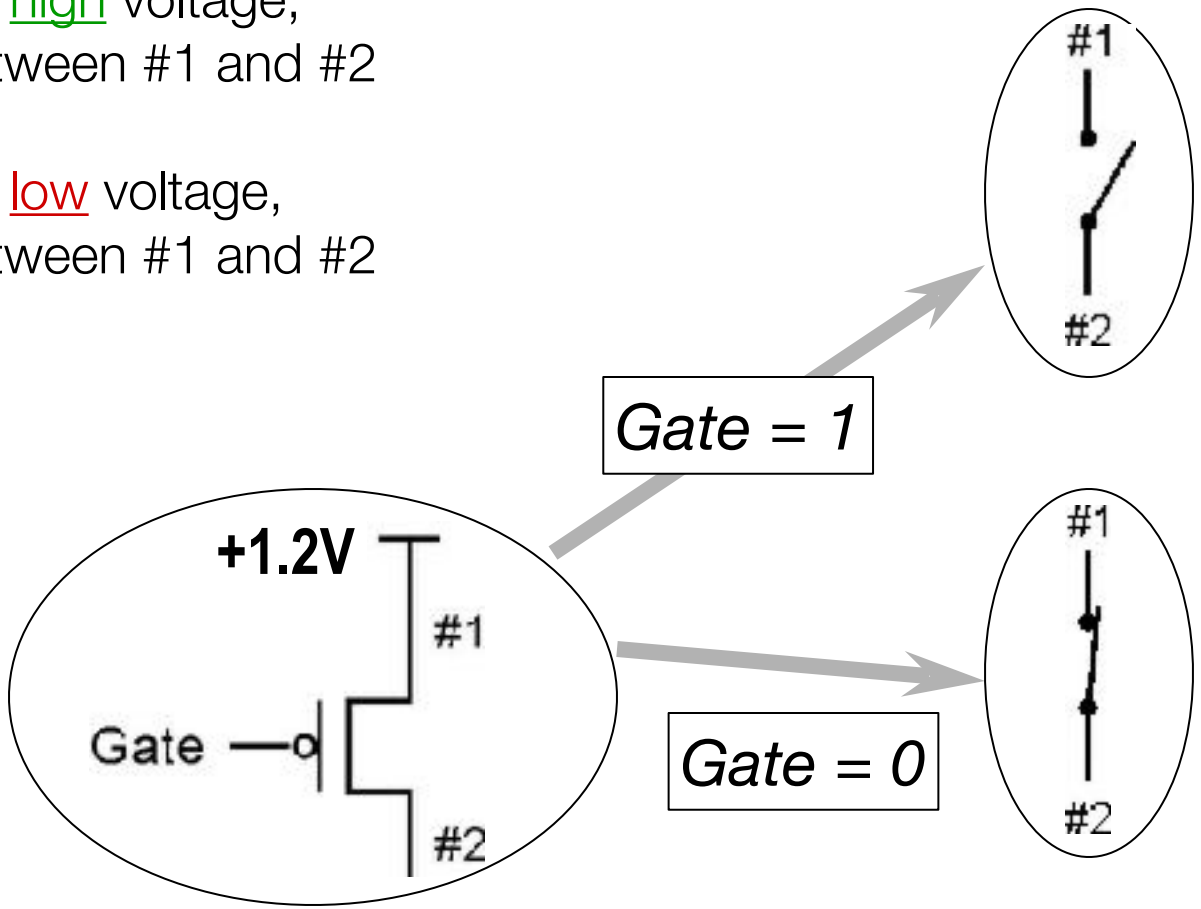
Why Bits?

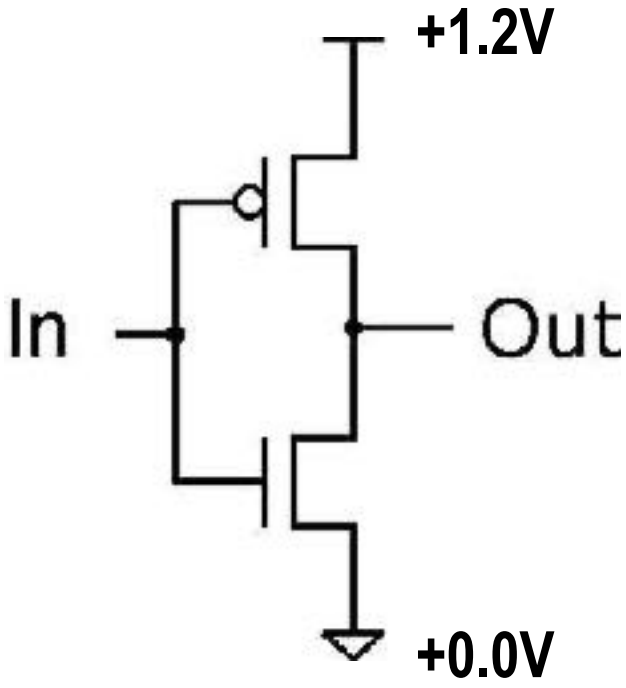
p-type is *complementary* to n-type (PMOS)

- when Gate has high voltage,
  open circuit between #1 and #2
  (switch open)
- when Gate has low voltage,
  short circuit between #1 and #2
  (switch closed)



Terminal #1 must be
connected to +1.2V

Inverter



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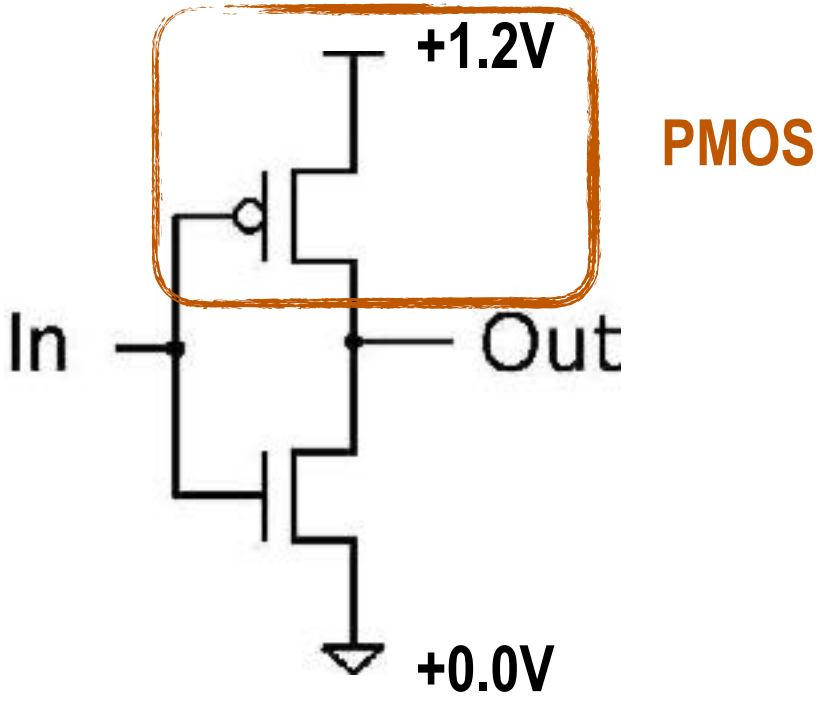

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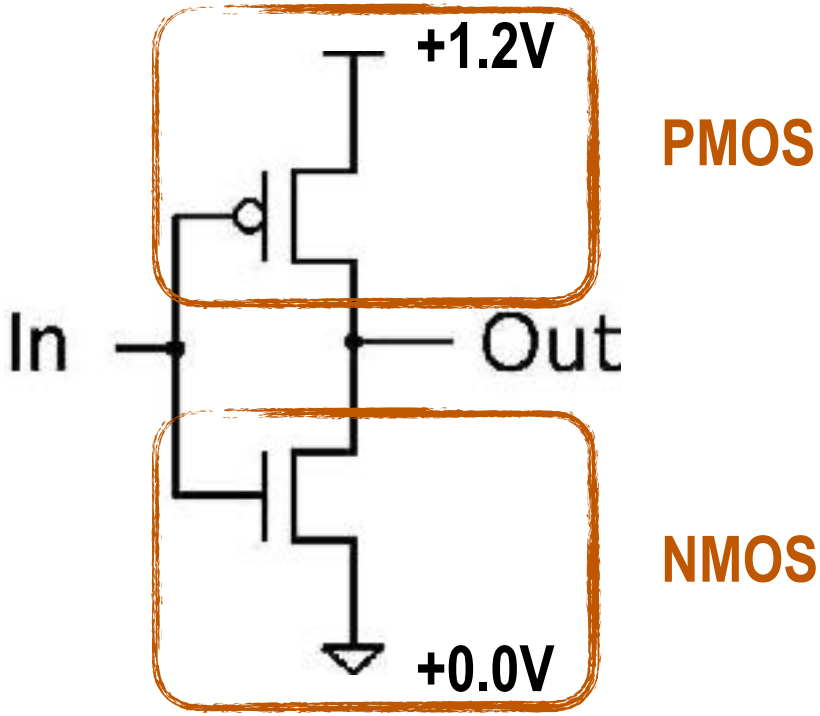

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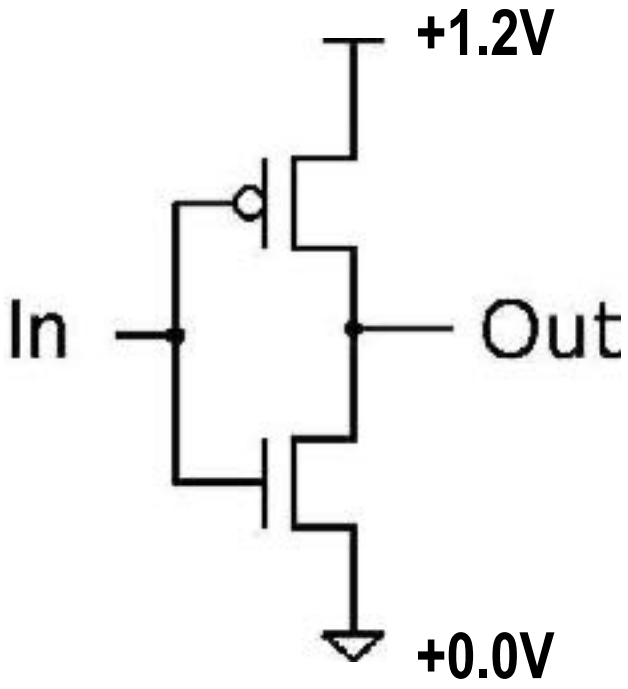

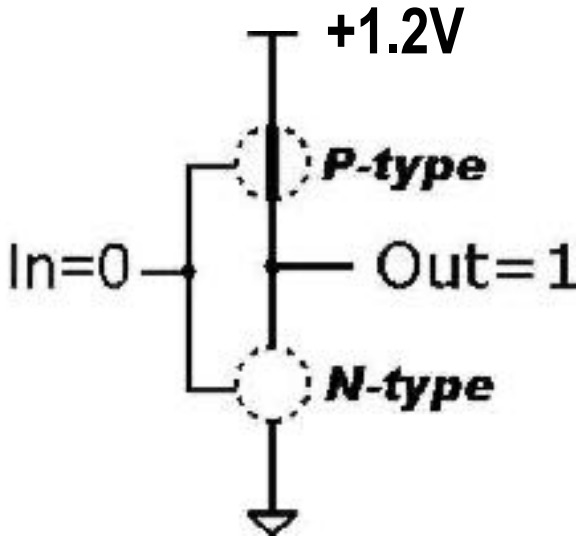

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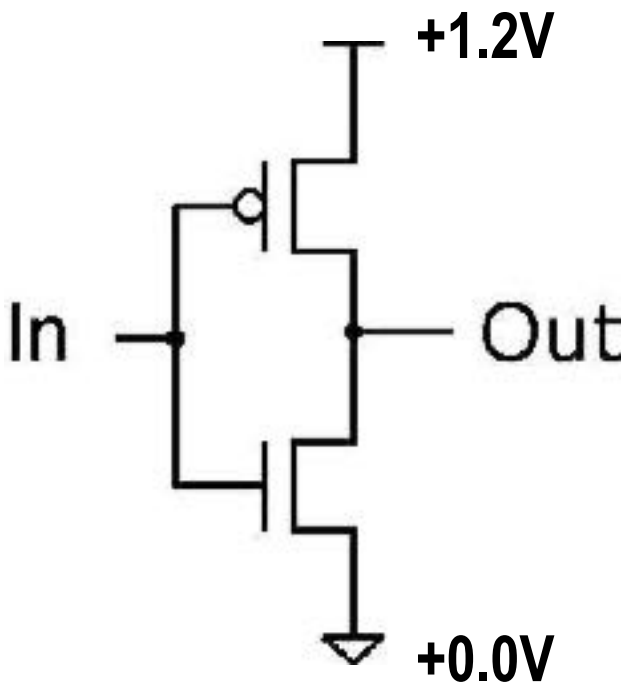
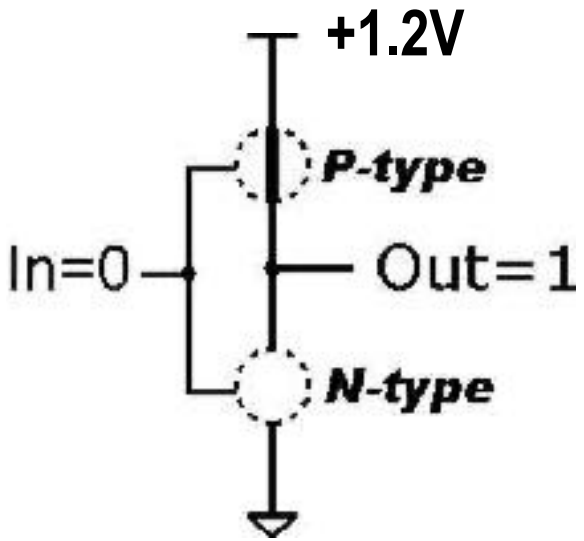
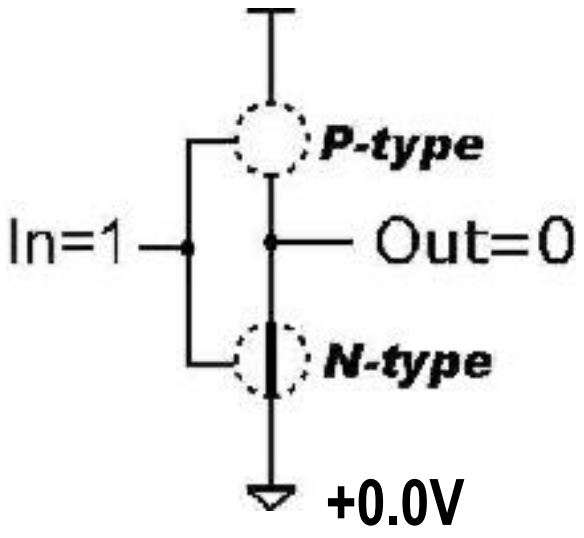

+1.2V
In
Out
+0.0V


+1.2V
P-type
In=0
Out=1
N-type


P-type
In=1
Out=0
N-type
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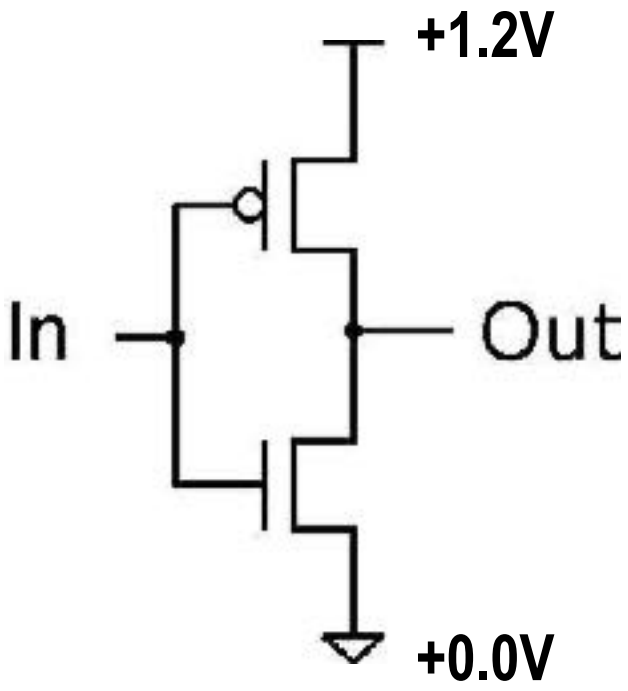
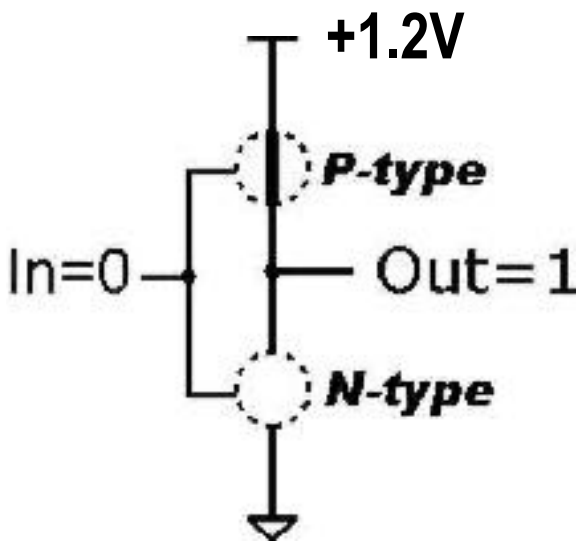
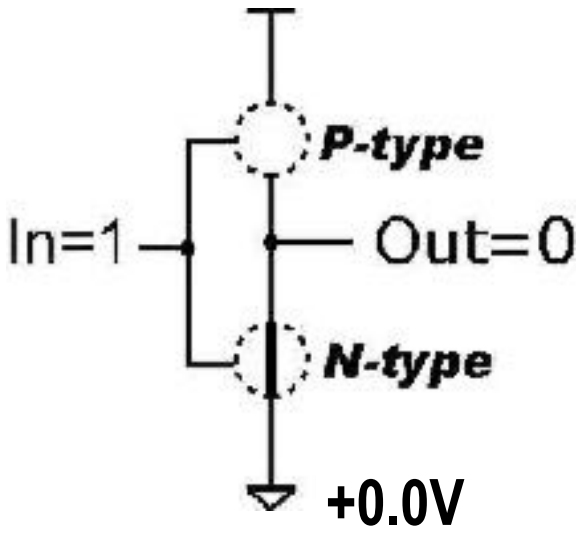


In	Out
0	1
1	0





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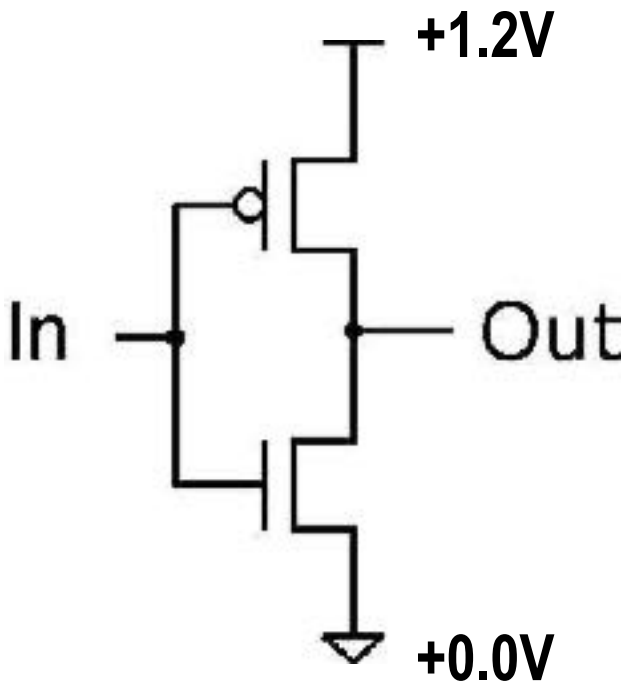

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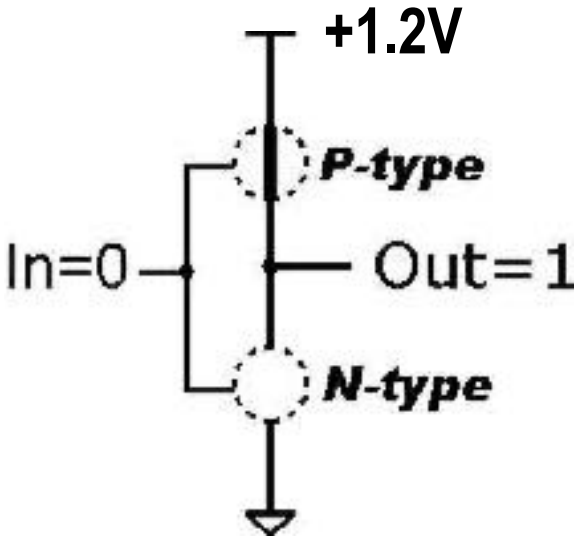
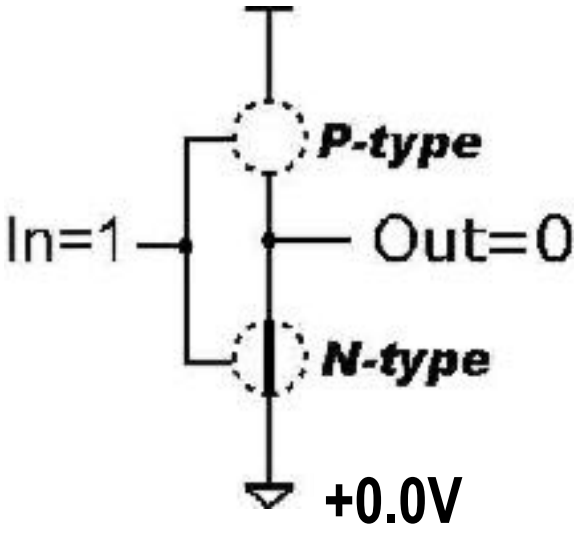
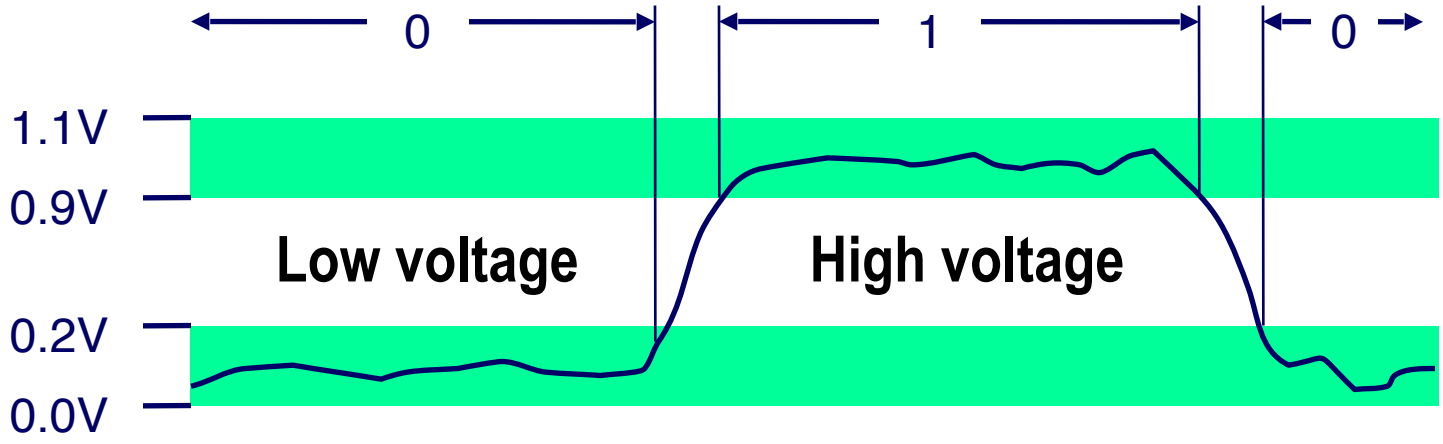
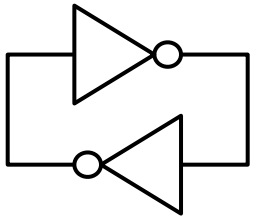




Store/Access Data





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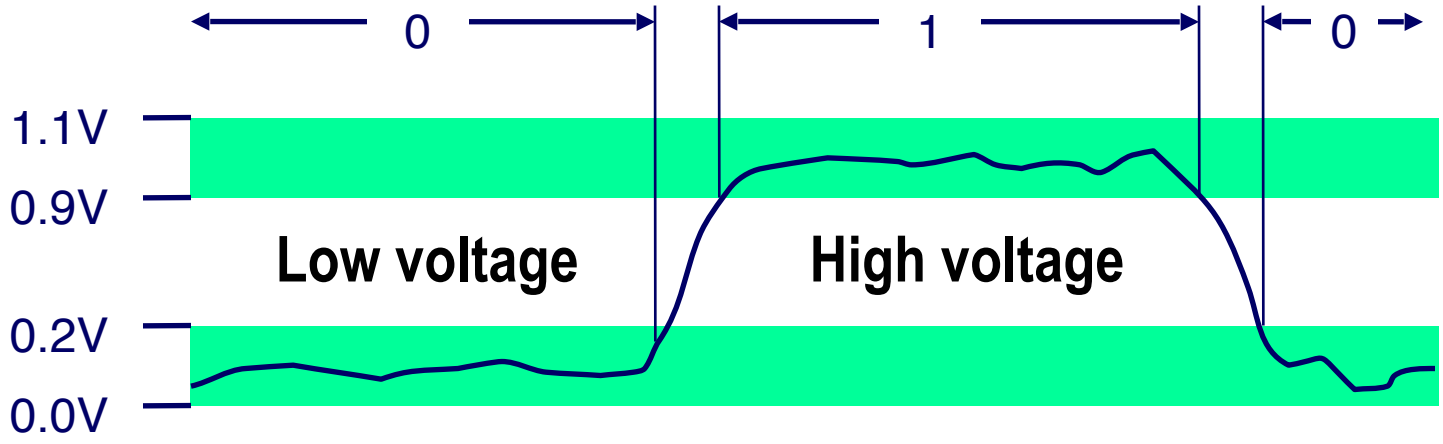

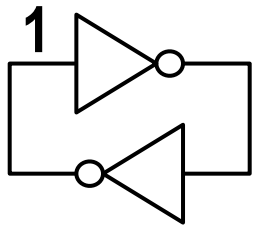

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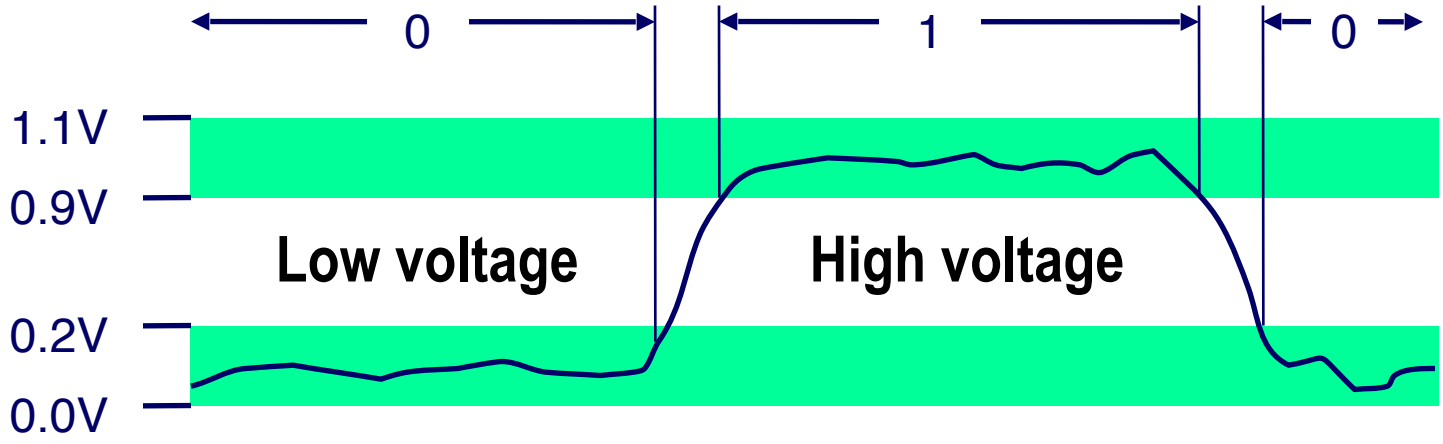

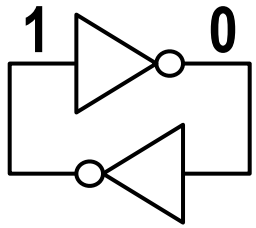

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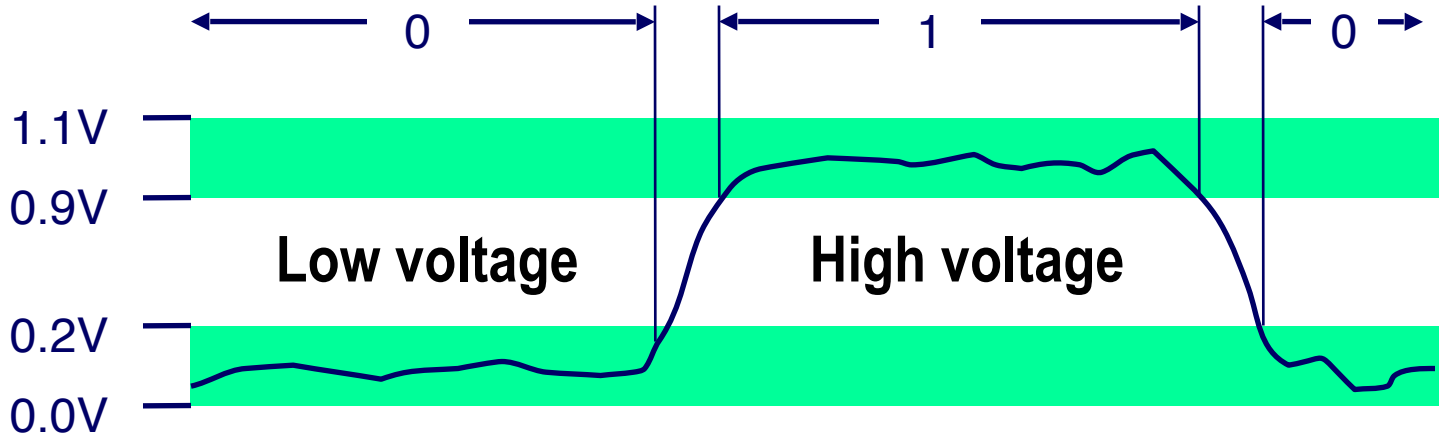

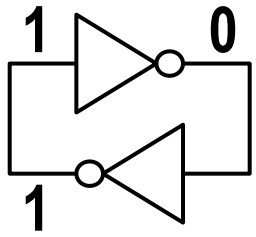

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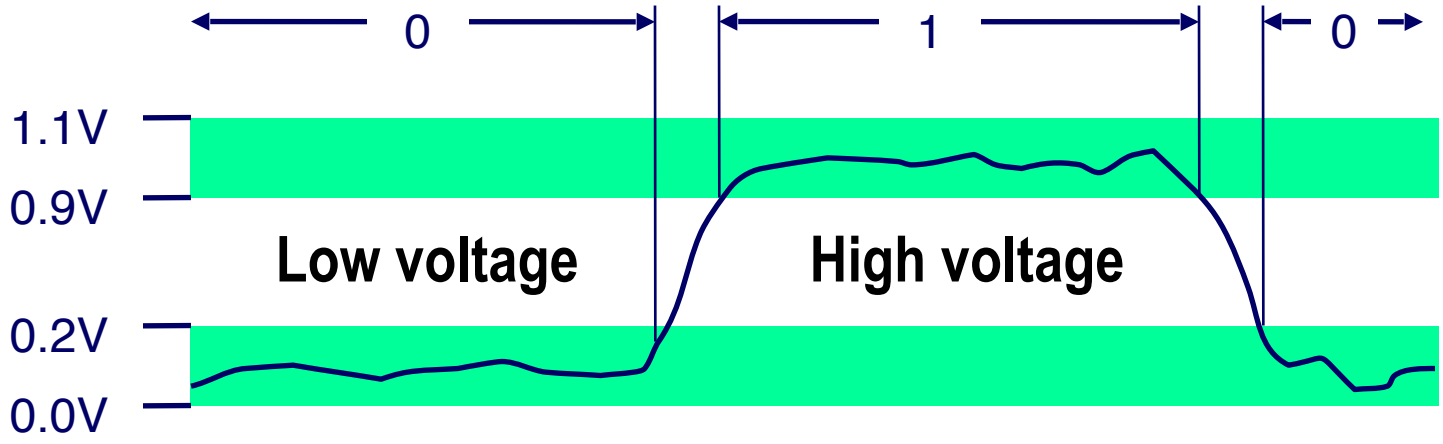
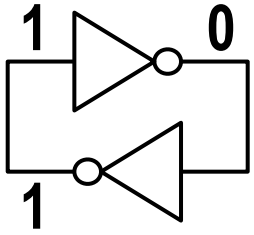


- Two cross coupled inverters store a single bit
  - Feedback path persists the value in the “cell”



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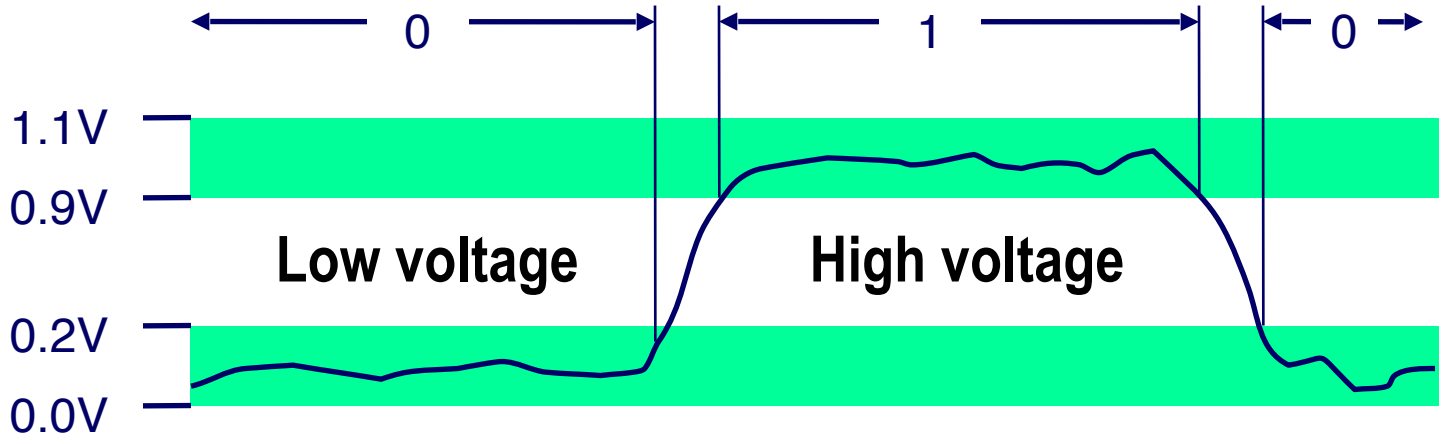

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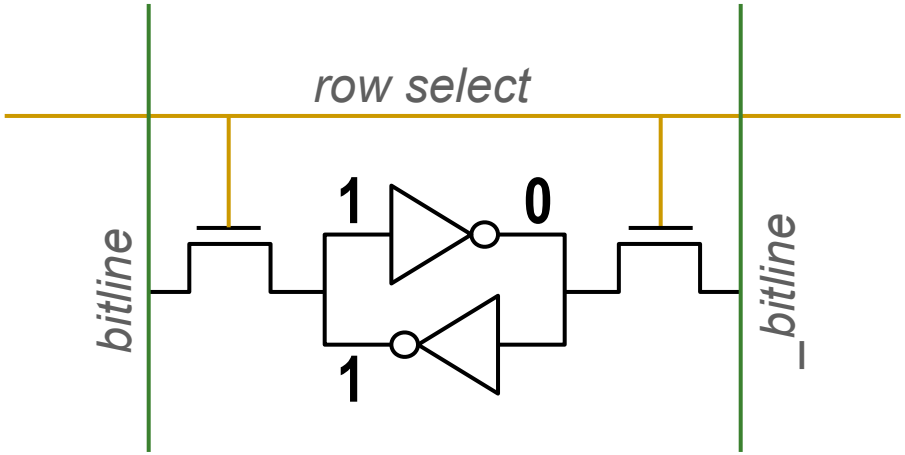

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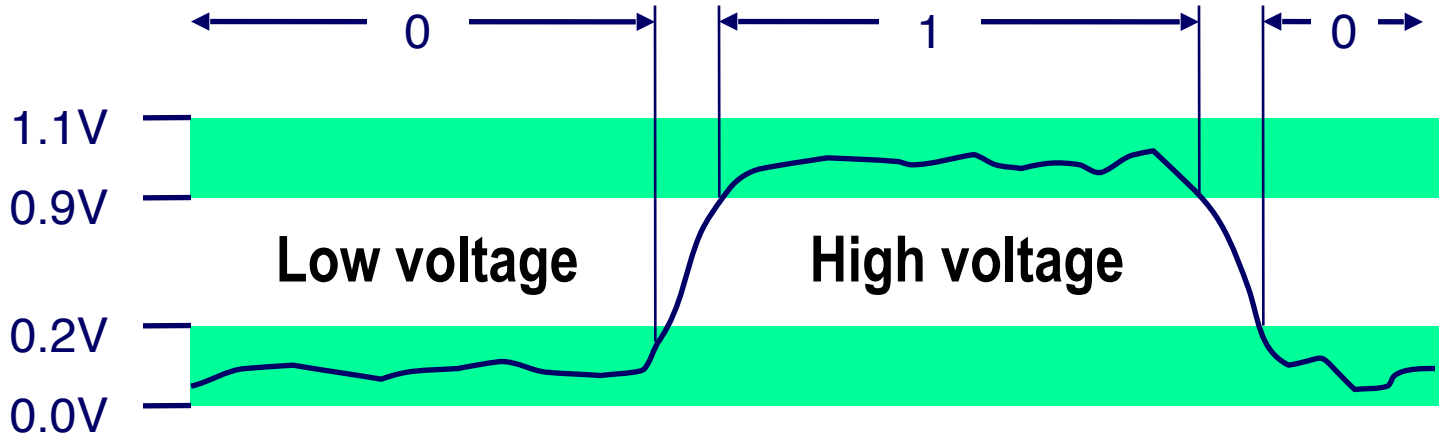

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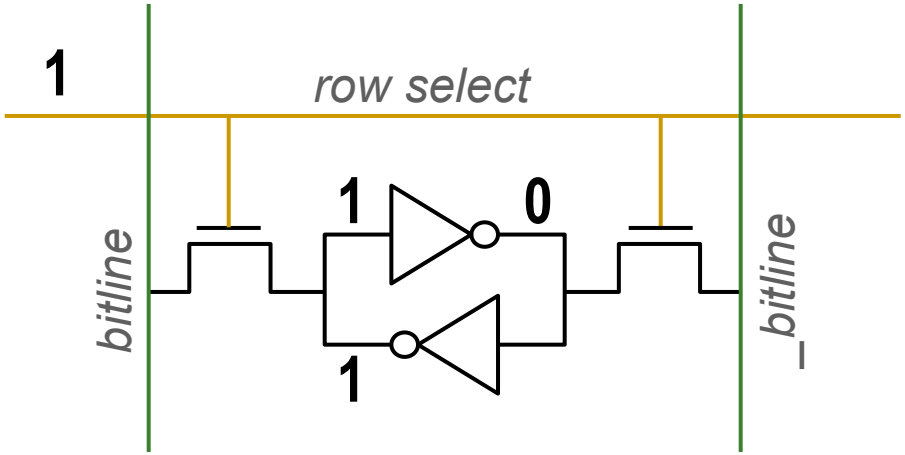

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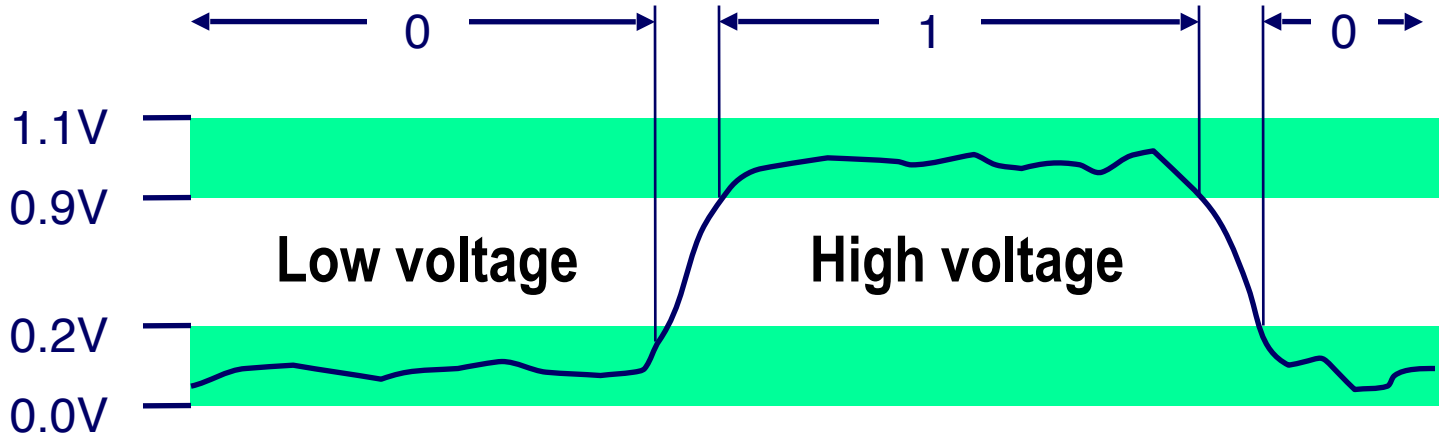

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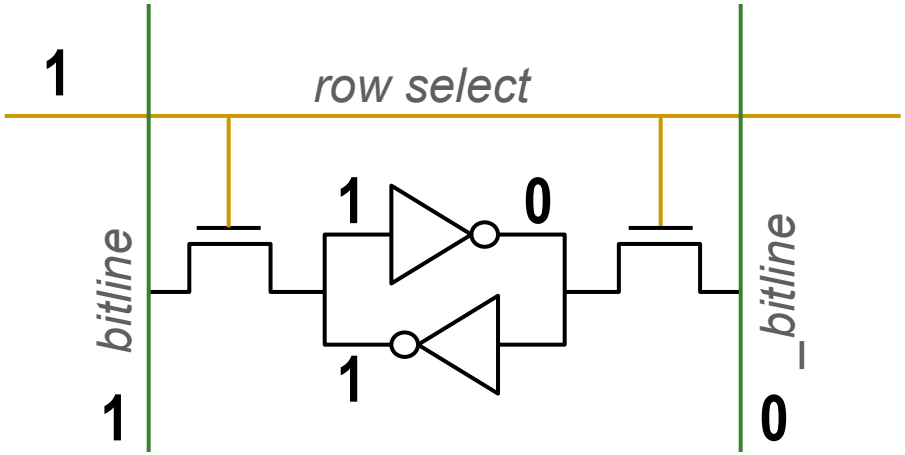

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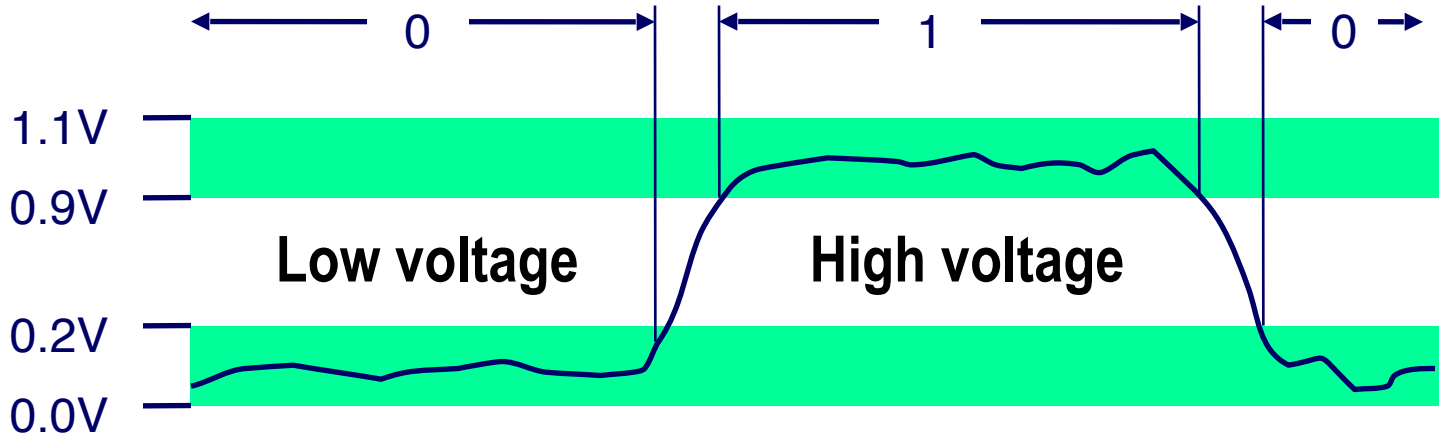

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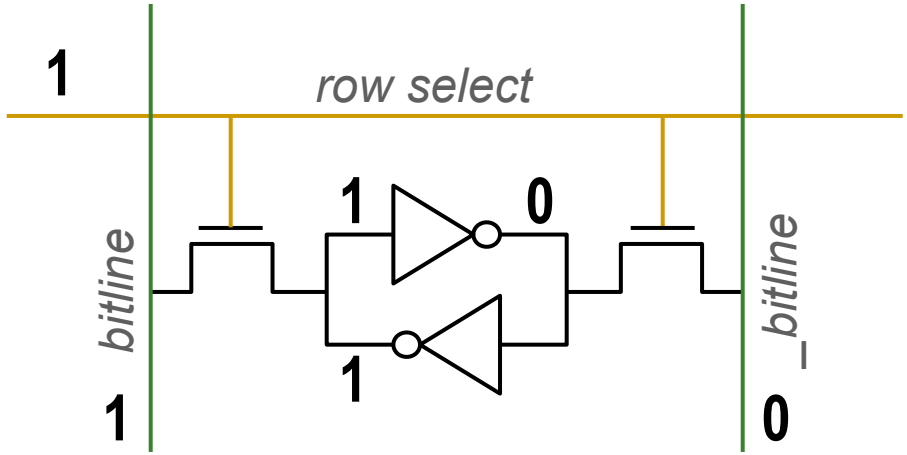

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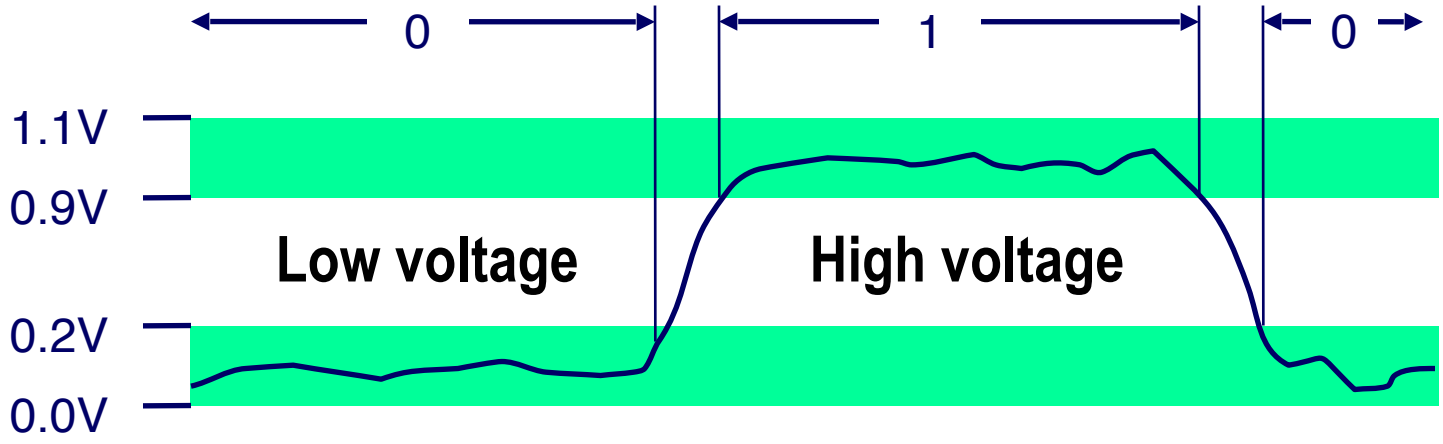

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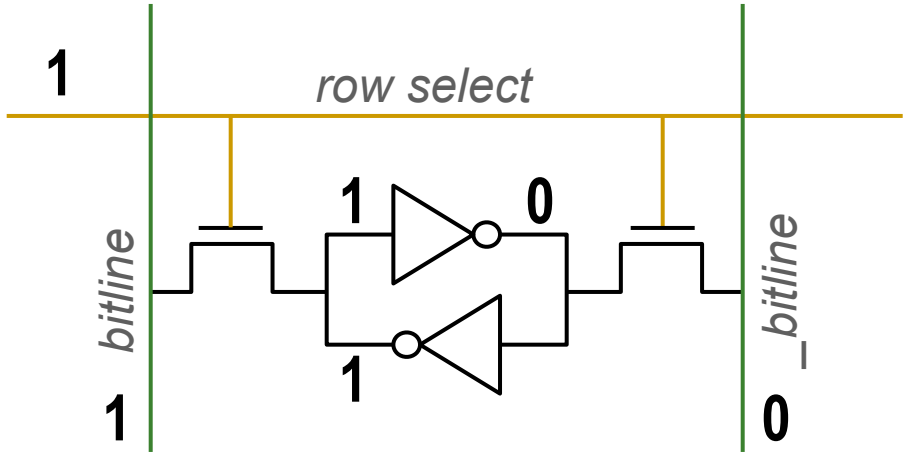

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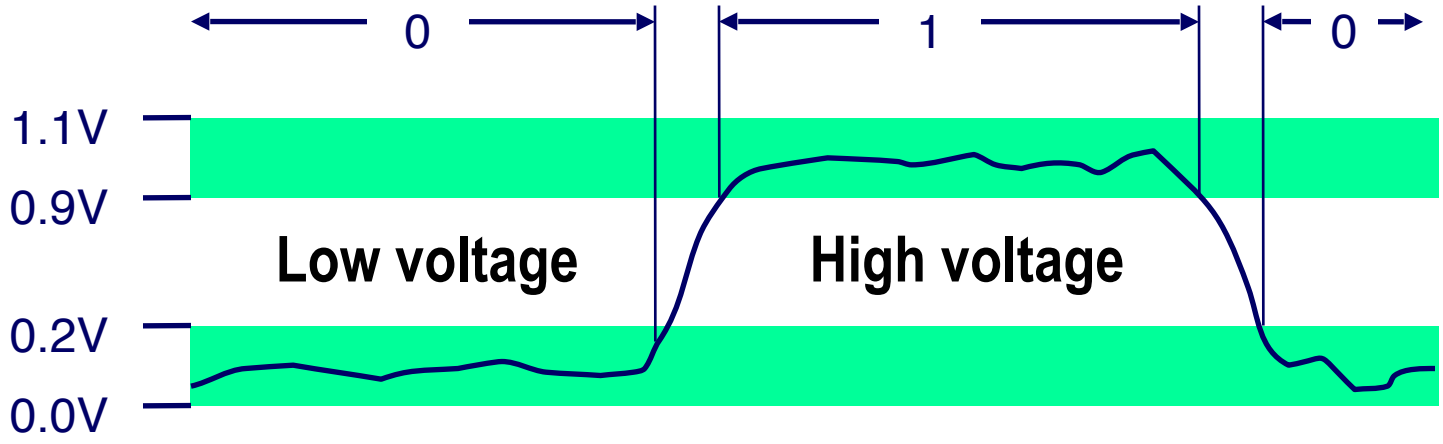

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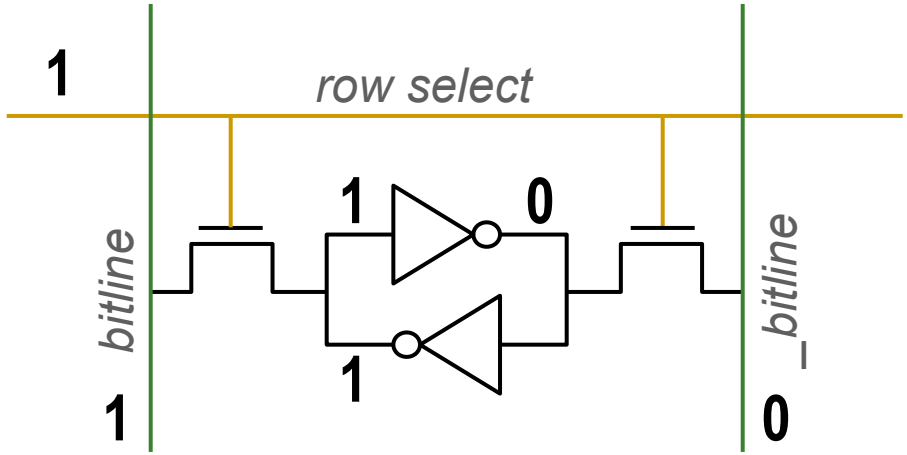

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- Transistors have become smaller over the years
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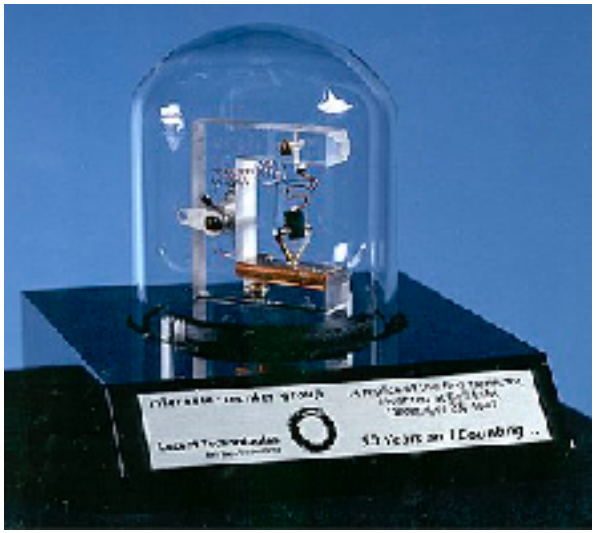

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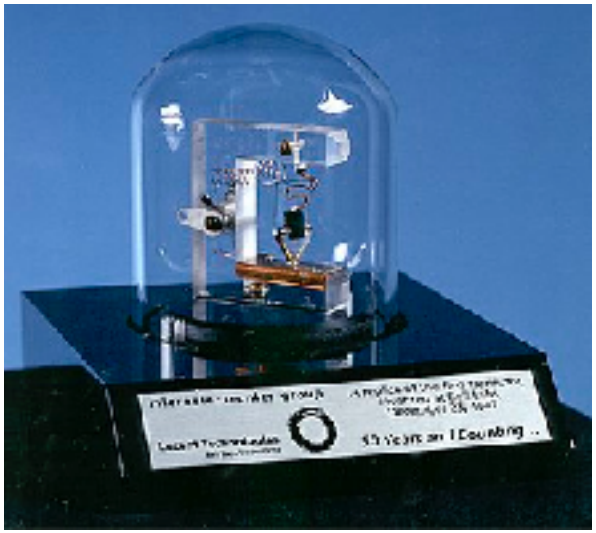
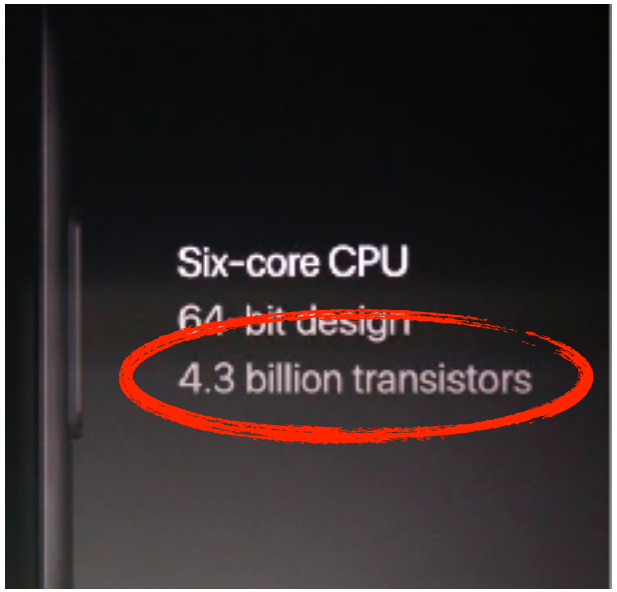
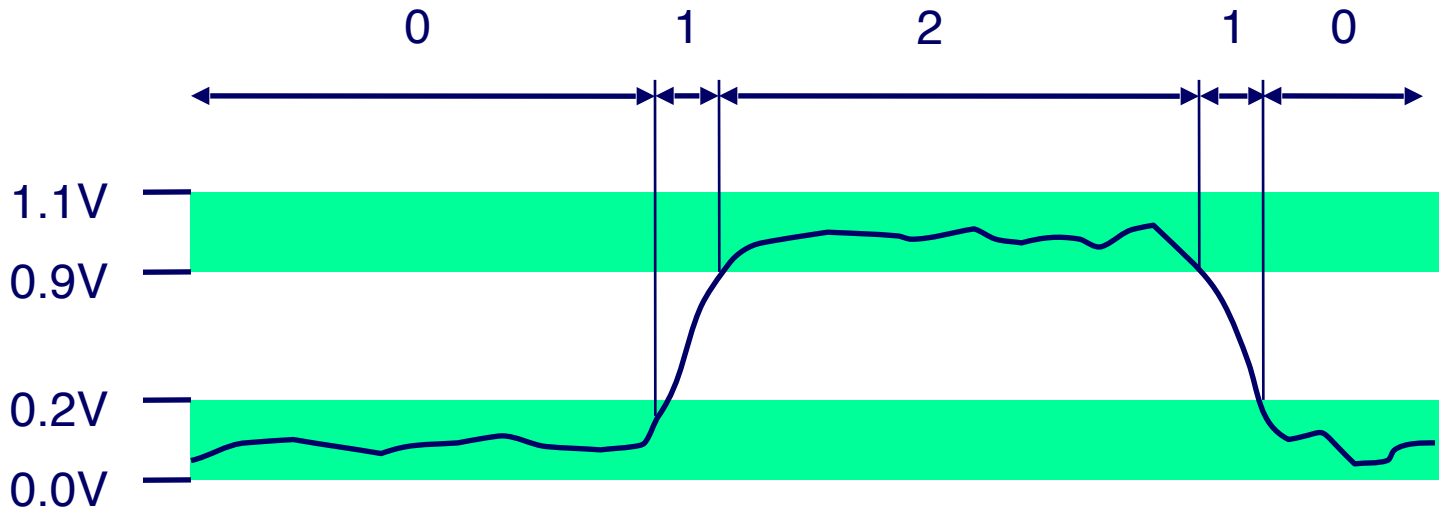




Why Limit Ourselves Only to Binary?

- Voltage is continuous. Why interpret it only as 0s and 1s?



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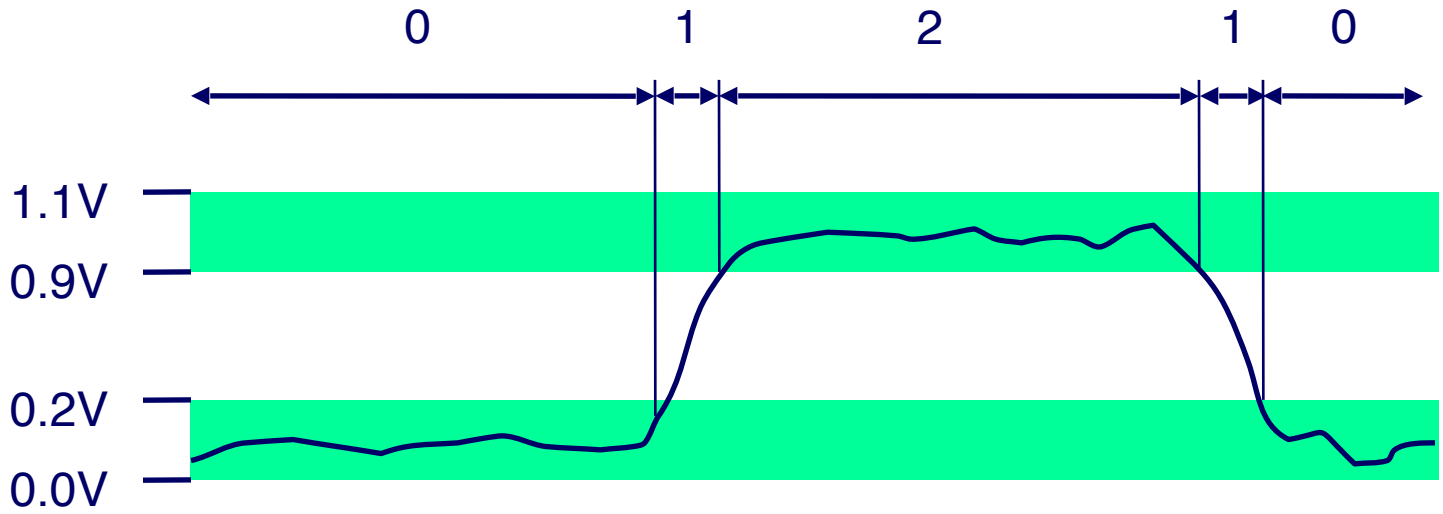

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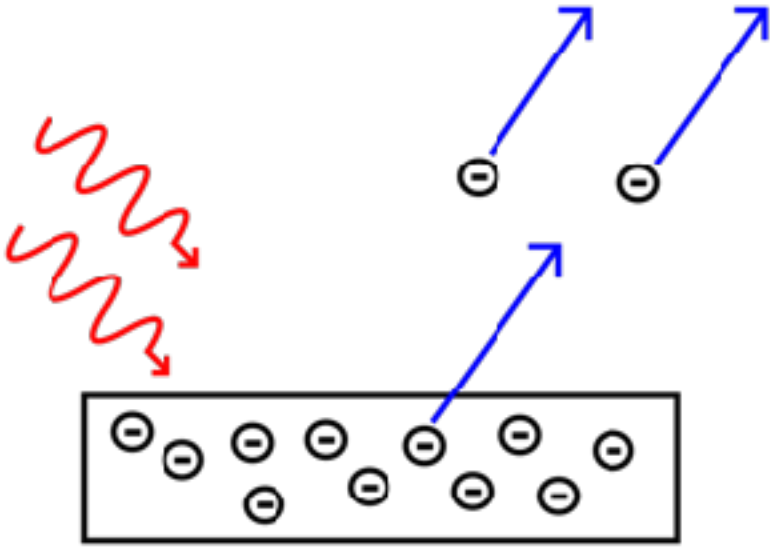

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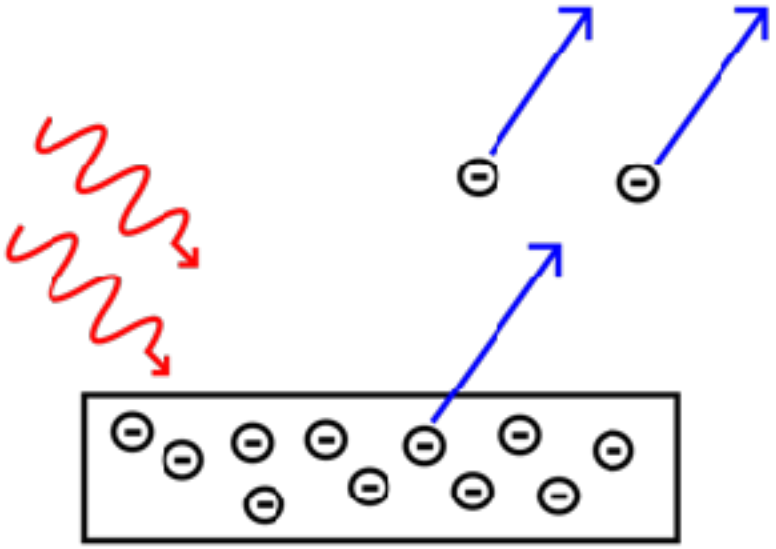

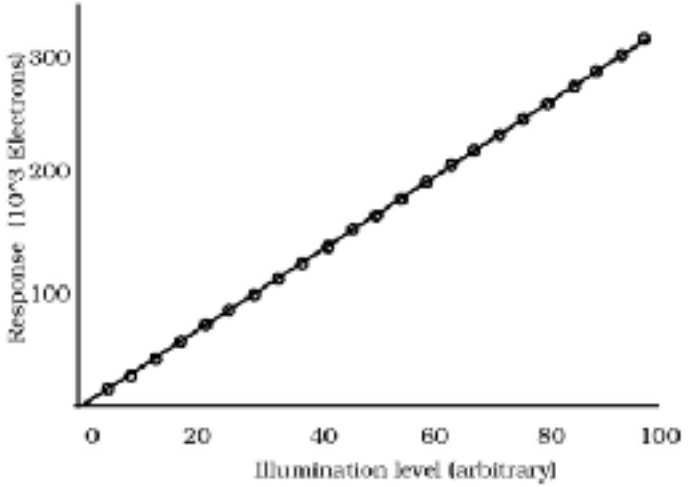

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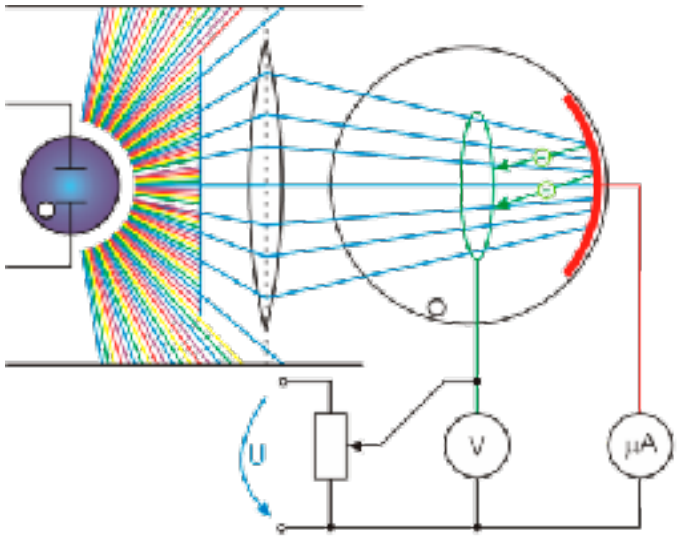

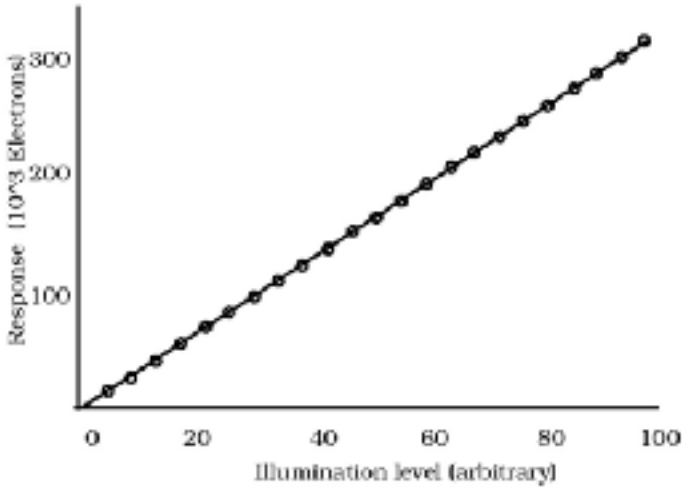

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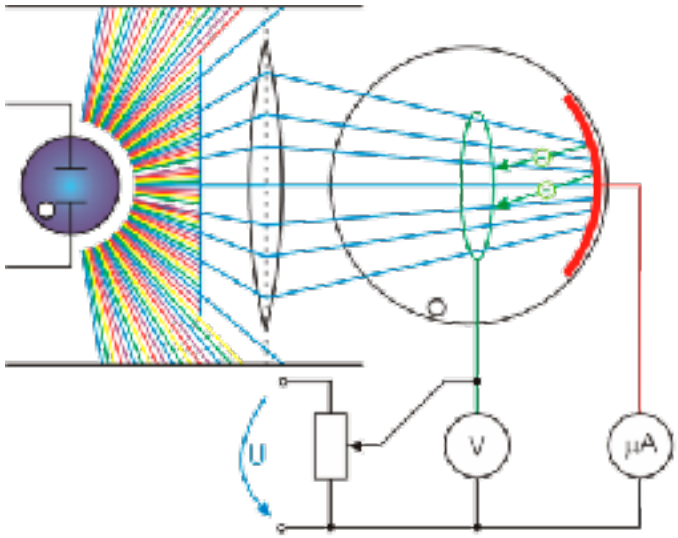

Binary Notation

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Decimal	Binary
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
10	1010
11	1011
12	1100
13	1101
14	1110
15	1111

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```
  0110
+ 0101
------
  1011
```

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```
  0110
+ 0101
------
  1011
```

```
  6
+ 5
---
 11
```

Decimal	Binary
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
10	1010
11	1011
12	1100
13	1101
14	1110
15	1111

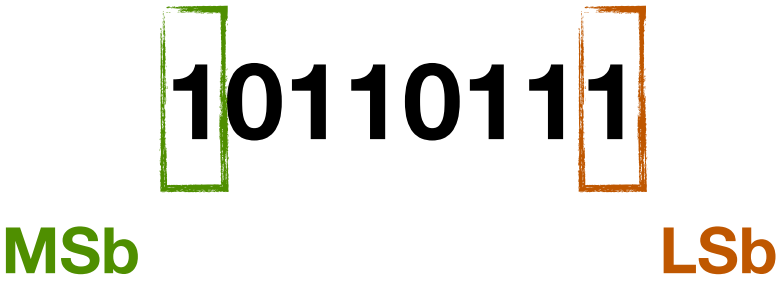
Hexdecimal (Hex) Notation

- Base 16 Number Representation
  - Use characters ‘0’ to ‘9’ and ‘A’ to ‘F’
  - Four bits per Hex digit
  - $11111110_2 = FE_{16}$
- Write $FA1D37B_{16}$ in C as
  - 0xFA1D37B
  - 0xfa1d37b

Hex	Decimal	Binary
0	0	0000
1	1	0001
2	2	0010
3	3	0011
4	4	0100
5	5	0101
6	6	0110
7	7	0111
8	8	1000
9	9	1001
A	10	1010
B	11	1011
C	12	1100
D	13	1101
E	14	1110
F	15	1111

Bit, Byte, Word

- Byte = 8 bits
  - Binary 00000000_2 to $11111111_{2;}$ Decimal: 0_{10} to 255_{10}; Hex: 00_{16} to FF_{16}
  - Least Significant Bit (LSb) vs. Most Significant Bit (MSb)



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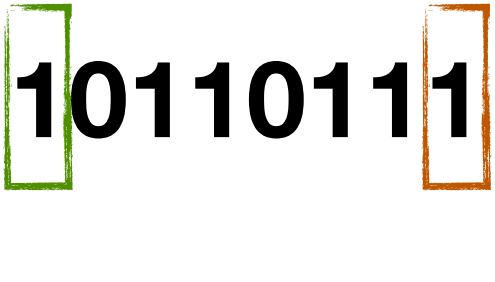


- Word = 4 Bytes (32-bit machine) / 8 Bytes (64-bit machine)
  - Least Significant Byte (LSB) vs. Most Significant Byte (MSB)

Today: Representing Information in Binary

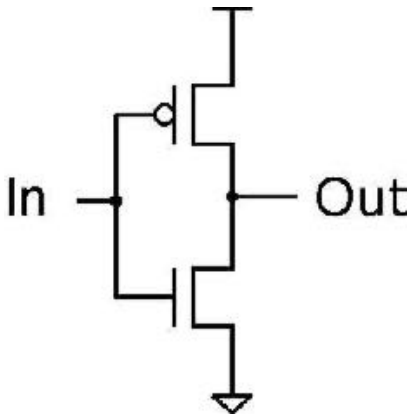
- Why Binary (bits)?
- Bit-level manipulations
- Integers
  - Representation: unsigned and signed
  - Conversion, casting
  - Expanding, truncating
  - Addition, negation, multiplication, shifting
  - Summary
- Representations in memory, pointers, strings

Bit-level manipulations

Not

- **~A = 1 when A=0**

~	
0	1
1	0



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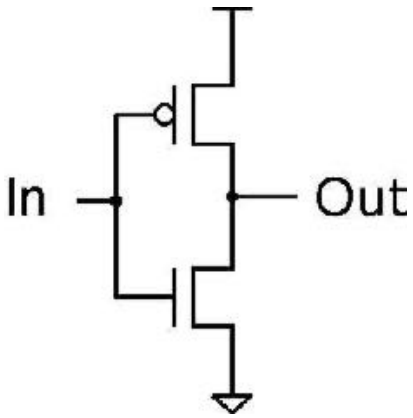

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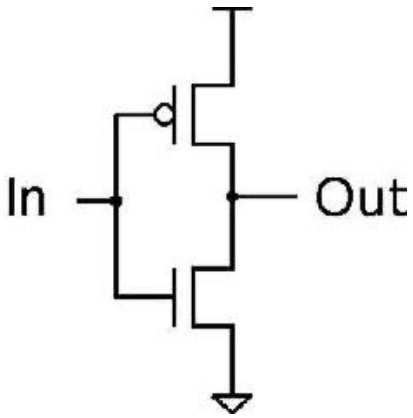

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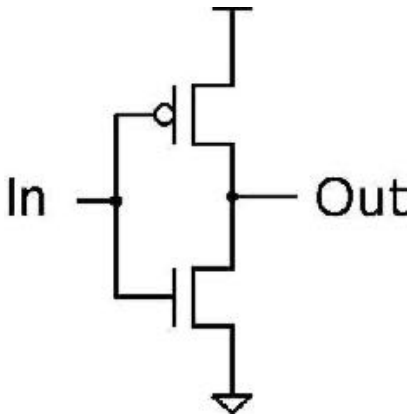

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Exclusive-Or (Xor)

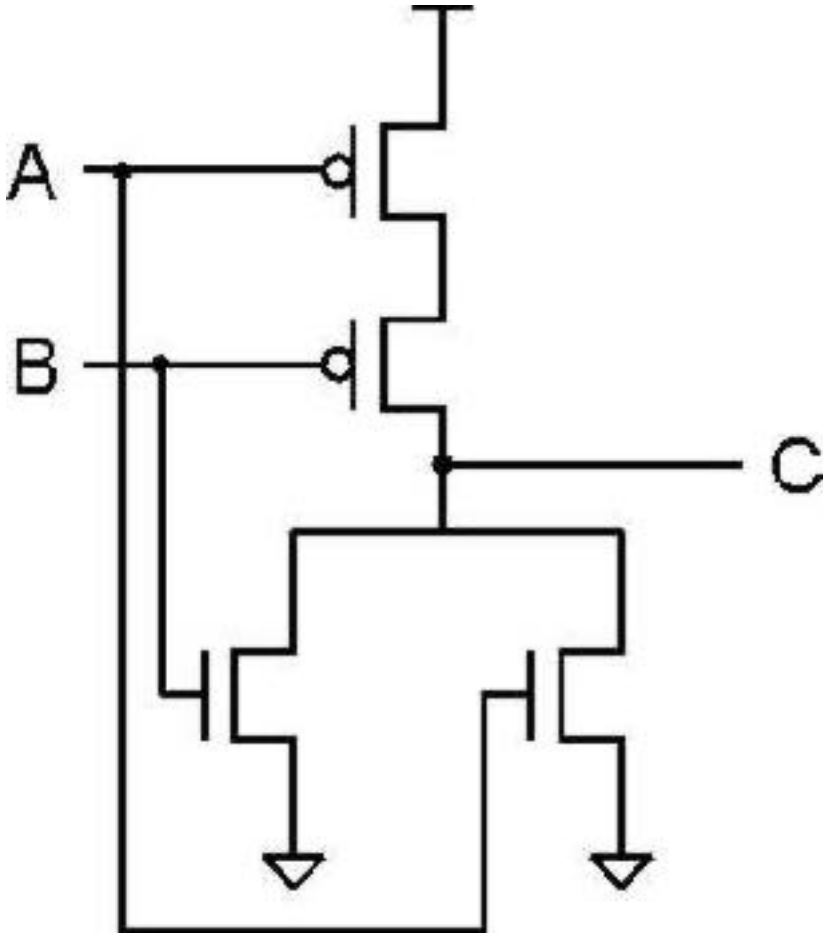
- A^B = 1 when either A=1 or B=1, but not both

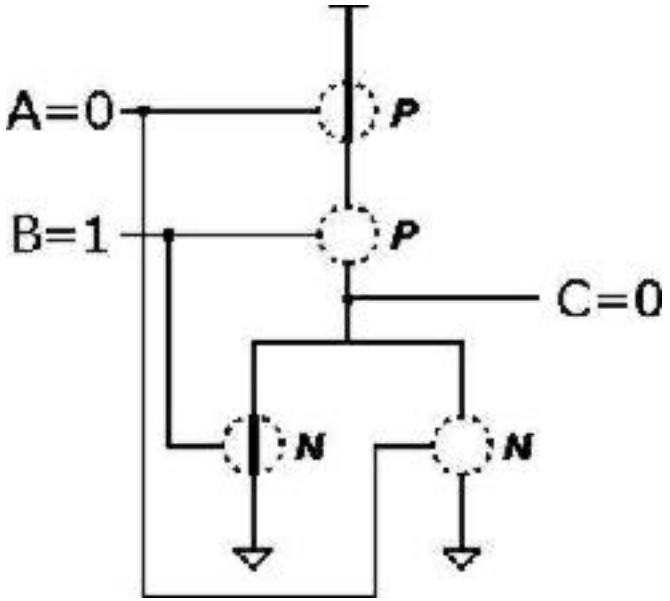
^	0	1
0	0	1
1	1	0

NOR (OR + NOT)

A	B	C
0	0	1
0	1	0
1	0	0
1	1	0

NOR (OR + NOT)





A	B	C
0	0	1
0	1	0
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Bit Vector Operations

- Operate on Bit Vectors
  - Operations applied bitwise

```
  01101001     01101001     01101001
& 01010101   | 01010101   ^ 01010101   ~ 01010101
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```

Bit-Level Operations in C

- Operations &, |, ~, ^ Available in C
  - Apply to any "integral" data type
    - `long, int, short, char, unsigned`
  - View arguments as bit vectors
  - Arguments applied bit-wise
- Examples (Char data type)
  - `~0x41` → `0xBE`
    - $\sim 01000001_2 \rightarrow 10111110_2$
  - `~0x00` → `0xFF`
    - $\sim 00000000_2 \rightarrow 11111111_2$
  - `0x69 & 0x55` → `0x41`
    - $01101001_2 \;\&\; 01010101_2 \rightarrow 01000001_2$
  - `0x69 | 0x55` → `0x7D`
    - $01101001_2 \mid 01010101_2 \rightarrow 01111101_2$

Aside: Logic Operations in C

- Contrast to Logical Operators
  - `&&, ||, !`
    - View 0 as “False”
    - Anything nonzero as “True”
    - Always return 0 or 1
    - Early termination (e.g., 0 && 1 && 1)
- Examples (char data type)
  - `!0x41 → 0x00`
  - `!0x00 → 0x01`
  - `!!0x41 → 0x01`
  - `0x69 && 0x55 → 0x01`
  - `0x69 || 0x55 → 0x01`
  - `p && *p` (avoids null pointer access)

Shift Operations

- Left Shift: `x << y`
  - Shift bit-vector **x** left **y** positions
    - Throw away extra bits on left
  - Fill with 0's on right
- Right Shift: `x >> y`
  - Shift bit-vector **x** right **y** positions
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  - Logical shift
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  - Arithmetic shift
    - Replicate most significant bit on left
- Undefined Behavior
  - Shift amount < 0 or ≥ total amount of bits

Argument x	01100010
<< 3	
Log. >> 2	
Arith. >> 2	

Argument x	10100010
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Argument x	10100010
<< 3	
Log. >> 2	
Arith. >> 2	

Shift Operations

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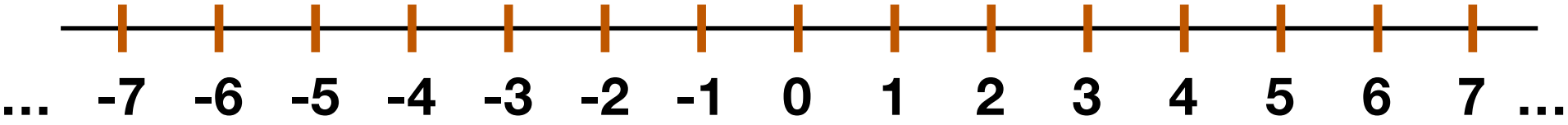
Log. >> 2	*00*101000
Arith. >> 2	*11*101000

Today: Representing Information in Binary

- Why Binary (bits)?
- Bit-level manipulations
- Integers
  - Representation: unsigned and signed
  - Conversion, casting
  - Expanding, truncating
  - Addition, negation, multiplication, shifting
  - Summary
- Representations in memory, pointers, strings

Representing Numbers in Binary

- Different types of number
  - Integer (Negative and Non-negative)
  - Fractions
  - Irrationals



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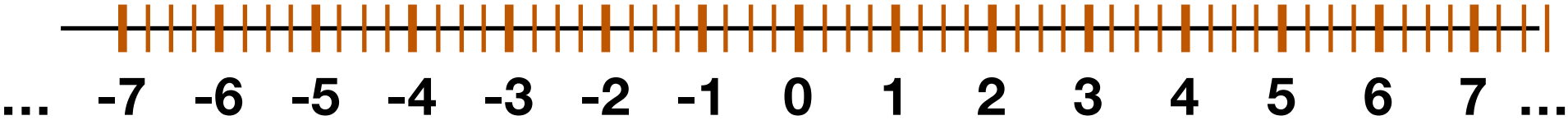

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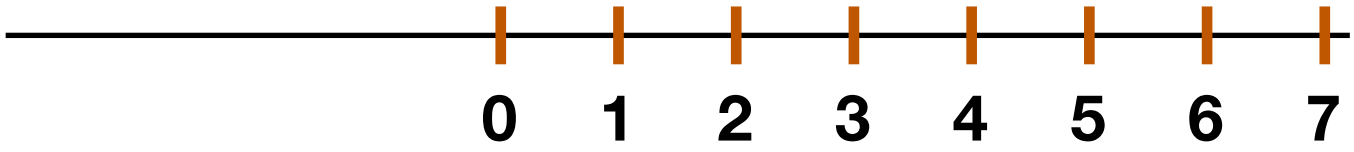

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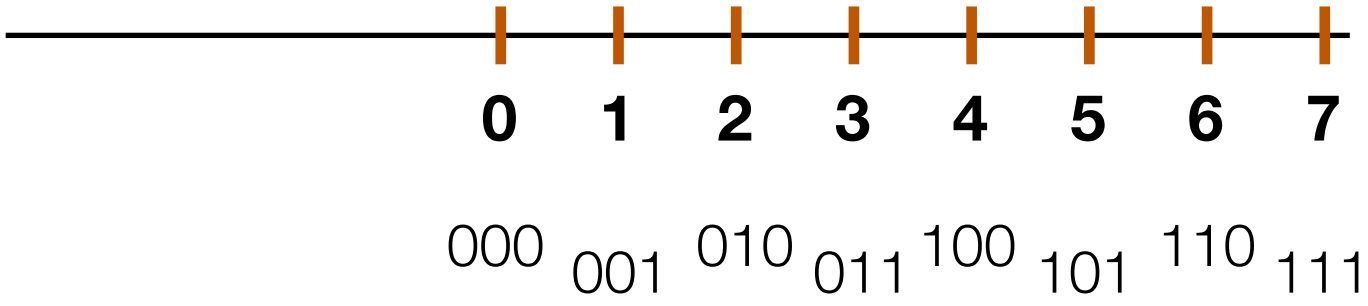

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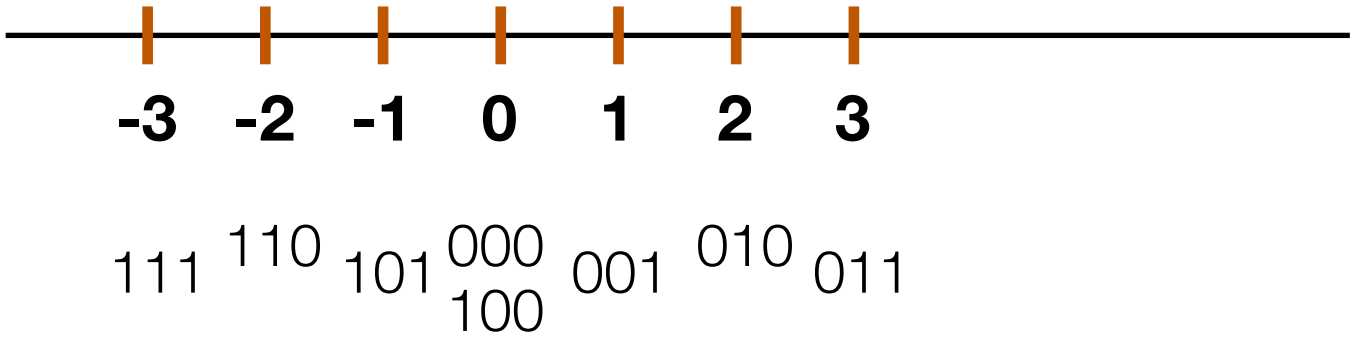

Sign-Magnitude Implications

- Bits have different semantics
  - Two zeros…
  - Normal arithmetic doesn't work
  - Make hardware design harder

Signed Value	Binary
0	000
1	001
2	010
3	011
-0	100
-1	101
-2	110
-3	111

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-----
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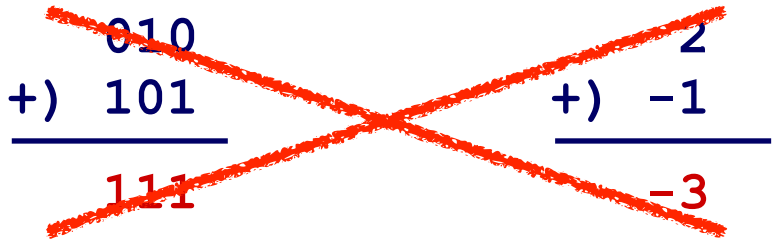

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