# CSC 252: Computer Organization Spring 2025: Lecture 4

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#### **Announcement**

- Programming Assignment 1 is out
  - Details: <a href="https://cs.rochester.edu/courses/252/spring2025/">https://cs.rochester.edu/courses/252/spring2025/</a>
    labs/assignment1.html
  - Due on Feb. 12, 11:59 PM
  - You have 3 slip days

#### **Announcement**

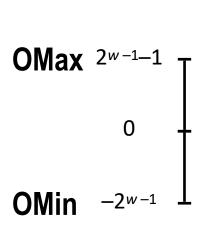
- Programming assignment 1 is in C language. Seek help from TAs.
- TAs are best positioned to answer your questions about programming assignments!!!
- Programming assignments do NOT repeat the lecture materials. They ask you to synthesize what you have learned from the lectures and work out something new.

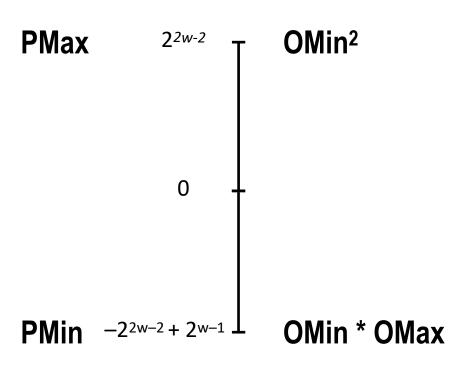
#### Multiplication

- Goal: Computing Product of w-bit numbers x, y
- Exact results can be bigger than w bits
  - Up to 2w bits (both signed and unsigned)

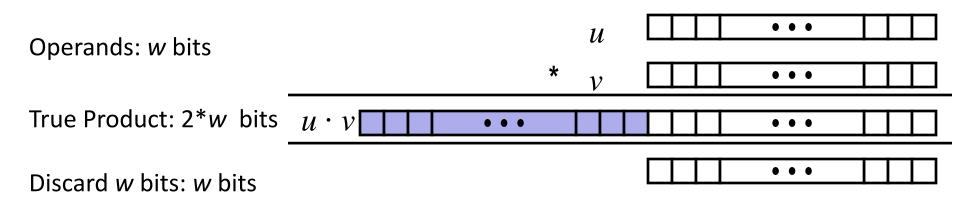
**Original Number (w bits)** 

**Product (2w bits)** 





## Unsigned Multiplication in C



- Standard Multiplication Function
  - Ignores high order w bits
- Effectively Implements the following:

$$UMult_{w}(u, v) = u \cdot v \mod 2^{w}$$

#### **Today: Floating Point**

- Background: Fractional binary numbers and fixed-point
- Floating point representation
- IEEE 754 standard
- Rounding, addition, multiplication
- Floating point in C
- Summary

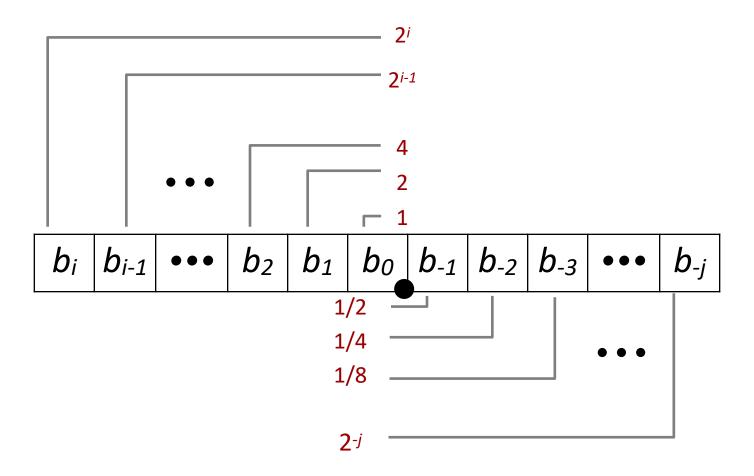
#### Recall: Represent Fractions in Binary

- What does 10.01<sub>2</sub> mean?
  - C.f., Decimal

$$12.45 = 1*10^{1} + 2*10^{0} + 4*10^{-1} + 5*10^{-2}$$

$$10.012 = 1*21 + 0*20 + 0*2-1 + 1*2-2$$
$$= 2.2510$$

#### **Fractional Binary Numbers**



#### **Fixed-Point Representation**

- Binary point stays fixed
- Fixed interval between representable numbers
  - The interval in this example is 0.25<sub>10</sub>



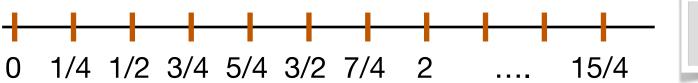
- Still need to remember the binary point, but just once for all numbers, which is implicit given the data type
- Usual arithmetics still work
  - No need to align (already aligned)

Decimal	<b>Binary</b>
0	00.00
0.25	00.01
0.5	00.10
0.75	00.11
1	01.00
1.25	01.01
1.5	01.10
1.75	01.11
2	10.00
2.25	10.01
2.5	10.10
2.75	10.11
3	11.00
3.25	11.01
3.5	11.10
3.75	11.11

## Limitations of Fixed-Point (#1)

- Can exactly represent numbers only of the form x/2<sup>k</sup>
  - Other rational numbers have repeating bit representations

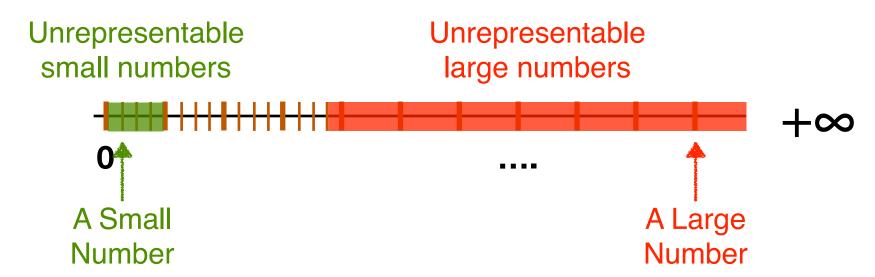
<b>Decimal Value</b>	Binary Representation
1/3	0.0101010101[01]
1/5	0.001100110011[0011]
1/10	0.0001100110011[0011]





## Limitations of Fixed-Point (#2)

- Can't represent very small and very large numbers at the same time
  - To represent very large numbers, the (fixed) interval needs to be large, making it hard to represent small numbers
  - To represent very small numbers, the (fixed) interval needs to be small, making it hard to represent large numbers



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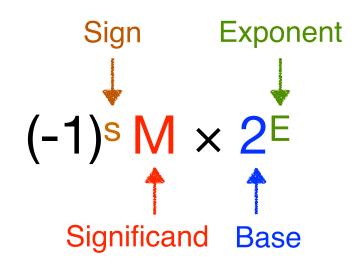
#### Primer: (Normalized) Scientific Notation

- In decimal: M × 10<sup>E</sup>
  - E is an integer
  - Normalized form: 1<= |M| < 10

Decimal Value	Scientific Notation
2	2×10 <sup>0</sup>
-4,321.768	-4.321768×10 <sup>3</sup>
0.000 000 007 51	7.51×10 <sup>-9</sup>

# Primer: (Normalized) Scientific Notation

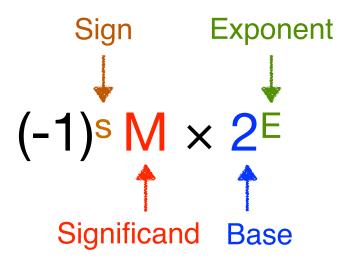
- In binary: (-1)<sup>s</sup> M 2<sup>E</sup>
- Normalized form:
  - 1 <= M < 2
  - $M = 1.b_0b_1b_2b_3...$ Fraction



Binary Value	Scientific Notation
1110110110110	(-1) <sup>0</sup> 1.110110110110 x 2 <sup>12</sup>
-101.11	$(-1)^1$ 1.0111 x 2 <sup>2</sup>
0.00101	(-1) <sup>0</sup> 1.01 x 2 <sup>-3</sup>

# Primer: (Normalized) Scientific Notation

- In binary: (-1)<sup>s</sup> M 2<sup>E</sup>
- Normalized form:
  - 1 <= M < 2
  - $M = 1.b_0b_1b_2b_3...$  Fraction

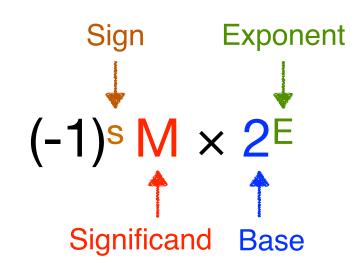


- If I tell you that there is a number where:
  - Fraction = 0101
  - s = 1
  - E = 10
  - You could reconstruct the number as (-1)<sup>1</sup>1.0101x2<sup>10</sup>

# Primer: Floating Point Representation

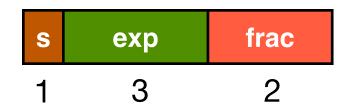
- In binary: (-1)<sup>s</sup> M 2<sup>E</sup>
- Normalized form:
  - 1 <= M < 2
  - $M = 1.b_0b_1b_2b_3...$
- Encoding
  - MSB s is sign bit s
  - exp field encodes Exponent (but not exactly the same, more later)
  - frac field encodes Fraction (but not exactly the same, more later)



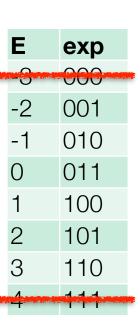


#### 6-bit Floating Point Example

$$v = (-1)^s M 2^E$$

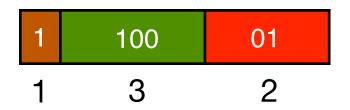


- exp has 3 bits, interpreted as an unsigned value
  - If exp were E, we could represent exponents from 0 to 7
  - How about negative exponent?
  - Subtract a bias term:  $E = \exp bias$  (i.e.,  $\exp = E + bias$ )
  - bias is always 2<sup>k-1</sup> 1, where k is number of exponent bits
- Example when we use 3 bits for exp (i.e., k = 3):
  - bias = 3
  - If E = -2, exp is 1 (001<sub>2</sub>)
  - Reserve 000 and 111 for other purposes (more on this later)
  - We can now represent exponents from -2 (exp 001) to 3 (exp 110)

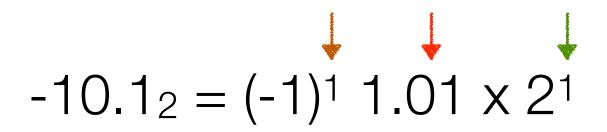


## 6-bit Floating Point Example

$$v = (-1)^s M 2^E$$

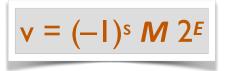


- frac has 2 bits, append them after "1." to form M
  - *frac* = 10 implies M = 1.10
- Putting it Together: An Example:



E	ехр
	000
-2	001
-1	010
0	011
1	100
2	101
3	110
<u> </u>	444

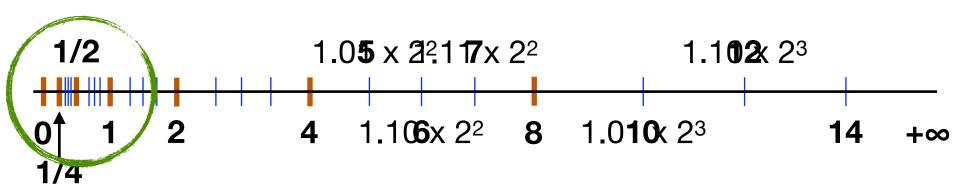
## Representable Numbers (Positive Only)



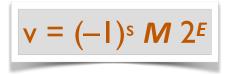


E	exp	E	exp
	000	1	100
-2	001	2	101
-1	010	3	110
0	011	<u> </u>	- Historia

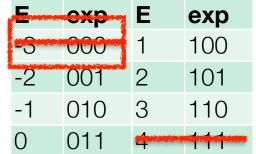
- Uneven interval (c.f., fixed interval in fixed-point)
  - More dense toward 0, sparser toward infinite
  - Allow encoding small and large numbers at the same time



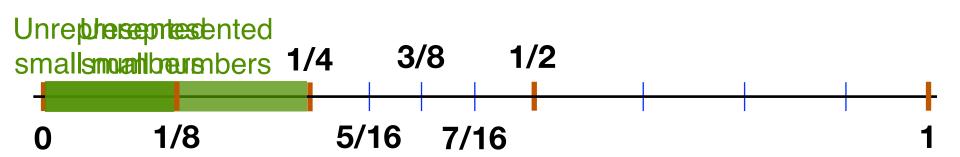
## Representable Numbers (Positive Only)







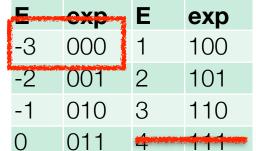
- Always round to 0 is inelegant
- Using 000 for exp doesn't solve it either



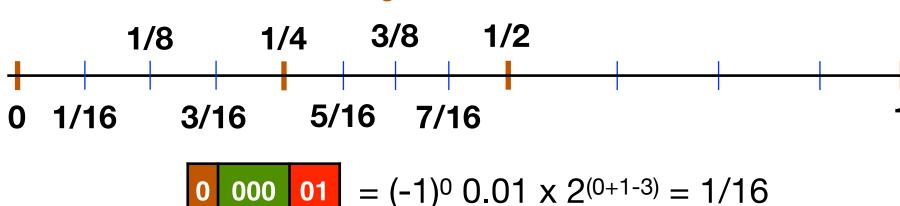
#### Subnormal (De-normalized) Numbers

$$v = (-1)^s M 2^E$$





- Idea: Evenly divide between 0 and 1/4 rather than exponentially decreasing when exp = 0 (subnormal/denormalized numbers)
- E = (exp + 1) bias (instead of exp bias)
- M = 0.frac (instead of 1.frac)
- Subnormal numbers allow graceful underflow



#### **Special Values**

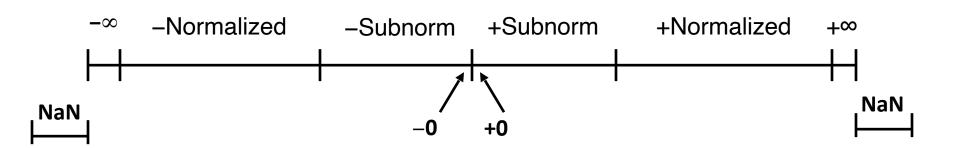
$$v = (-1)^s M 2^E$$



E	exp	E	ехр
-2	000	1	100
-2	001	2	101
-1	010	3	110
0	011		

- There are many special values in scientific computing
  - +/- ∞, Not-a-Numbers (NaNs) (e.g., 0 / 0, 0 / ∞, ∞ / ∞, sqrt(-1), ∞ ∞, ∞ x 0, etc.)
- exp = 111 is reserved to represent these numbers
- exp = 111, frac = 00
  - +/-  $\infty$  (depending on the s bit). Overflow results.
  - Arithmetic on  $\infty$  is exact:  $1.0/0.0 = -1.0/-0.0 = +\infty$ ,  $1.0/-0.0 = -\infty$
- exp = 111, frac != 00
  - Represent NaNs

#### Visualization: Floating Point Encodings



#### Infinite Amount of Real Numbers



Finite Amount of Floating Point Numbers

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#### **IEEE 754 Floating Point Standard**

• Single precision: 32 bits



• Double precision: 64 bits



#### **IEEE Floating Point**

#### IEEE Standard 754

- Established in 1985 as uniform standard for floating point arithmetic
  - Before that, many idiosyncratic formats
- Supported by all major CPUs (and even GPUs and other processors)

#### Driven by numerical concerns

- Nice standards for rounding, overflow, underflow
- Hard to make fast in hardware
  - Numerical analysts predominated over hardware designers in defining standard

## Single Precision (32-bit) Example

$$v = (-1)^s M 2^E$$

bias = 
$$2^{(8-1)}-1 = 127$$

0	10001100	1101101101101000000000
1	8-bit	23-bit

$$15213_{10} = 11101101101101_2$$
$$= (-1)^0 1.1101101101101_2 \times 2^{13}$$

$$exp = E + bias = 140_{10}$$