#### Threading Building Block (TBB) by Intel

- C++ library
  - Represents higher-level task-based parallelism that abstracts platform details and threading from the user
  - Doesn't require special language/compiler
  - Can be used on any processor with any OS
- Task scheduler implements task stealing for dynamic workload balancing
- Implements common parallel performance pattern such as parallel loops, flow graphs etc.

#### TBB

- Implements concurrent containers scalable alternative to serial data containers such as C++ STL containers
- Provides a comprehensive set of synchronization primitives – mutexes, condition variables, atomic operations
- Provides the ability to specify priorities for tasks/ task groups
- Implements parallel data structures such as concurrent priority queues, concurrent unordered sets, etc.

# Grand Central Dispatch (GCD) by Apple



- Also implements task queues using a thread pool paradigm
  - Uses threads at the lower level
- Applications create tasks that can be expressed either as a function or as a "block" (akin to a closure)
- Mac OS X, Apache HTTP Server

#### GCD

- Application framework
  - Dispatch queues queue of tasks; concurrent or serial. Queues with different priority levels are created by the library that can execute tasks concurrently
  - Dispatch source allow client to register blocks/functions to execute asynchronously on system events or POSIX signal
  - Dispatch groups allow several tasks to be grouped for later joining
  - Dispatch semaphores allow a client to permit a certain number of tasks to execute concurrently

## Lithe: Composing Parallel Software Efficiently

Pan, Hindman, Asanovic MIT/Berkeley PLDI 2010

### Software Trends

- Parallel applications using parallel libraries in turn using other parallel libraries
- E.g.
  - SuiteSparseQR
    - Incorporated into MATLAB
    - Uses Intel's TBB
    - Calls BLAS libraries
    - Which use OpenMP
- State of the art
  - User required to make decisions on degree of parallelism at each level
  - Thread virtualization implies user has no control over influencing resource allocation

#### **Possible Solutions**

- Require all parallelism to be expressed using a universal high-level abstraction
- Provide a low-level substrate for parallel libraries to interface with

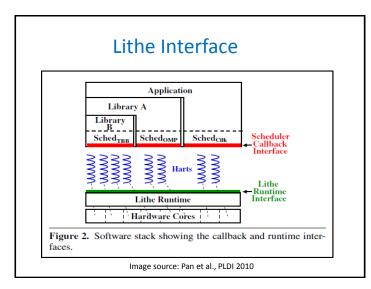
#### Lithe

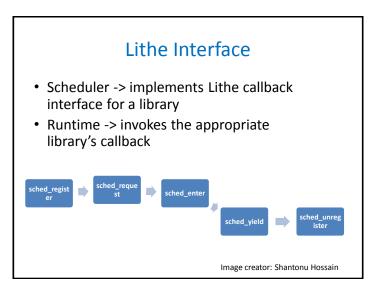
- Cooperative hierarchical resource management
  - Hardware threads
  - Hierarchy of schedulers
  - Cooperative request/release/allocation of threads up and down the hierarchy

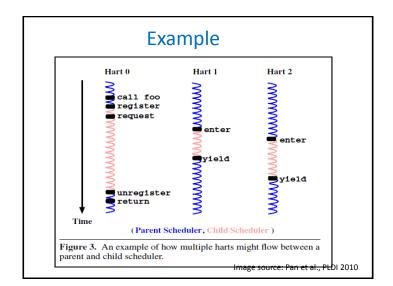
### Lithe Implementation

- Harts: hardware threads, one per physical core/hardware context
- Execution contexts

#### **Conventional System Vs Lithe** Application Application Library A Library B Library C Library A Library B Library C 3 ₹ 05 Harts Threads Core 0 Core 1 Core 2 Core 0 Core 1 Core 2 Core 3 Core 3 Hardware Hardwar (b) (a) Figure 1. In conventional systems (a), each library is only aware of its own set of virtualized threads, which the operating system multiplexes onto available physical processors. Lithe, (b), provides unvirtualized processing resources, or harts, which are shared cooperatively by the application libraries, preventing resource oversubscription. Image source: Pan et al., PLDI 2010







Call	backs
le 1. The Lithe runtime f	unctions and their corresponding
 Lithe Runtime Interface	Scheduler Callback Interface
sched_register(sched)	register(child)
sched_unregister()	unregister(child)
sched_request(nharts)	request(child, nharts)
sched_enter(child)	enter()
sched_yield()	yield(child)
sched_reenter()	enter()
ctx_init(ctx, stack)	N/A
ctx_fini(ctx)	N/A
ctx_run(ctx, fn)	N/A
ctx_pause(fn)	N/A
ctx_resume(ctx)	N/A
ctx_block(ctx)	block(ctx)
ctx_unblock(ctx)	unblock(ctx)

